



<b>SURFACE VEHICLE RECOMMENDED PRACTICE</b>	<b>J1699®-2</b>	<b>DEC2024</b>
	Issued 1998-01 Revised 2017-09 Stabilized 2024-12	
Superseding J1699-2 SEP2017		
Test Cases for OBD-II Scan Tools and I/M Test Equipment		

RATIONALE

The document definitions are not applicable to current technology.

STABILIZED NOTICE

This document has been declared "STABILIZED" by SAE Vehicle E E System Diagnostic Standards Committee and will no longer be subjected to periodic reviews for currency. Users are responsible for verifying references and continued suitability of technical requirements. Newer technology may exist.

SAENORM.COM : Click to view the full PDF of J1699-2\_202412

SAE Executive Standards Committee Rules provide that: "This report is published by SAE to advance the state of technical and engineering sciences. The use of this report is entirely voluntary, and its applicability and suitability for any particular use, including any patent infringement arising therefrom, is the sole responsibility of the user."

SAE reviews each technical report at least every five years at which time it may be revised, reaffirmed, stabilized, or cancelled. SAE invites your written comments and suggestions.

Copyright © 2024 SAE International

All rights reserved. No part of this publication may be reproduced, stored in a retrieval system, transmitted, in any form or by any means, electronic, mechanical, photocopying, recording, or otherwise, or used for text and data mining, AI training, or similar technologies, without the prior written permission of SAE.

**TO PLACE A DOCUMENT ORDER:** Tel: 877-606-7323 (inside USA and Canada)  
Tel: +1 724-776-4970 (outside USA)  
Fax: 724-776-0790  
Email: CustomerService@sae.org  
http://www.sae.org

SAE WEB ADDRESS:

**For more information on this standard, visit**  
[https://www.sae.org/standards/content/J1699/2\\_202412/](https://www.sae.org/standards/content/J1699/2_202412/)

## TABLE OF CONTENTS

1.	SCOPE.....	7
1.1	Purpose.....	7
2.	REFERENCES.....	7
2.1	Applicable Documents.....	7
2.1.1	SAE Publications.....	7
2.1.2	ISO Publications.....	7
2.2	Related Publications.....	8
2.2.1	SAE Publications.....	8
2.2.2	ISO Publications.....	8
2.2.3	Other Publications.....	8
3.	DEFINITIONS.....	8
3.1	Definition of Terms.....	8
3.2	Acronyms.....	9
4.	OVERVIEW.....	9
4.1	Documentation Conventions.....	10
5.	GENERAL TEST SET-UP.....	11
5.1	DUT Requirements.....	11
5.2	Test Equipment Requirements.....	11
5.3	Typical Test Set-Up.....	12
5.4	Pre-Defined Messages, Message Timing, and Test Set-Up States.....	12
5.4.1	General States.....	12
5.4.2	CAN.....	12
5.4.3	ISO 15765-4.....	14
5.4.4	ISO 14230-4.....	29
5.4.5	ISO 9141-2.....	42
5.4.6	SAE J1850 VPW.....	54
5.4.7	SAE J1850 PWM.....	65

SAENORM.COM : Click to view the full PDF of J1699\_2\_202412

6.	MECHANICAL EVALUATION.....	76
6.1	General Tests.....	76
6.1.1	Verify Mechanical Dimensions for SAE J1962 Connector Head .....	76
6.1.2	Verify SAE J1962 Cable Length .....	76
7.	ELECTRICAL EVALUATION .....	77
7.1	General Tests.....	77
7.1.1	Verify the Current Draw of DUT .....	77
7.2	ISO 15765-4 Tests .....	78
7.2.1	Verify Network Termination.....	78
7.2.2	Verify Capacitance .....	79
7.3	ISO 14230-4 Tests .....	79
7.3.1	Verify Network Termination.....	79
7.3.2	Verify Capacitance .....	80
7.4	ISO 9141-2 Tests .....	80
7.4.1	Verify Network Termination.....	80
7.4.2	Verify Capacitance .....	81
7.5	SAE J1850 VPW Tests .....	81
7.6	SAE J1850 PWM Tests.....	81
8.	PROTOCOL EVALUATION .....	81
8.1	General Tests.....	81
8.1.1	Verify OBD II Initialization Sequence .....	82
8.2	ISO 15765-4 Tests .....	83
8.2.1	Test BS and STMIN are 0 (11-bit, 500K).....	83
8.2.2	Verify Protocol Initialization (11-bit, 500K).....	84
8.2.3	Verify Protocol Initialization (29-bit, 500K).....	84
8.2.4	Verify Protocol Initialization with 8 ECUs (11-bit, 500K).....	85
8.2.5	Verify Protocol Initialization with 8 ECUs (29-bit, 500K).....	86
8.2.6	Test Protocol Initialization with No Acknowledgement (11-bit, 500K) .....	87
8.2.7	Verify Protocol Initialization with Delayed Acknowledgement (11-bit, 500K).....	88
8.2.8	Test Protocol Initialization with Error Frame Detected (11-bit, 500K).....	88
8.2.9	Verify Protocol Initialization with NRC=\$21 (11-bit, 500K) .....	89
8.3	ISO 14230-4 Tests .....	90
8.3.1	Verify Bit Times for 10400 Baud Communications .....	90
8.3.2	Verify 5-Baud Initialization with Key Bytes \$8FE9 (2 ECUs) .....	91
8.3.3	Verify 5-Baud Initialization with Key Bytes \$8F6B (2 ECUs) .....	92
8.3.4	Verify 5-Baud Initialization with Key Bytes \$8F6D (2 ECUs).....	93
8.3.5	Verify 5-Baud Initialization with Key Bytes \$8FEF (2 ECUs).....	94
8.3.6	Verify Fast Initialization with Key Bytes \$8FE9 (2 ECUs).....	95
8.3.7	Verify Fast Initialization with Key Bytes \$8F6B (2 ECUs).....	96
8.3.8	Verify Fast Initialization with Key Bytes \$8F6D (2 ECUs).....	97
8.3.9	Verify Fast Initialization with Key Bytes \$8FEF (2 ECUs).....	98
8.3.10	Verify Fast Initialization with Key Bytes \$8FEF (8 ECUs).....	99
8.3.11	Verify Fast Initialization with No OBD II Key Bytes (1 ECU).....	100
8.3.12	Verify Fast Initialization with Non-OBD II Key Bytes \$0000 (1 ECU) .....	101
8.4	ISO 9141-2 Tests .....	101
8.4.1	Verify Bit Times for 10400 Baud Communications .....	101
8.4.2	Verify 5-Baud Initialization with Key Bytes \$0808 (2 ECUs).....	102
8.4.3	Verify 5-Baud Initialization with Key Bytes \$0808 (8 ECUs).....	103
8.4.4	Verify 5-Baud Initialization with Key Bytes \$9494 (2 ECUs).....	104
8.4.5	Verify Second Attempt at 5-Baud Initialization with Key Bytes \$9494 (2 ECUs).....	105
8.4.6	Verify 5-Baud Initialization when SID \$01 PID \$00 First Response is Corrupted (2 ECUs).....	106
8.5	SAE J1850 VPW Tests .....	107
8.5.1	Verify Protocol Initialization (2 ECUs).....	107
8.5.2	Verify Protocol Initialization (8 ECUs).....	108
8.6	SAE J1850 PWM Tests.....	108
8.6.1	Verify Protocol Initialization (2 ECUs).....	109
8.6.2	Verify Protocol Initialization (8 ECUs).....	109

9.	NETWORK EVALUATION .....	110
9.1	General Tests.....	110
9.2	ISO 15765-4 Tests .....	110
9.2.1	Verify a SID \$01 PID Supported PID Requests (11-bit, 500K; 1 ECU) .....	110
9.2.2	Verify a SID \$01 PID Supported PID Requests (29-bit, 500K; 1 ECU) .....	111
9.2.3	Verify a SID \$01 PID Supported PID Requests - Group (11-bit, 500K; 1 ECU).....	112
9.2.4	Verify a SID \$01 PID Supported PID Requests - Group (29-bit, 500K; 1 ECU).....	112
9.2.5	Verify a SID \$01 PID Requests (11-bit, 500K; 1 ECU).....	113
9.2.6	Verify a SID \$01 PID Requests (29-bit, 500K; 1 ECU).....	114
9.2.7	Verify a SID \$01 PID Requests - Group Req. (11-bit, 500K; 1 ECU).....	115
9.2.8	Verify a SID \$01 PID Requests - Group Req. (29-bit, 500K; 1 ECU).....	116
9.2.9	Verify a SID \$01 PID \$01 Request for Spark Ignition (11-bit, 500K; 2 ECUs w/ Order Swap).....	117
9.2.10	Verify a SID \$01 PID \$01 Request for Spark Ignition (29-bit, 500K; 2 ECUs w/ Order Swap).....	118
9.2.11	Verify a SID \$01 PID \$01 Request for Compression Ignition (11-bit, 500K; 2 ECUs w/ Order Swap).....	119
9.2.12	Verify a SID \$01 PID \$01 Request for Compression Ignition (29-bit, 500K; 2 ECUs w/ Order Swap).....	120
9.2.13	Verify a SID \$01 PID \$0C Request (11-bit, 500K; 2 ECUs w/ Order Swap) .....	121
9.2.14	Verify a SID \$01 PID \$0C Request (29-bit, 500K; 2 ECUs w/ Order Swap) .....	121
9.2.15	Verify a SID \$02 PID Freeze Frame DTC Request with no DTCs (11-bit, 500K; 2 ECUs w/ Order Swap) .....	122
9.2.16	Verify a SID \$02 PID Freeze Frame DTC Request with No DTCs (29-bit, 500K; 2 ECUs w/ Order Swap) .....	122
9.2.17	Verify a SID \$02 PID Freeze Frame DTC Request with DTCs (11-bit, 500K; 2 ECUs).....	123
9.2.18	Verify a SID \$02 PID Freeze Frame DTC Request with DTCs (29-bit, 500K; 2 ECUs).....	123
9.2.19	Verify a SID \$03 Confirmed DTC Request with No DTCs (11-bit, 500K; 2 ECUs w/ Order Swap).....	124
9.2.20	Verify a SID \$03 Confirmed DTC Request with No DTCs (29-bit, 500K; 2 ECUs w/ Order Swap).....	124
9.2.21	Verify a SID \$03 Confirmed DTC Request with DTCs (11-bit, 500K; 2 ECUs).....	125
9.2.22	Verify a SID \$03 Confirmed DTC Request with DTCs (29-bit, 500K; 2 ECUs).....	125
9.2.23	Verify a SID \$04 Clear Codes Request (11-bit, 500K; 2 ECUs w/ Order Swap).....	126
9.2.24	Verify a SID \$04 Clear Codes Request (29-bit, 500K; 2 ECUs w/ Order Swap).....	126
9.2.25	Test a SID \$04 Clear Codes Request with NRC=\$22 (11-bit, 500K; 2 ECUs).....	127
9.2.26	Test a SID \$04 Clear Codes Request with NRC=\$22 (29-bit, 500K; 2 ECUs).....	127
9.2.27	Verify a SID \$04 Clear Codes Request with NRC=\$78 (11-bit, 500K; 2 ECUs) .....	128
9.2.28	Verify a SID \$04 Clear Codes Request with NRC=\$78 (29-bit, 500K; 2 ECUs) .....	128
9.2.29	Verify a SID \$06 MID Supported MID Request (11-bit, 500K; 1 ECU).....	129
9.2.30	Verify a SID \$06 MID Supported MID Request (29-bit, 500K; 1 ECU).....	129
9.2.31	Verify a SID \$07 Pending DTC Request with No DTCs (11-bit, 500K; 2 ECUs w/ Order Swap).....	130
9.2.32	Verify a SID \$07 Pending DTC Request with No DTCs (29-bit, 500K; 2 ECUs w/ Order Swap).....	130
9.2.33	Verify a SID \$07 Pending DTC Request with DTCs (11-bit, 500K; 2 ECUS).....	131
9.2.34	Verify a SID \$07 Pending DTC Request with DTCs (29-bit, 500K; 2 ECUs).....	131
9.2.35	Verify a SID \$08 TID Supported TID Request (11-bit, 500K; 1 ECU) .....	132
9.2.36	Verify a SID \$08 TID Supported TID Request (29-bit, 500K; 1 ECU) .....	132
9.2.37	Verify a SID \$09 INF Supported INF Request (11-bit, 500K; 2 ECUs w/ Order Swap).....	133
9.2.38	Verify a SID \$09 INF Supported INF Request Diesel (11-bit, 500K; 2 ECUs w/ Order Swap).....	133
9.2.39	Verify a SID \$09 INF Supported INF Request (29-bit, 500K; 2 ECUs w/ Order Swap).....	134
9.2.40	Verify a SID \$09 INF Supported INF Request Diesel (29-bit, 500K; 2 ECUs w/ Order Swap).....	134
9.2.41	Verify a SID \$09 INF Supported INF Request (11-bit, 500K; 8 ECUs w/ Order Swap).....	135
9.2.42	Verify a SID \$09 INF Supported INF Request Diesel (11-bit, 500K; 8 ECUs w/ Order Swap).....	136
9.2.43	Verify a SID \$09 INF Supported INF Request (29-bit, 500K; 8 ECUs w/ Order Swap).....	137
9.2.44	Verify a SID \$09 INF Supported INF Request Diesel (29-bit, 500K; 8 ECUs w/ Order Swap).....	138
9.2.45	Verify a SID \$09 VIN Request (11-bit, 500K; 1 ECU).....	139
9.2.46	Verify a SID \$09 VIN Request (29-bit, 500K; 1 ECU).....	139
9.2.47	Verify a SID \$09 VIN Request with NRC=\$78 (11-bit, 500K; 1 ECU) .....	140
9.2.48	Verify a SID \$09 VIN Request with NRC=\$78 (29-bit, 500K; 1 ECU) .....	140
9.2.49	Verify a SID \$09 CALID Request (11-bit, 500K; 2 ECUs w/ Order Swap) .....	141
9.2.50	Verify a SID \$09 CALID Request (29-bit, 500K; 2 ECUs w/ Order Swap) .....	141
9.2.51	Verify a SID \$09 CALID Request with 9 CALIDs (11-bit, 500K; 8 ECUs w/ Order Swap) .....	142
9.2.52	Verify a SID \$09 CALID Request with 9 CALIDs (29-bit, 500K; 8 ECUs w/ Order Swap) .....	143
9.2.53	Verify a SID \$09 CVN Request (11-bit, 500K; 2 ECU w/ Order Swap) .....	144
9.2.54	Verify a SID \$09 CVN Request (29-bit, 500K; 2 ECU w/ Order Swap) .....	144
9.2.55	Verify a SID \$09 CVN Request with 9 CVNs (11-bit, 500K; 8 ECUs w/ Order Swap) .....	145
9.2.56	Verify a SID \$09 CVN Request with 9 CVNs (29-bit, 500K; 8 ECUs w/ Order Swap) .....	146

9.2.57	Verify a SID \$09 IPT (INF\$08, 32 bytes) Request (11-bit, 500K; 2 ECUs w/ Order Swap) .....	147
9.2.58	Verify a SID \$09 IPT (INF\$08, 32 bytes) Request (29-bit, 500K; 2 ECUs w/ Order Swap) .....	148
9.2.59	Verify a SID \$09 IPT (INF\$08, 40 bytes) Request (11-bit, 500K; 2 ECUs w/ Order Swap) .....	149
9.2.60	Verify a SID \$09 IPT (INF\$08, 40 bytes) Request (29-bit, 500K; 2 ECUs w/ Order Swap) .....	150
9.2.61	Verify a SID \$09 ECU Name Request (11-bit, 500K; 8 ECUs w/ Order Swap).....	151
9.2.62	Verify a SID \$09 ECU Name Request (29-bit, 500K; 8 ECUs w/ Order Swap).....	152
9.2.63	Verify a SID \$09 IPT (INF\$0B, 32 bytes) Request (11-bit, 500K; 2 ECUs w/ Order Swap).....	153
9.2.64	Verify a SID \$09 IPT (INF\$0B, 32 bytes) Request (29-bit, 500K; 2 ECUs w/ Order Swap).....	154
9.2.65	Verify a SID \$09 IPT (INF\$0B, 36 bytes) Request (11-bit, 500K; 2 ECUs w/ Order Swap).....	155
9.2.66	Verify a SID \$09 IPT (INF\$0B, 36 bytes) Request (29-bit, 500K; 2 ECUs w/ Order Swap).....	156
9.2.67	Verify a SID \$09 ESN Request (11-bit, 500K; 8 ECUs).....	157
9.2.68	Verify a SID \$09 ESN Request (29-bit, 500K; 8 ECUs).....	158
9.2.69	Verify a SID \$09 EROTAN Request (11-bit, 500K; 8 ECUs w/ Order Swap).....	159
9.2.70	Verify a SID \$09 EROTAN Request (29-bit, 500K; 8 ECUs w/ Order Swap).....	160
9.2.71	Verify a SID \$0A Permanent DTC Request with No DTCs (11-bit, 500K; 2 ECUs w/ Order Swap) .....	161
9.2.72	Verify a SID \$0A Permanent DTC Request with No DTCs (29-bit, 500K; 2 ECUs w/ Order Swap) .....	161
9.2.73	Verify a SID \$0A Permanent DTC Request with DTCs (11-bit, 500K; 2 ECUs) .....	162
9.2.74	Verify a SID \$0A Permanent DTC Request with DTCs (29-bit, 500K; 2 ECUs).....	162
9.3	ISO 14230-4 Tests .....	163
9.3.1	Verify a SID \$01 PID Supported PIDs Requests (1 ECU).....	163
9.3.2	Verify a SID \$01 PID \$01 Request for Spark Ignition (2 ECUs w/ Order Swap) .....	164
9.3.3	Verify a SID \$01 PID \$01 Request for Compression Ignition (2 ECUs w/ Order Swap) .....	165
9.3.4	Verify a SID \$01 PID \$0C Request (2 ECUs w/ Order Swap).....	166
9.3.5	Verify a SID \$02 PID Freeze Frame DTC Request with No DTCs (2 ECUs w/ Order Swap).....	166
9.3.6	Verify a SID \$02 PID Freeze Frame DTC Request with DTCs (2 ECUs).....	167
9.3.7	Verify a SID \$03 Confirmed DTC Request with No DTCs (2 ECUs w/ Order Swap) .....	167
9.3.8	Verify a SID \$03 Confirmed DTC Request with DTCs (2 ECUs).....	168
9.3.9	Verify a SID \$04 Clear Codes Request (2 ECUs w/ Order Swap) .....	168
9.3.10	Test a SID \$04 Clear Codes Request with NRC=\$22 (2 ECUs) .....	169
9.3.11	Verify a SID \$04 Clear Codes Request with NRC=\$78 (2 ECUs).....	169
9.3.12	Verify a SID \$05 TID \$01 Request (1 ECU).....	170
9.3.13	Verify a SID \$06 MID Supported MID Request (1 ECU) .....	170
9.3.14	Verify a SID \$07 Pending DTC Request with No DTCs (2 ECUs w/ Order Swap).....	171
9.3.15	Verify a SID \$07 Pending DTC Request with DTCs (2 ECUs).....	171
9.3.16	Verify a SID \$08 TID Supported TID Request (1 ECU).....	172
9.3.17	Verify a SID \$09 INF Supported INF Request (2 ECUs w/ Order Swap) .....	172
9.3.18	Verify a SID \$09 INF Supported INF Request Diesel (2 ECUs w/ Order Swap) .....	173
9.3.19	Verify a SID \$09 INF Supported INF Request (8 ECUs w/ Order Swap) .....	173
9.3.20	Verify a SID \$09 VIN Request (1 ECU) .....	174
9.3.21	Verify a SID \$09 VIN Request with NRC=\$78 (1 ECU).....	175
9.3.22	Verify a SID \$09 CALID Request (2 ECUs w/ Order Swap) .....	176
9.3.23	Verify a SID \$09 CALID Request with 9 CALIDs from Each of Eight ECUs (8 ECUs w/ Order Swap)....	176
9.3.24	Verify a SID \$09 CVN Request (2 ECU).....	178
9.3.25	Verify a SID \$09 CVN Request with 9 CVNs (8 ECUs w/ Order Swap) .....	178
9.3.26	Verify a SID \$09 IPT (INF\$08, 32 bytes) Request (2 ECUs w/ Order Swap).....	179
9.3.27	Verify a SID \$09 IPT (INF\$08, 40 bytes) Request (2 ECUs w/ Order Swap).....	181
9.3.28	Verify a SID \$09 IPT (INF\$0B, 32 bytes) Request (2 ECUs w/ Order Swap).....	182
9.3.29	Verify a SID \$09 IPT (INF\$0B, 36 bytes) Request (2 ECUs w/ Order Swap).....	183
9.4	ISO 9141-2 Tests .....	184
9.4.1	Verify a SID \$01 PID Supported PIDs Requests (1 ECU).....	184
9.4.2	Verify a SID \$01 PID \$01 Request for Spark Ignition (2 ECUs w/ Order Swap) .....	185
9.4.3	Verify a SID \$01 PID \$01 Request for Compression Ignition (2 ECUs w/ Order Swap) .....	186
9.4.4	Verify a SID \$01 PID \$0C Request (2 ECUs w/ Order Swap).....	187
9.4.5	Verify a SID \$02 PID Freeze Frame DTC Request with No DTCs (2 ECUs w/ Order Swap).....	187
9.4.6	Verify a SID \$02 PID Freeze Frame DTC Request with DTCs (2 ECUs).....	188
9.4.7	Verify a SID \$03 Confirmed DTC Request with No DTCs (2 ECUs w/ Order Swap) .....	188
9.4.8	Verify a SID \$03 Confirmed DTC Request with DTCs (2 ECUs).....	189
9.4.9	Verify a SID \$04 Clear Codes Request (2 ECUs w/ Order Swap) .....	189
9.4.10	Test a SID \$04 Clear Codes Request with NRC=\$22 (2 ECUs) .....	190
9.4.11	Verify a SID \$04 Clear Codes Request with NRC=\$78 (2 ECUs).....	190
9.4.12	Verify a SID \$05 TID \$01 Request (1 ECU).....	191

9.4.13	Verify a SID \$06 MID Supported MID Request (1 ECU) .....	191
9.4.14	Verify a SID \$07 Pending DTC Request with No DTCs (2 ECUs w/ Order Swap).....	192
9.4.15	Verify a SID \$07 Pending DTC Request with DTCs (2 ECUs) .....	192
9.4.16	Verify a SID \$08 TID Supported TID Request (1 ECU) .....	193
9.4.17	Verify a SID \$09 INF Supported INF Request (2 ECUs w/ Order Swap) .....	193
9.4.18	Verify a SID \$09 INF Supported INF Request Diesel (2 ECUs w/ Order Swap) .....	194
9.4.19	Verify a SID \$09 INF Supported INF Request (8 ECUs w/ Order Swap) .....	194
9.4.20	Verify a SID \$09 VIN Request (1 ECU) .....	195
9.4.21	Verify a SID \$09 VIN Request with NRC=\$78 (1 ECU).....	196
9.4.22	Verify a SID \$09 CALID Request (2 ECUs w/ Order Swap).....	197
9.4.23	Verify a SID \$09 CALID Request with 9 CALIDs (8 ECUs w/ Order Swap) .....	197
9.4.24	Verify a SID \$09 CVN Request (2 ECU).....	199
9.4.25	Verify a SID \$09 CVN Request with 9 CVNs (8 ECUs w/ Order Swap) .....	199
9.4.26	Verify a SID \$09 IPT (INF\$08, 32 bytes) Request (2 ECUs w/ Order Swap).....	200
9.4.27	Verify a SID \$09 IPT (INF\$08, 40 bytes) Request (2 ECUs w/ Order Swap).....	202
9.4.28	Verify a SID \$09 IPT (INF\$0B, 32 bytes) Request (2 ECUs w/ Order Swap).....	203
9.4.29	Verify a SID \$09 IPT (INF\$0B, 36 bytes) Request (2 ECUs w/ Order Swap).....	204
9.5	SAE J1850 VPW Tests .....	205
9.5.1	Verify a SID \$01 PID Supported PIDs Requests (1 ECU).....	206
9.5.2	Verify a SID \$01 PID \$01 Request for Spark Ignition (2 ECUs w/ Order Swap) .....	207
9.5.3	Verify a SID \$01 PID \$01 Request for Compression Ignition (2 ECUs w/ Order Swap) .....	208
9.5.4	Verify a SID \$01 PID \$0C Request (2 ECUs w/ Order Swap) .....	209
9.5.5	Verify a SID \$02 PID Freeze Frame DTC Request with No DTCs (2 ECUs w/ Order Swap) .....	209
9.5.6	Verify a SID \$02 PID Freeze Frame DTC Request with DTCs (2 ECUs).....	210
9.5.7	Verify a SID \$03 Confirmed DTC Request with No DTCs (2 ECUs w/ Order Swap) .....	210
9.5.8	Verify a SID \$03 Confirmed DTC Request with DTCs (2 ECUs).....	211
9.5.9	Verify a SID \$04 Clear Codes Request (2 ECUs w/ Order Swap) .....	211
9.5.10	Test a SID \$04 Clear Codes Request with NRC=\$22 (2 ECUs) .....	212
9.5.11	Verify a SID \$04 Clear Codes Request with NRC=\$78 (2 ECUs) .....	212
9.5.12	Verify a SID \$05 TID \$01 Request (1 ECU).....	213
9.5.13	Verify a SID \$06 MID Supported MID Request (1 ECU) .....	213
9.5.14	Verify a SID \$07 Pending DTC Request with No DTCs (2 ECUs w/ Order Swap).....	214
9.5.15	Verify a SID \$07 Pending DTC Request with DTCs (2 ECUs) .....	214
9.5.16	Verify a SID \$08 TID Supported TID Request (1 ECU).....	215
9.5.17	Verify a SID \$09 INF Supported INF Request (2 ECUs w/ Order Swap) .....	215
9.5.18	Verify a SID \$09 INF Supported INF Request Diesel (2 ECUs w/ Order Swap) .....	216
9.5.19	Verify a SID \$09 INF Supported INF Request (2 ECUs w/ Order Swap) .....	216
9.5.20	Verify a SID \$09 VIN Request (1 ECU) .....	217
9.5.21	Verify a SID \$09 VIN Request with NRC=\$78 (1 ECU).....	218
9.5.22	Verify a SID \$09 CALID Request (2 ECUs w/ Order Swap) .....	219
9.5.23	Verify a SID \$09 CALID Request with 9 CALIDs (8 ECUs w/ Order Swap) .....	219
9.5.24	Verify a SID \$09 CVN Request (2 ECU).....	221
9.5.25	Verify a SID \$09 CVN Request with 9 CVNs from Each of ECUs (8 ECUs w/ Order Swap) .....	221
9.5.26	Verify a SID \$09 IPT (INF\$08, 32 bytes) Request (2 ECUs w/ Order Swap) .....	222
9.5.27	Verify a SID \$09 IPT (INF\$08, 40 bytes) Request (2 ECUs w/ Order Swap) .....	224
9.5.28	Verify a SID \$09 IPT (INF\$0B, 32 bytes) Request (2 ECUs w/ Order Swap).....	225
9.5.29	Verify a SID \$09 IPT (INF\$0B, 36 bytes) Request (2 ECUs w/ Order Swap).....	226
9.6	SAE J1850 PWM Tests.....	227
9.6.1	Verify a SID \$01 PID Supported PIDs Requests (1 ECU).....	228
9.6.2	Verify a SID \$01 PID \$01 Request for Spark Ignition (2 ECUs w/ Order Swap) .....	229
9.6.3	Verify a SID \$01 PID \$01 Request for Compression Ignition (2 ECUs w/ Order Swap) .....	230
9.6.4	Verify a SID \$01 PID \$0C Request (2 ECUs w/ Order Swap).....	231
9.6.5	Verify a SID \$02 PID Freeze Frame DTC Request with No DTCs (2 ECUs w/ Order Swap) .....	231
9.6.6	Verify a SID \$02 PID Freeze Frame DTC Request with DTCs (2 ECUs).....	232
9.6.7	Verify a SID \$03 Confirmed DTC Request with No DTCs (2 ECUs w/ Order Swap) .....	232
9.6.8	Verify a SID \$03 Confirmed DTC Request with DTCs (2 ECUs).....	233
9.6.9	Verify a SID \$04 Clear Codes Request (2 ECUs w/ Order Swap) .....	233
9.6.10	Test a SID \$04 Clear Codes Request with NRC=\$22 (2 ECUs) .....	234
9.6.11	Verify a SID \$04 Clear Codes Request with NRC=\$78 (2 ECUs).....	234
9.6.12	Verify a SID \$05 TID \$01 Request (1 ECU).....	235
9.6.13	Verify a SID \$06 MID Supported MID Request (1 ECU) .....	235

9.6.14	Verify a SID \$07 Pending DTC Request with No DTCs (2 ECUs w/ Order Swap).....	236
9.6.15	Verify a SID \$07 Pending DTC Request with DTCs (2 ECUs).....	236
9.6.16	Verify a SID \$08 TID Supported TID Request (1 ECU).....	237
9.6.17	Verify a SID \$09 INF Supported INF Request (2 ECUs w/ Order Swap).....	237
9.6.18	Verify a SID \$09 INF Supported INF Request (8 ECUs w/ Order Swap).....	238
9.6.19	Verify a SID \$09 VIN Request (1 ECU).....	239
9.6.20	Verify a SID \$09 VIN Request with NRC=\$78 (1 ECU).....	240
9.6.21	Verify a SID \$09 CALID Request (2 ECUs w/ Order Swap).....	240
9.6.22	Verify a SID \$09 CALID Request with 9 CALIDs (8 ECUs w/ Order Swap).....	241
9.6.23	Verify a SID \$09 CVN Request (2 ECU).....	242
9.6.24	Verify a SID \$09 CVN Request with 9 CVNs from Each of ECUs (8 ECUs w/ Order Swap).....	243
9.6.25	Verify a SID \$09 IPT (INF\$08, 32 bytes) Request (2 ECUs w/ Order Swap).....	244
9.6.26	Verify a SID \$09 IPT (INF\$08, 40 bytes) Request (2 ECUs w/ Order Swap).....	245
10.	PRESENTATION EVALUATION.....	246
10.1	General Tests.....	246
10.2	ISO 15765-4 Tests.....	246
10.3	ISO 14230-4 Tests.....	246
10.4	ISO 9141-2 Tests.....	246
10.5	SAE J1850 VPW Tests.....	246
10.6	SAE J1850 PWM Tests.....	247
11.	NOTES.....	247
11.1	Revision Indicator.....	247
APPENDIX A	.....	248

SAENORM.COM : Click to view the full PDF of J1699\_2\_202412

## 1. SCOPE

To define test cases for the OBD-II interface on external test equipment (such as an OBD-II Scan Tool, Inspection/Maintenance Tester, etc.) which can be used to verify compliance with the applicable standards such as SAE J1978 and SAE J1979 for Passenger Cars, Light-Duty Trucks, and Medium-Duty Vehicles and Engines (OBD II).

### 1.1 Purpose

To provide guidance to Scan Tool Manufacturers, Vehicle OEMs, and Regulators (CARB, EPA, etc.) for qualifying the OBD-II interface on external test equipment that is used for vehicle diagnostics and/or I/M testing.

## 2. REFERENCES

This specification takes precedence over all conflicts in the documents cited in this section.

### 2.1 Applicable Documents

The following publications form a part of this specification to the extent specified herein. Unless otherwise indicated, the latest issue of SAE publications shall apply.

#### 2.1.1 SAE Publications

Available from SAE International, 400 Commonwealth Drive, Warrendale, PA 15096-0001, Tel: 877-606-7323 (inside USA and Canada) or +1 724-776-4970 (outside USA), [www.sae.org](http://www.sae.org).

SAE J1850	Class B Data Communications Network Interface
SAE J1930	Electrical/Electronic Systems Diagnostic Terms, Definitions, Abbreviations, and Acronyms. This is equivalent to ISO/DIS 15031-2 - Communication Between Vehicle and External Equipment for Emissions-Related Diagnostics - Part 2: Terms, definitions, abbreviations and acronyms.
SAE J1978	OBD II Scan Tool. This is equivalent to ISO/DIS 15031-4 - Communication Between Vehicle and External Equipment for Emissions-Related Diagnostics - Part 4: External test equipment.
SAE J1979	E/E Diagnostic Test Modes. This is equivalent to ISO/DIS 15031-5 (April 30, 2002) - Communication Between Vehicle and External Equipment for Emissions-Related Diagnostics - Part 5: Emissions-related diagnostic services.
SAE J2012	Diagnostic Trouble Code Definitions. This is equivalent to ISO/DIS 15031-6 (April 30, 2002) - Communication Between Vehicle and External Equipment for Emissions-Related Diagnostics - Part 6: Diagnostic trouble code definitions.
SAE J2284/3	High-Speed CAN (HSC) for Vehicle Applications at 500 KBPS
SAE J2534	Recommended Practice for Pass-Thru Vehicle Programming

#### 2.1.2 ISO Publications

Copies of these documents are available online at <http://webstore.ansi.org/>.

ISO 9141-2	Road vehicles - Diagnostic systems - Part 2: CARB requirements for interchange of digital information
ISO 14230-4	Road vehicles - Diagnostic systems - Part 4: KWP 2000 requirements for emission related systems

ISO/DIS 15031-2	Communication Between Vehicle and External Equipment for Emissions-Related Diagnostics - Part 2: Terms, definitions, abbreviations and acronyms (equivalent to SAE J1930)
ISO/DIS 15031-3	Communication Between Vehicle and External Equipment for Emissions-Related Diagnostics - Part 3: Diagnostic connector and related electrical circuits, specification and use (equivalent to SAE J1962)
ISO/DIS 15031-4	Communication Between Vehicle and External Equipment for Emissions-Related Diagnostics (equivalent to SAE J1978)
ISO/DIS 15031-5	Communication Between Vehicle and External Equipment for Emissions-Related Diagnostics - Part 5: Emissions-related diagnostic services (equivalent to SAE J1979)
ISO/DIS 15031-6	Communication Between Vehicle and External Equipment for Emissions-Related Diagnostics - Part 6: Diagnostic trouble code definitions (equivalent to SAE J2012)
ISO 15765-4	Road vehicles - Diagnostics on Controller Area Network (CAN) - Part 4: Requirements for emissions-related systems

## 2.2 Related Publications

The following publications are for information purposes only and are not a required part of this document.

### 2.2.1 SAE Publications

Available from SAE International, 400 Commonwealth Drive, Warrendale, PA 15096-0001, Tel: 877-606-7323 (inside USA and Canada) or +1 724-776-4970 (outside USA), [www.sae.org](http://www.sae.org).

SAE J1699-1 SAE J1850 Verification Test Procedures

SAE J1699-3 OBD II Compliance Test Cases

### 2.2.2 ISO Publications

Copies of these documents are available online at <http://webstore.ansi.org/>.

ISO 11898 Road vehicles, Interchange of Digital information, Controller Area Network (CAN) for High Speed Communication

ISO 14229 Road Vehicles, Diagnostic Systems, Specification of Diagnostic Services

### 2.2.3 Other Publications

CARB Regulation Title 13, California Code Regulations, Section 1968.2, Malfunction and Diagnostic System Requirements for 2004 and Subsequent Model-Year Passenger Cars, Light-Duty Trucks, and Medium-Duty Vehicles and Engines (OBD II). Web address: <https://www.arb.ca.gov/msprog/obdprog/obdregs.htm>.

## 3. DEFINITIONS

### 3.1 Definition of Terms

The definitions of terms that are related to the use of this document may be found in the publications listed under 2.1, Applicable Documents.

### 3.2 Acronyms

The following are common acronyms used in this document:

- A/C Air Conditioning
- CAN Controller Area Network
- CALID Calibration Identification
- CID Component ID
- CVN Calibration Verification Number
- DLC Data Length Code (also Data Link Connector, e.g., SAE J1962 connector)
- DTC Diagnostic Trouble Code
- DTM Diagnostic Test Mode
- DUT Device Under Test (such as, a Generic Scan Tool, I/M Test Equipment, etc.)
- ECU Electronic Control Unit
- ECUSIM Electronic Control Unit Simulation (used to stimulate the DUT)
- HEV Hybrid Electric Vehicle
- HSC High Speed CAN
- ID Identification (number)
- I/M Inspection and Maintenance
- ISO International Standards Organization
- Kbps Kilobits per second
- KWP Key Word Protocol
- MIL Malfunction Indicator Lamp
- OBD-II On Board Diagnostics (level 2)
- OBDM On Board Diagnostic Monitor
- PCM Powertrain Control ECU
- PWM Pulse Width Modulation
- PID Parameter Identification (number)
- RPM Revolutions Per Minute (engine speed)
- SID Service ID
- TID Test ID
- VIN Vehicle Identification Number
- VPW Variable Pulse Width (modulation)

### 4. OVERVIEW

This document defines a set of compliance tests for OBD II Scan Tools and I/M Test Equipment. The test cases are designed to gain confidence that communications can be established and maintained with the vehicle. These tests will evaluate a variety of characteristics including mechanical attributes, electrical attributes, adherence to required protocol standards, support for required network services, and presentation of vehicle data. The requirements for these test cases are drawn from a variety of documents, but mainly focus on OBD II communication for light and medium duty vehicles as specified in SAE J1979 (ISO 15031-5).

Since OBD II Scan Tools and I/M Test Equipment have a variety of different requirements, the specific set of test cases required to verify compliance will also need to vary. For example, I/M Test Equipment may have no need to make any SID \$06 requests and therefore the test cases for SID \$06 would not be applicable (and would not be part of that device's compliance test). This document contains a super-set of test cases, from which product specific test case subsets can be selected to verify compliance.

While this document only defines the test cases, it is expected that some level of automation will need to exist to perform the test cases as described. It is envisioned that this would consist of a PC, some instrumentation, a SAE J2534-2 Interface, and some custom software.

## 4.1 Documentation Conventions

This document details a number of test cases, which have been divided into sections for ease of testing. The test cases that have an expected positive (or successful) result start with the phrase “Verify”. Test cases that expect a failure (or result in something other than successful) start with the phrase “Test”. Each test case consists of one or more steps. Each step (labeled “ACTION”) has and an associated evaluation criteria (labeled “EVALUATION:”).

The phrase “ACTION” shall be followed by the specific device to be used. “ECUSIM” indicates that the associated action must be applied to the ECU Simulation Device. The ECU Simulation Device provides the network messages that would typically come from a vehicle. It is envisioned that this would consist of a PC, a SAE J2534-2 Interface, and some software to operate in the manner described in the test cases. “INST” indicates that the associated action must be applied to some piece of Instrumentation (like the Power Supply, Scope, DVOM, etc.). The Instrumentation hardware could also be part of the ECU Simulation Device. “DUT” indicates that the associated action must be applied to the Device Under Test.

The evaluation criteria (“EVALUATION:”) shall identify the expected results. In some instances, it is easier to identify the criteria for passing (for example, “Pass if ...”). In these cases, any result not listed as a pass shall be considered a failure. However, in some cases it will be easier to identify the criteria for failing (for example, “Fail if ...”). In these cases, any result not listed as a failure shall be considered a pass. A test case failure is defined as the results of an individual action that does not meet the associated evaluation criteria. If a test case has failed, any remaining actions for that test case need not be executed as continuing on may produce unpredictable results.

Each test case shall have an associated Test ID. This number shall be unique and shall not change. The Test ID shall be used when referencing a test case, as section numbers in the document may change in subsequent revisions. The Test IDs have been structured to aid in automation. The Test ID is a hexadecimal number that shall have the following format with each letter representing a single hex digit:

\$GRTFFCCC

where:

G indicates the test Group (a total of 16):

- 0 for Mechanical
- 1 for Electrical
- 2 for Protocol
- 3 for Network
- 4 for Presentation

R is RESERVED and must be 0

T indicates the Type of test (a total of 16):

- 0 for General Tests
- 1 for ISO 15765-4 Tests
- 2 for ISO 14230-4 Tests
- 3 for ISO 9141-2 Tests
- 4 for SAE J1850 VPW Tests
- 5 for SAE J1850 PWM Tests

FF indicates the Feature to be tested (a total of 256):

When G = 0 (Mechanical)

- 00 for General
- 01 for Connector
- 02 for Cable

When G = 1 (Electrical)

- 00 for General
- 01 for Voltage
- 02 for Current
- 03 for Capacitance
- 04 for Impedance/Resistance
- 05 for Wave Shaping

When G = 2 (Protocol)

- 00 for General
- 01 for Bit Times
- 02 for Transport Protocol
- 03 for Initialization
- 10 for ETI Anomalies

When G = 3 (Network)

00 for General

01 for SID 1

02 for SID 2

03 for SID 3

04 for SID 4

05 for SID 5

06 for SID 6

07 for SID 7

08 for SID 8

09 for SID 9

0A for SID A

FF for ETI Anomalies

When G = 4 (Presentation)

00 for General

01 for SAE J1978

CCC indicates the specific test case number (a total of 4095)

In the body of the Test Cases, specific messages and timing parameters will be referred to by an alpha-numeric text in bold (for example, **SID\$01\_PID\$00**). These references correspond to specific messages and parameter values are listed in various tables throughout Section 5.

All numbers are in decimal unless otherwise indicated. (Hexadecimal values shall have a leading '\$'; for example, \$7AF or \$800 are hexadecimal values.)

All references to protocol specific parameters that are specified in other documents will be in italics (for example, *W0*).

## 5. GENERAL TEST SET-UP

This section provides an overview of how the test equipment and the DUT should be setup. The 'Setup' section of each test case will describe the exact state of each piece of test equipment. Please note that not every piece of test equipment will be used in every test case. All testing shall be conducted at a temperature of 23 °C ( $\pm 5$  °C) and between 25 and 95% relative humidity.

### 5.1 DUT Requirements

The DUT shall include all equipment required to operate in the manner described in the test cases. This shall include the mechanism used to attach to the vehicle's SAE J1962 connector.

### 5.2 Test Equipment Requirements

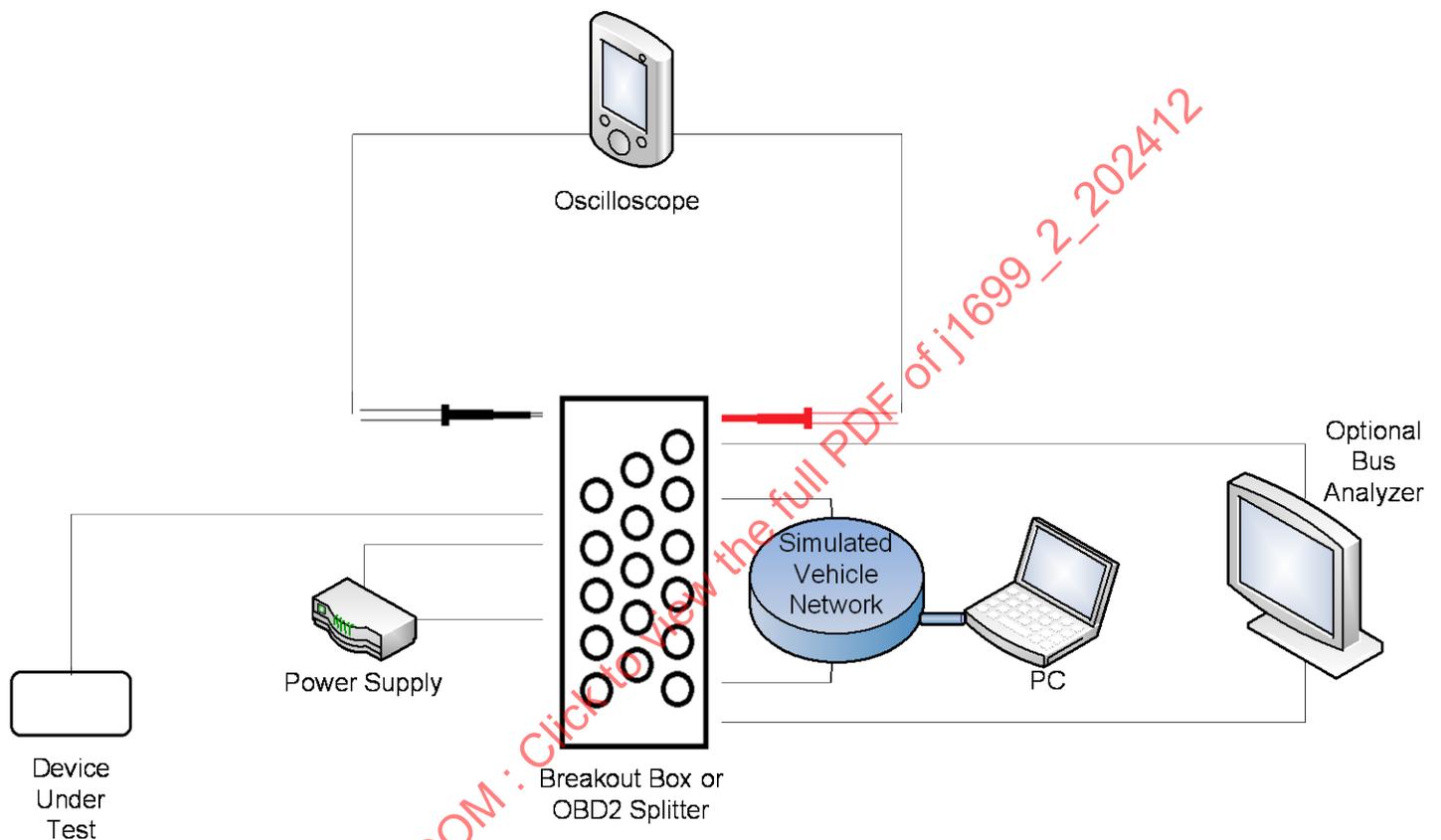
The following list of test equipment is suggested when executing the tests contained in this document. The suggested equipment will also aid in test automation. Please note that not every piece of test equipment will be required for every test.

- Dual Channel 1 GHz Oscilloscope or equivalent
- SAE J2534-2 Interface, capable of disabling CAN In-Frame Acknowledgements
- A generic PC running a Windows™ operating system and capable of executing WIN32 function calls
- Software that can run on the PC and interact with the SAE J2534-2 Interface in the manner described in the test cases (e.g., simulate the network messages that would typically come from a vehicle, etc.)
- A SAE J1962 Break-Out Box or OBD2 Splitter

- A current probe or current meter
- Calipers, a ruler, and a micrometer
- A +20 V DC (5 A) Variable Power Supply - This will provide power the ECUSIM and DUT during the tests. Additionally, this power supply shall provide power for a variety of electrical tests.

### 5.3 Typical Test Set-Up

Figure 1 outlines the typical set-up.



**Figure 1 - Typical test set-up**

### 5.4 Pre-Defined Messages, Message Timing, and Test Set-Up States

#### 5.4.1 General States

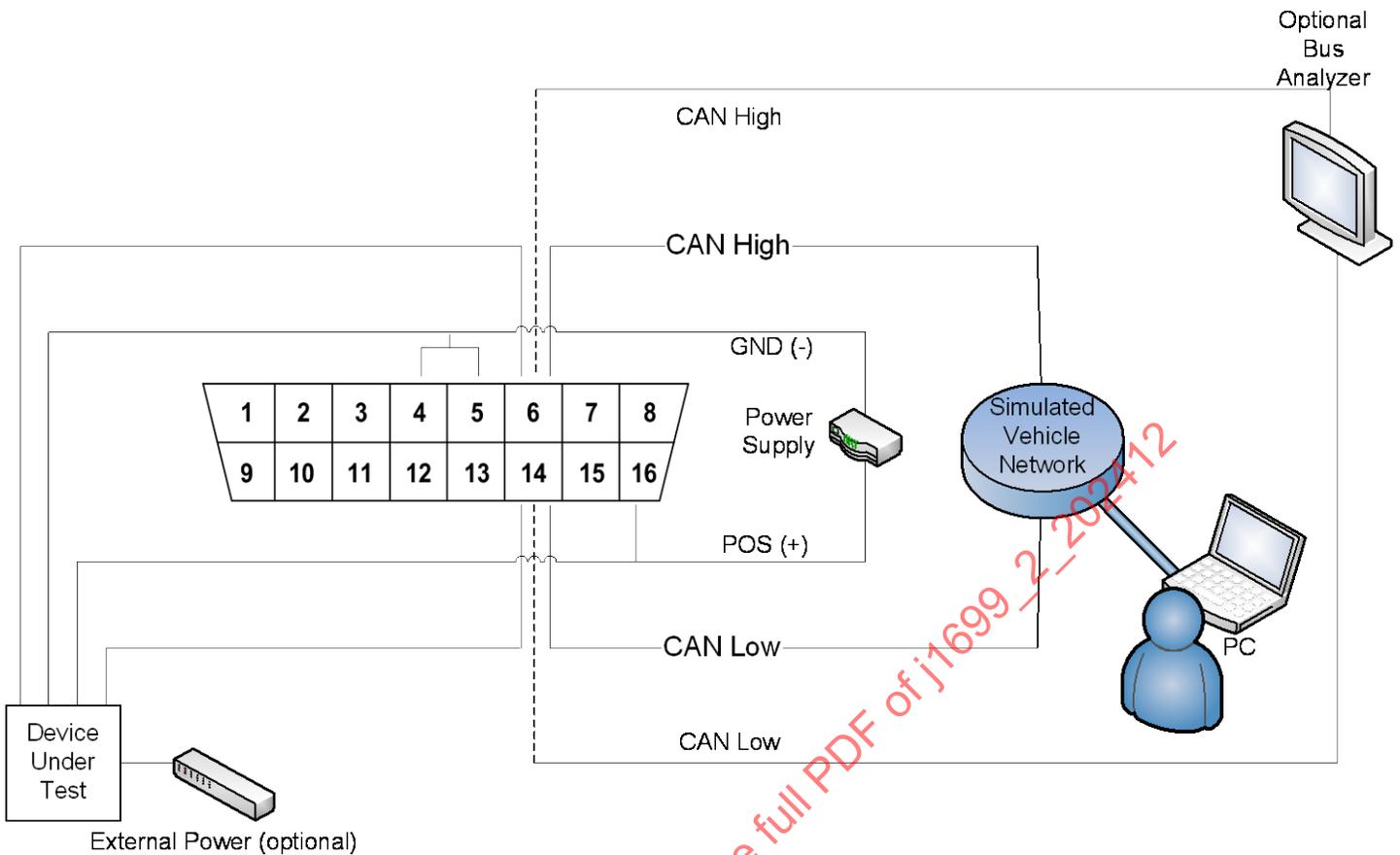
##### 5.4.1.1 Power On

The Power On state is immediately after power up when the device is connected, but there is no active OBD communication.

#### 5.4.2 CAN

##### 5.4.2.1 CAN Electrical Connections

Figure 2 outlines the typical set-up for CAN testing.



**Figure 2 - Test set-up for CAN testing**

5.4.2.2 CAN ECU Addresses

At this time, none have been defined.

5.4.2.3 CAN Messages

The following messages have been pre-defined for 11-bit CAN. For convenience, the test cases in this document will use the text in the 'Message Reference' column when referring to the specific set of message bytes, detailed in the 'Message Bytes' column of the table below.

Message Reference	Message Bytes
CAN 11 SID\$09 INF\$04	\$00 \$00 \$07 \$DF \$02 \$09 \$04
CAN 11 FC BSO STMIN0	\$00 \$00 \$07 \$DF \$30 \$00 \$00
CAN 11 ECU1 SID\$09 INF\$04 FF	\$00 \$00 \$07 \$E8 \$10 \$13 \$49 \$04 \$01 \$31 \$32 \$35

The first four bytes are the CAN ID bits 28-24 first, followed by CAN ID bits 23-16, CAN ID bits 15-8, CAN ID bits 7-0.

**Figure 3 - Pre-defined messages for CAN (11-bit)**

## 5.4.2.4 CAN Message Timing

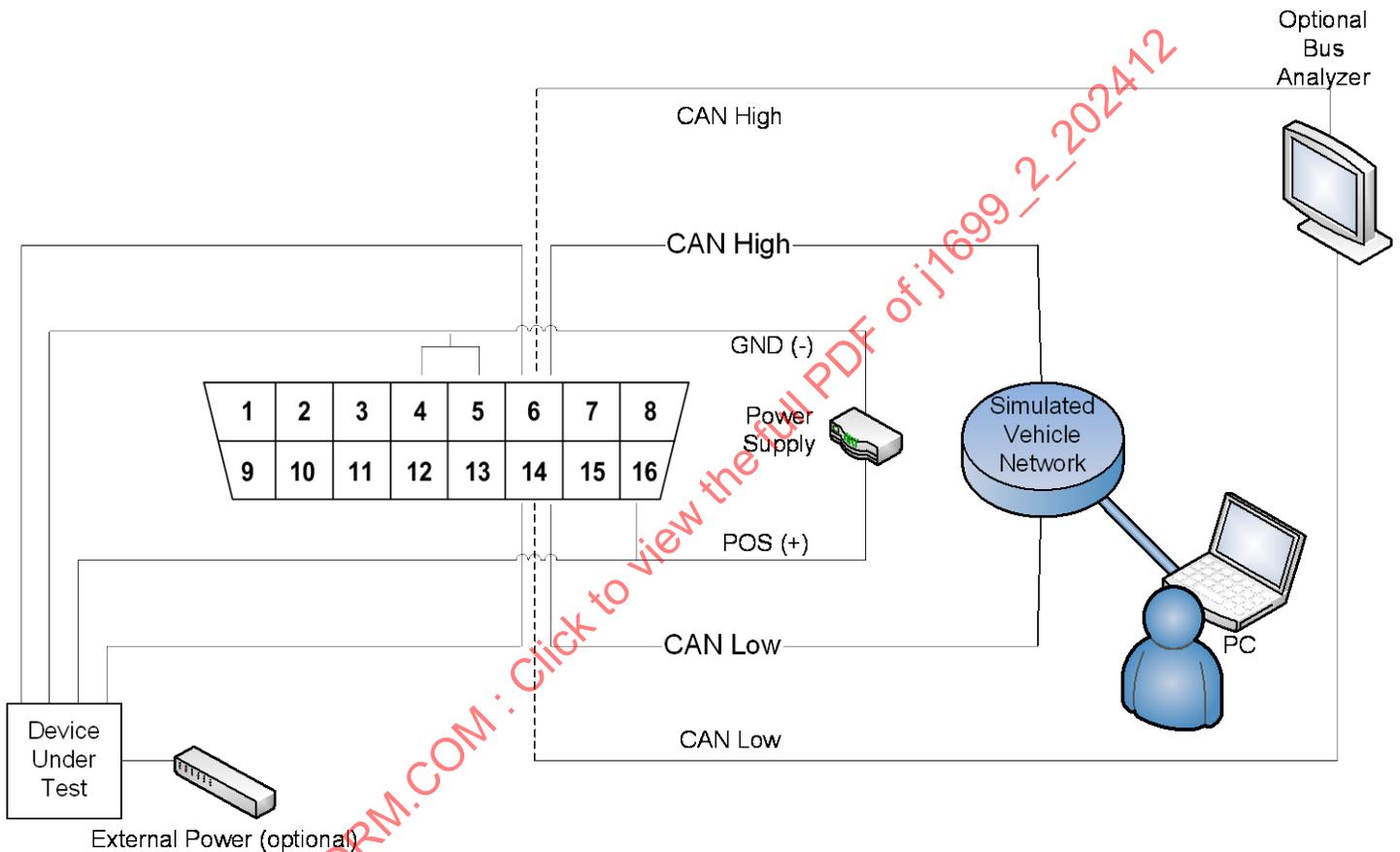
At this time, none have been defined.

## 5.4.2.5 CAN (11-bit, 500K) Connected State

The device shall initially be in the 'Power On' state, connected to the CAN network with the appropriate termination (as depicted in 5.4.3.1), then configured for 11-bit CAN Identifiers at a baud rate of 500000.

## 5.4.3 ISO 15765-4

## 5.4.3.1 ISO 15765-4 Electrical Connections



**Figure 4 - Test set-up for ISO 15765-4 testing**

## 5.4.3.2 ISO 15765-4 ECU Addresses

The following address bytes have been pre-defined for 11-bit ISO 15765-4. For convenience, the test cases in this document will use the text in the 'ECU Reference' column when referring to the specific set of address bytes, detailed in the 'Address Bytes' column of the table below.

ECU Reference	Address Bytes
ECU #1	\$00 \$00 \$07 \$E8
ECU #2	\$00 \$00 \$07 \$E9
ECU #3	\$00 \$00 \$07 \$EA
ECU #4	\$00 \$00 \$07 \$EB
ECU #5	\$00 \$00 \$07 \$EC
ECU #6	\$00 \$00 \$07 \$ED
ECU #7	\$00 \$00 \$07 \$EE
ECU #8	\$00 \$00 \$07 \$EF

The first four bytes are the CAN ID bits 28-24 first, followed by CAN ID bits 23-16, CAN ID bits 15-8, CAN ID bits 7-0.

**Figure 5 - ECU reference and associated addresses for ISO 15765-4 (11-bit)**

The following address bytes have been pre-defined for 29-bit ISO 15765-4. For convenience, the test cases in this document will use the text in the 'ECU Reference' column when referring to the specific set of address bytes, detailed in the 'Address Bytes' column of the table below.

ECU Reference	Address Bytes
ECU #1	\$18 \$DA \$F1 \$10
ECU #2	\$18 \$DA \$F1 \$11
ECU #3	\$18 \$DA \$F1 \$12
ECU #4	\$18 \$DA \$F1 \$13
ECU #5	\$18 \$DA \$F1 \$14
ECU #6	\$18 \$DA \$F1 \$15
ECU #7	\$18 \$DA \$F1 \$16
ECU #8	\$18 \$DA \$F1 \$17

The first four bytes are the CAN ID bits 28-24 first, followed by CAN ID bits 23-16, CAN ID bits 15-8, CAN ID bits 7-0.

**Figure 6 - ECU reference and associated addresses for ISO 15765-4 (29-bit)**

## 5.4.3.3 ISO 15765-4 Messages

The following messages have been pre-defined for 11-bit ISO 15765-4. These messages represent the fully assembled message and do not include any transport layer mechanisms that appear in the individual CAN Frames (like the PCI byte, Flow Control messages, etc.). For convenience, the test cases in this document will use the text in the 'Message Reference' column when referring to the specific set of message bytes, detailed in the 'Message Bytes' column of the table below.

Message Reference	Message Bytes
15765 11 SID\$01 PID\$00	\$00 \$00 \$07 \$DF \$01 \$00
15765_11_SID\$01_PID\$00-\$E0	\$00 \$00 \$07 \$DF \$01 \$00 \$20 \$40 \$60 \$80 \$A0 \$C0 \$E0
15765 11 SID\$01 PID\$01	\$00 \$00 \$07 \$DF \$01 \$01
15765 11 SID\$01 PID\$0C	\$00 \$00 \$07 \$DF \$01 \$0C
15765 11 SID\$01 PID\$20	\$00 \$00 \$07 \$DF \$01 \$20
15765 11 SID\$01 PID\$40	\$00 \$00 \$07 \$DF \$01 \$40
15765 11 SID\$01 PID\$41	\$00 \$00 \$07 \$DF \$01 \$41
15765 11 SID\$01 PID\$01-\$0C-\$41	\$00 \$00 \$07 \$DF \$01 \$01 \$0C \$41
15765 11 ECU1 SID\$01 PID\$00	\$00 \$00 \$07 \$E8 \$41 \$00 \$80 \$10 \$00 \$00
15765 11 ECU1 SID\$01 PID\$00 20	\$00 \$00 \$07 \$E8 \$41 \$00 \$80 \$10 \$00 \$01
15765_11_ECU1_SID\$01_PID\$00-\$0E	\$00 \$00 \$07 \$E8 \$41 \$00 \$80 \$00 \$00 \$00 \$20 \$00 \$00 \$00 \$01 \$40 \$10 \$00 \$00 \$00
15765 11 ECU1 SID\$01 PID\$01	\$00 \$00 \$07 \$E8 \$41 \$01 \$00 \$07 \$EF \$EF
15765 11 ECU1 SID\$01 PID\$01 SPARK	\$00 \$00 \$07 \$E8 \$41 \$01 \$00 \$07 \$EF \$EF
15765 11 ECU1 SID\$01 PID\$01 COMP	\$00 \$00 \$07 \$E8 \$41 \$01 \$00 \$0F \$EB \$EB
15765 11 ECU1 SID\$01 PID\$0C KOEO	\$00 \$00 \$07 \$E8 \$41 \$0C \$00 \$00
15765 11 ECU1 SID\$01 PID\$20	\$00 \$00 \$07 \$E8 \$41 \$20 \$00 \$00 \$00 \$01
15765 11 ECU1 SID\$01 PID\$40	\$00 \$00 \$07 \$E8 \$41 \$40 \$10 \$00 \$00 \$00
15765 11 ECU1 SID\$01 PID\$41 SPARK	\$00 \$00 \$07 \$E8 \$41 \$41 \$00 \$07 \$EF \$EF
15765_11_ECU1_SID\$01_PID\$01-\$0C-\$41	\$00 \$00 \$07 \$E8 \$41 \$01 \$00 \$07 \$EF \$EF \$0C \$00 \$00 \$41 \$00 \$07 \$EF \$EF
15765 11 ECU1 SID\$01 NRC\$21	\$00 \$00 \$07 \$E8 \$7F \$01 \$21
15765 11 ECU2 SID\$01 PID\$00	\$00 \$00 \$07 \$E9 \$41 \$00 \$80 \$10 \$00 \$00
15765 11 ECU2 SID\$01 PID\$01 SPARK	\$00 \$00 \$07 \$E9 \$41 \$01 \$81 \$44 \$00 \$00
15765 11 ECU2 SID\$01 PID\$01 COMP	\$00 \$00 \$07 \$E9 \$41 \$01 \$81 \$4C \$00 \$00
15765 11 ECU2 SID\$01 PID\$0C KOEO	\$00 \$00 \$07 \$E9 \$41 \$0C \$00 \$00
15765 11 ECU3 SID\$01 PID\$00	\$00 \$00 \$07 \$EA \$41 \$00 \$00 \$00 \$00 \$00
15765 11 ECU4 SID\$01 PID\$00	\$00 \$00 \$07 \$EB \$41 \$00 \$00 \$00 \$00 \$00
15765 11 ECU5 SID\$01 PID\$00	\$00 \$00 \$07 \$EC \$41 \$00 \$00 \$00 \$00 \$00
15765 11 ECU6 SID\$01 PID\$00	\$00 \$00 \$07 \$ED \$41 \$00 \$00 \$00 \$00 \$00
15765 11 ECU7 SID\$01 PID\$00	\$00 \$00 \$07 \$EE \$41 \$00 \$00 \$00 \$00 \$00
15765 11 ECU8 SID\$01 PID\$00	\$00 \$00 \$07 \$EF \$41 \$00 \$00 \$00 \$00 \$00
15765 11 SID\$02 PID\$02	\$00 \$00 \$07 \$DF \$02 \$02 \$00
15765 11 ECU1 SID\$02 PID\$02 NoDTC	\$00 \$00 \$07 \$E8 \$42 \$02 \$00 \$00 \$00
15765 11 ECU2 SID\$02 PID\$02 NoDTC	\$00 \$00 \$07 \$E9 \$42 \$02 \$00 \$00 \$00
15765 11 ECU1 SID\$02 PID\$02 DTC	\$00 \$00 \$07 \$E8 \$42 \$02 \$00 \$01 \$07
15765 11 SID\$03	\$00 \$00 \$07 \$DF \$03
15765 11 ECU1 SID\$03 NoDTC	\$00 \$00 \$07 \$E8 \$43 \$00
15765_11_ECU1_SID\$03_DTC	\$00 \$00 \$07 \$E8 \$43 \$04 \$01 \$07 \$01 \$10 \$01 \$15 \$01 \$24
15765 11 ECU2 SID\$03 NoDTC	\$00 \$00 \$07 \$E9 \$43 \$00
15765 11 SID\$04	\$00 \$00 \$07 \$DF \$04
15765 11 ECU1 SID\$04 DONE	\$00 \$00 \$07 \$E8 \$44
15765 11 ECU1 SID\$04 NRC22	\$00 \$00 \$07 \$E8 \$7F \$44 \$22
15765 11 ECU1 SID\$04 NRC78	\$00 \$00 \$07 \$E8 \$7F \$44 \$78
15765 11 ECU2 SID\$04 DONE	\$00 \$00 \$07 \$E9 \$44
15765 11 ECU2 SID\$04 NRC22	\$00 \$00 \$07 \$E9 \$7F \$44 \$22
15765 11 SID\$06 MID\$00	\$00 \$00 \$07 \$DF \$06 \$00
15765 11 ECU1 SID\$06 MID\$00	\$00 \$00 \$07 \$E8 \$46 \$00 \$CC \$00 \$00 \$00
15765 11 SID\$07	\$00 \$00 \$07 \$DF \$07

15765_11_ECU1_SID\$07_NoDTC	\$00 \$00 \$07 \$E8 \$47 \$00
15765_11_ECU1_SID\$07_DTC	\$00 \$00 \$07 \$E8 \$47 \$04 \$01 \$07 \$01 \$10 \$01 \$15 \$01 \$24
15765_11_ECU2_SID\$07_NoDTC	\$00 \$00 \$07 \$E9 \$47 \$00
15765_11_SID\$08_TID\$00	\$00 \$00 \$07 \$DF \$08
15765_11_ECU1_SID\$08_TID\$00	\$00 \$00 \$07 \$E8 \$48 \$00 \$80 \$00 \$00 \$00
15765_11_SID\$09_INF\$00	\$00 \$00 \$07 \$DF \$09 \$00
15765_11_SID\$09_INF\$02	\$00 \$00 \$07 \$DF \$09 \$02
15765_11_SID\$09_INF\$04	\$00 \$00 \$07 \$DF \$09 \$04
15765_11_SID\$09_INF\$06	\$00 \$00 \$07 \$DF \$09 \$06
15765_11_SID\$09_INF\$08	\$00 \$00 \$07 \$DF \$09 \$08
15765_11_SID\$09_INF\$0A	\$00 \$00 \$07 \$DF \$09 \$0A
15765_11_SID\$09_INF\$0B	\$00 \$00 \$07 \$DF \$09 \$0B
15765_11_SID\$09_INF\$0D	\$00 \$00 \$07 \$DF \$09 \$0D
15765_11_SID\$09_INF\$0F	\$00 \$00 \$07 \$DF \$09 \$0F
15765_11_ECU1_SID\$09_INF\$00	\$00 \$00 \$07 \$E8 \$49 \$00 \$55 \$40 \$00 \$00
15765_11_ECU1_SID\$09_INF\$00_DIESEL	\$00 \$00 \$07 \$E8 \$49 \$00 \$54 \$6A \$00 \$00
15765_11_ECU1_SID\$09_INF\$02	\$00 \$00 \$07 \$E8 \$49 \$02 \$01 \$31 \$47 \$59 \$44 \$45 \$36 \$33 \$41 \$37 \$41 \$30 \$30 \$30 \$30 \$30 \$30
15765_11_ECU1_SID\$09_INF\$04	\$00 \$00 \$07 \$E8 \$49 \$04 \$01 \$31 \$32 \$35 \$38 \$39 \$37 \$37 \$32 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00
15765_11_ECU1_SID\$09_INF\$04_9	\$00 \$00 \$07 \$E8 \$49 \$04 \$09 \$31 \$31 \$35 \$38 \$39 \$37 \$37 \$37 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$32 \$31 \$35 \$38 \$39 \$37 \$37 \$37 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$33 \$31 \$35 \$38 \$39 \$37 \$37 \$36 \$00 \$00 \$00 \$00 \$00 \$00 \$34 \$31 \$35 \$38 \$39 \$37 \$37 \$35 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$35 \$31 \$35 \$38 \$39 \$37 \$37 \$34 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$36 \$31 \$35 \$38 \$39 \$37 \$37 \$33 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$37 \$31 \$35 \$38 \$39 \$37 \$37 \$32 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$38 \$31 \$35 \$38 \$39 \$37 \$37 \$31 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$39 \$31 \$35 \$38 \$39 \$37 \$37 \$30 \$00 \$00 \$00 \$00 \$00 \$00 \$00
15765_11_ECU1_SID\$09_INF\$06	\$00 \$00 \$07 \$E8 \$49 \$06 \$01 \$17 \$91 \$BC \$88
15765_11_ECU1_SID\$09_INF\$06_9	\$00 \$00 \$07 \$E8 \$49 \$06 \$09 \$17 \$91 \$BC \$88 \$18 \$91 \$BC \$87 \$19 \$91 \$BC \$86 \$1A \$91 \$BC \$85 \$1B \$91 \$BC \$84 \$1C \$91 \$BC \$83 \$1D \$91 \$BC \$82 \$1E \$55 \$AA \$81 \$1F \$FF \$00 \$80
15765_11_ECU1_SID\$09_INF\$08_32	\$00 \$00 \$07 \$E8 \$49 \$08 \$10 \$00 \$64 \$00 \$69 \$00 \$0A \$00 \$0F \$00 \$00 \$00 \$00 \$00 \$1E \$00 \$23 \$00 \$00 \$00 \$00 \$00 \$32 \$00 \$37 \$00 \$3C \$00 \$41 \$00 \$46 \$00 \$4B
15765_11_ECU1_SID\$09_INF\$08_40	\$00 \$00 \$07 \$E8 \$49 \$08 \$14 \$00 \$64 \$00 \$69 \$00 \$0A \$00 \$0F \$00 \$00 \$00 \$00 \$00 \$1E \$00 \$23 \$00 \$00 \$00 \$00 \$00 \$32 \$00 \$37 \$00 \$3C \$00 \$41 \$00 \$46 \$00 \$4B \$00 \$5A \$00 \$5F \$00 \$00 \$00 \$00
15765_11_ECU1_SID\$09_INF\$0A	\$00 \$00 \$07 \$E8 \$49 \$0A \$01 \$45 \$43 \$4D \$31 \$2D \$45 \$6E \$67 \$69 \$6E \$65 \$43 \$6F \$6E \$74 \$72 \$6F \$6C \$31 \$00
15765_11_ECU1_SID\$09_INF\$0B_32	\$00 \$00 \$07 \$E8 \$49 \$0B \$10 \$00 \$64 \$00 \$69 \$00 \$0A \$00 \$0F \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$32 \$00 \$37 \$00 \$3C \$00 \$41 \$00 \$46 \$00 \$4B
15765_11_ECU1_SID\$09_INF\$0B_36	\$00 \$00 \$07 \$E8 \$49 \$0B \$12 \$00 \$64 \$00 \$69 \$00 \$0A \$00 \$0F \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$32 \$00 \$37 \$00 \$3C \$00 \$41 \$00 \$46 \$00 \$4B \$00 \$50 \$00 \$55

15765_11_ECU1_SID\$09_INF\$0D	\$00 \$00 \$07 \$E8 \$49 \$0D \$01 \$00 \$00 \$00 \$00 \$56 \$41 \$4C \$44 \$45 \$53 \$4E \$39 \$38 \$37 \$36 \$35 \$31
15765_11_ECU1_SID\$09_INF\$0F	\$00 \$00 \$07 \$E8 \$49 \$0F \$01 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$56 \$41 \$4C \$44 \$45 \$52 \$54 \$41 \$4E \$31
15765_11_ECU1_SID\$09_NRC78	\$00 \$00 \$07 \$E8 \$7F \$49 \$78
15765_11_ECU2_SID\$09_INF\$00	\$00 \$00 \$07 \$E9 \$49 \$00 \$15 \$40 \$00 \$00
15765_11_ECU2_SID\$09_INF\$00_DIESEL	\$00 \$00 \$07 \$E9 \$49 \$00 \$14 \$6A \$00 \$00
15765_11_ECU2_SID\$09_INF\$04	\$00 \$00 \$07 \$E9 \$49 \$04 \$01 \$32 \$34 \$32 \$32 \$31 \$34 \$31 \$39 \$00 \$00 \$00 \$00 \$00 \$00 \$00
15765_11_ECU2_SID\$09_INF\$04_9	\$00 \$00 \$07 \$E9 \$49 \$04 \$09 \$31 \$32 \$35 \$38 \$39 \$37 \$37 \$38 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$32 \$32 \$35 \$38 \$39 \$37 \$37 \$37 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$33 \$32 \$35 \$38 \$39 \$37 \$37 \$36 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$34 \$32 \$35 \$38 \$39 \$37 \$37 \$35 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$35 \$32 \$35 \$38 \$39 \$37 \$37 \$34 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$36 \$32 \$35 \$38 \$39 \$37 \$37 \$33 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$37 \$32 \$35 \$38 \$39 \$37 \$37 \$32 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$38 \$32 \$35 \$38 \$39 \$37 \$37 \$31 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$39 \$32 \$35 \$38 \$39 \$37 \$37 \$30 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00
15765_11_ECU2_SID\$09_INF\$06	\$00 \$00 \$07 \$E9 \$49 \$06 \$01 \$27 \$91 \$BC \$88
15765_11_ECU2_SID\$09_INF\$06_9	\$00 \$00 \$07 \$E9 \$49 \$06 \$09 \$27 \$91 \$BC \$88 \$28 \$91 \$BC \$87 \$29 \$91 \$BC \$86 \$2A \$91 \$BC \$85 \$2B \$91 \$BC \$84 \$2C \$91 \$BC \$83 \$2D \$91 \$BC \$82 \$2E \$55 \$AA \$81 \$2F \$FF \$00 \$80
15765_11_ECU2_SID\$09_INF\$08_32	\$00 \$00 \$07 \$E9 \$49 \$08 \$10 \$00 \$64 \$00 \$69 \$00 \$00 \$00 \$00 \$00 \$14 \$00 \$19 \$00 \$00 \$00 \$00 \$28 \$00 \$2D \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00
15765_11_ECU2_SID\$09_INF\$08_40	\$00 \$00 \$07 \$E9 \$49 \$08 \$14 \$00 \$64 \$00 \$69 \$00 \$00 \$00 \$00 \$00 \$14 \$00 \$19 \$00 \$00 \$00 \$00 \$28 \$00 \$2D \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$5A \$00 \$5F
15765_11_ECU2_SID\$09_INF\$0A	\$00 \$00 \$07 \$E9 \$49 \$0A \$01 \$45 \$43 \$4D \$32 \$2D \$45 \$6E \$67 \$69 \$6E \$65 \$43 \$6F \$6E \$74 \$72 \$6F \$6C \$32 \$00
15765_11_ECU2_SID\$09_INF\$0B_32	\$00 \$00 \$07 \$E9 \$49 \$0B \$10 \$00 \$64 \$00 \$69 \$00 \$00 \$00 \$00 \$00 \$14 \$00 \$19 \$00 \$00 \$00 \$00 \$28 \$00 \$2D \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00
15765_11_ECU2_SID\$09_INF\$0B_36	\$00 \$00 \$07 \$E9 \$49 \$0B \$12 \$00 \$64 \$00 \$69 \$00 \$00 \$00 \$00 \$00 \$14 \$00 \$19 \$00 \$00 \$00 \$00 \$28 \$00 \$2D \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00
15765_11_ECU2_SID\$09_INF\$0D	\$00 \$00 \$07 \$E9 \$49 \$0D \$01 \$00 \$00 \$00 \$00 \$56 \$41 \$4C \$44 \$45 \$53 \$4E \$39 \$38 \$37 \$36 \$35 \$32
15765_11_ECU2_SID\$09_INF\$0F	\$00 \$00 \$07 \$E9 \$49 \$0F \$01 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$56 \$41 \$4C \$44 \$45 \$52 \$54 \$41 \$4E \$32
15765_11_ECU3_SID\$09_INF\$00	\$00 \$00 \$07 \$EA \$49 \$00 \$14 \$40 \$00 \$00
15765_11_ECU3_SID\$09_INF\$00_DIESEL	\$00 \$00 \$07 \$EA \$49 \$00 \$14 \$4A \$00 \$00
15765_11_ECU3_SID\$09_INF\$04_9	\$00 \$00 \$07 \$EA \$49 \$04 \$09 \$31 \$33 \$35 \$38 \$39 \$37 \$37 \$38 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$32 \$33 \$35 \$38 \$39 \$37 \$37 \$37 \$00 \$00 \$00

	\$00 \$00 \$00 \$00 \$33 \$33 \$35 \$38 \$39 \$37 \$37 \$36 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$34 \$33 \$35 \$38 \$39 \$37 \$37 \$35 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$35 \$33 \$35 \$38 \$39 \$37 \$37 \$34 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$36 \$33 \$35 \$38 \$39 \$37 \$37 \$33 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$37 \$33 \$35 \$38 \$39 \$37 \$37 \$32 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$38 \$33 \$35 \$38 \$39 \$37 \$37 \$31 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$39 \$33 \$35 \$38 \$39 \$37 \$37 \$30 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00
15765_11_ECU3_SID\$09_INF\$06_9	\$00 \$00 \$07 \$EA \$49 \$06 \$09 \$37 \$91 \$BC \$88 \$38 \$91 \$BC \$87 \$39 \$91 \$BC \$86 \$3A \$91 \$BC \$85 \$3B \$91 \$BC \$84 \$3C \$91 \$BC \$83 \$3D \$91 \$BC \$82 \$3E \$55 \$AA \$81 \$3F \$FF \$00 \$80
15765_11_ECU3_SID\$09_INF\$0A	\$00 \$00 \$07 \$EA \$49 \$0A \$01 \$54 \$43 \$4D \$31 \$2D \$54 \$72 \$61 \$6E \$73 \$6D \$69 \$73 \$43 \$74 \$72 \$6C \$31 \$00 \$00
15765_11_ECU3_SID\$09_INF\$0D	\$00 \$00 \$07 \$EA \$49 \$0D \$01 \$00 \$00 \$00 \$00 \$56 \$41 \$4C \$44 \$45 \$53 \$4E \$39 \$38 \$37 \$36 \$35 \$33
15765_11_ECU3_SID\$09_INF\$0F	\$00 \$00 \$07 \$EA \$49 \$0F \$01 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$56 \$41 \$4C \$44 \$45 \$52 \$54 \$41 \$4E \$33
15765_11_ECU4_SID\$09_INF\$00	\$00 \$00 \$07 \$EB \$49 \$00 \$14 \$40 \$00 \$00
15765_11_ECU4_SID\$09_INF\$00_DIESEL	\$00 \$00 \$07 \$EB \$49 \$00 \$14 \$4A \$00 \$00
15765_11_ECU4_SID\$09_INF\$04_9	\$00 \$00 \$07 \$EB \$49 \$04 \$09 \$31 \$34 \$35 \$38 \$39 \$37 \$37 \$38 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$32 \$34 \$35 \$38 \$39 \$37 \$37 \$37 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$33 \$34 \$35 \$38 \$39 \$37 \$37 \$36 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$34 \$34 \$35 \$38 \$39 \$37 \$37 \$35 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$35 \$34 \$35 \$38 \$39 \$37 \$37 \$34 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$36 \$34 \$35 \$38 \$39 \$37 \$37 \$33 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$37 \$34 \$35 \$38 \$39 \$37 \$37 \$32 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$38 \$34 \$35 \$38 \$39 \$37 \$37 \$31 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$39 \$34 \$35 \$38 \$39 \$37 \$37 \$30 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00
15765_11_ECU4_SID\$09_INF\$06_9	\$00 \$00 \$07 \$EB \$49 \$06 \$09 \$47 \$91 \$BC \$88 \$48 \$91 \$BC \$87 \$49 \$91 \$BC \$86 \$4A \$91 \$BC \$85 \$4B \$91 \$BC \$84 \$4C \$91 \$BC \$83 \$4D \$91 \$BC \$82 \$4E \$55 \$AA \$81 \$4F \$FF \$00 \$80
15765_11_ECU4_SID\$09_INF\$0A	\$00 \$00 \$07 \$EB \$49 \$0A \$01 \$45 \$43 \$43 \$31 \$2D \$45 \$6D \$69 \$73 \$43 \$72 \$69 \$74 \$49 \$6E \$66 \$6F \$31 \$00 \$00
15765_11_ECU4_SID\$09_INF\$0D	\$00 \$00 \$07 \$EB \$49 \$0D \$01 \$00 \$00 \$00 \$00 \$56 \$41 \$4C \$44 \$45 \$53 \$4E \$39 \$38 \$37 \$36 \$35 \$34
15765_11_ECU4_SID\$09_INF\$0F	\$00 \$00 \$07 \$EB \$49 \$0F \$01 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$56 \$41 \$4C \$44 \$45 \$52 \$54 \$41 \$4E \$34
15765_11_ECU5_SID\$09_INF\$00	\$00 \$00 \$07 \$EC \$49 \$00 \$14 \$40 \$00 \$00
15765_11_ECU5_SID\$09_INF\$00_DIESEL	\$00 \$00 \$07 \$EC \$49 \$00 \$14 \$4A \$00 \$00
15765_11_ECU5_SID\$09_INF\$04_9	\$00 \$00 \$07 \$EC \$49 \$04 \$09 \$31 \$35 \$35 \$38 \$39 \$37 \$37 \$38 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$32 \$35 \$35 \$38 \$39 \$37 \$37 \$37 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$33 \$35 \$35 \$38 \$39 \$37 \$37 \$36 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$34 \$35 \$35 \$38 \$39 \$37 \$37 \$35 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$35 \$35 \$35 \$38 \$39 \$37 \$37 \$34 \$00 \$00 \$00 \$00

	\$00 \$00 \$00 \$00 \$36 \$35 \$35 \$38 \$39 \$37 \$37 \$33 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$37 \$35 \$35 \$38 \$39 \$37 \$37 \$32 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$38 \$35 \$35 \$38 \$39 \$37 \$37 \$31 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$39 \$35 \$35 \$38 \$39 \$37 \$37 \$30 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00
15765_11_ECU5_SID\$09_INF\$06_9	\$00 \$00 \$07 \$EC \$49 \$06 \$09 \$57 \$91 \$BC \$88 \$58 \$91 \$BC \$87 \$59 \$91 \$BC \$86 \$5A \$91 \$BC \$85 \$5B \$91 \$BC \$84 \$5C \$91 \$BC \$83 \$5D \$91 \$BC \$82 \$5E \$55 \$AA \$81 \$5F \$FF \$00 \$80
15765_11_ECU5_SID\$09_INF\$0A	\$00 \$00 \$07 \$EC \$49 \$0A \$01 \$45 \$43 \$43 \$32 \$2D \$45 \$6D \$69 \$73 \$43 \$72 \$69 \$74 \$49 \$6E \$66 \$6F \$32 \$00 \$00
15765_11_ECU5_SID\$09_INF\$0D	\$00 \$00 \$07 \$EC \$49 \$0D \$01 \$00 \$00 \$00 \$00 \$56 \$41 \$4C \$44 \$45 \$53 \$4E \$39 \$38 \$37 \$36 \$35 \$35
15765_11_ECU5_SID\$09_INF\$0F	\$00 \$00 \$07 \$EC \$49 \$0F \$01 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$56 \$41 \$4C \$44 \$45 \$52 \$54 \$41 \$4E \$35
15765_11_ECU6_SID\$09_INF\$00	\$00 \$00 \$07 \$ED \$49 \$00 \$14 \$40 \$00 \$00
15765_11_ECU6_SID\$09_INF\$00_DIESEL	\$00 \$00 \$07 \$ED \$49 \$00 \$14 \$4A \$00 \$00
15765_11_ECU6_SID\$09_INF\$04_9	\$00 \$00 \$07 \$ED \$49 \$04 \$09 \$31 \$36 \$35 \$38 \$39 \$37 \$37 \$38 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$32 \$36 \$35 \$38 \$39 \$37 \$37 \$37 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$33 \$36 \$35 \$38 \$39 \$37 \$37 \$36 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$34 \$36 \$35 \$38 \$39 \$37 \$37 \$35 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$35 \$36 \$35 \$38 \$39 \$37 \$37 \$34 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$36 \$36 \$35 \$38 \$39 \$37 \$37 \$33 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$37 \$36 \$35 \$38 \$39 \$37 \$37 \$32 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$38 \$36 \$35 \$38 \$39 \$37 \$37 \$31 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$39 \$36 \$35 \$38 \$39 \$37 \$37 \$30 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00
15765_11_ECU6_SID\$09_INF\$06_9	\$00 \$00 \$07 \$ED \$49 \$06 \$09 \$67 \$91 \$BC \$88 \$68 \$91 \$BC \$87 \$69 \$91 \$BC \$86 \$6A \$91 \$BC \$85 \$6B \$91 \$BC \$84 \$6C \$91 \$BC \$83 \$6D \$91 \$BC \$82 \$6E \$55 \$AA \$81 \$6F \$FF \$00 \$80
15765_11_ECU6_SID\$09_INF\$0A	\$00 \$00 \$07 \$ED \$49 \$0A \$01 \$54 \$43 \$4D \$32 \$2D \$54 \$72 \$61 \$6E \$73 \$6D \$69 \$73 \$43 \$74 \$72 \$6C \$32 \$00 \$00
15765_11_ECU6_SID\$09_INF\$0D	\$00 \$00 \$07 \$ED \$49 \$0D \$01 \$00 \$00 \$00 \$00 \$56 \$41 \$4C \$44 \$45 \$53 \$4E \$39 \$38 \$37 \$36 \$35 \$36
15765_11_ECU6_SID\$09_INF\$0F	\$00 \$00 \$07 \$ED \$49 \$0F \$01 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$56 \$41 \$4C \$44 \$45 \$52 \$54 \$41 \$4E \$36
15765_11_ECU7_SID\$09_INF\$00	\$00 \$00 \$07 \$EE \$49 \$00 \$14 \$40 \$00 \$00
15765_11_ECU7_SID\$09_INF\$00_DIESEL	\$00 \$00 \$07 \$EE \$49 \$00 \$14 \$4A \$00 \$00
15765_11_ECU7_SID\$09_INF\$04_9	\$00 \$00 \$07 \$EE \$49 \$04 \$09 \$31 \$37 \$35 \$38 \$39 \$37 \$37 \$38 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$32 \$37 \$35 \$38 \$39 \$37 \$37 \$37 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$33 \$37 \$35 \$38 \$39 \$37 \$37 \$36 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$34 \$37 \$35 \$38 \$39 \$37 \$37 \$35 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$35 \$37 \$35 \$38 \$39 \$37 \$37 \$34 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$36 \$37 \$35 \$38 \$39 \$37 \$37 \$33 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$37 \$37 \$35 \$38 \$39 \$37 \$37 \$32 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$38 \$37 \$35 \$38 \$39 \$37 \$37 \$31 \$00 \$00 \$00 \$00

	\$00 \$00 \$00 \$00 \$39 \$37 \$35 \$38 \$39 \$37 \$37 \$30 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00
15765_11_ECU7_SID\$09_INF\$06_9	\$00 \$00 \$07 \$EE \$49 \$06 \$09 \$77 \$91 \$BC \$88 \$78 \$91 \$BC \$87 \$79 \$91 \$BC \$86 \$7A \$91 \$BC \$85 \$7B \$91 \$BC \$84 \$7C \$91 \$BC \$83 \$7D \$91 \$BC \$82 \$7E \$55 \$AA \$81 \$7F \$FF \$00 \$80
15765_11_ECU7_SID\$09_INF\$0A	\$00 \$00 \$07 \$EE \$49 \$0A \$01 \$54 \$43 \$43 \$31 \$2D \$54 \$72 \$61 \$6E \$73 \$66 \$43 \$61 \$73 \$65 \$43 \$74 \$72 \$6C \$31
15765_11_ECU7_SID\$09_INF\$0D	\$00 \$00 \$07 \$EE \$49 \$0D \$01 \$00 \$00 \$00 \$00 \$56 \$41 \$4C \$44 \$45 \$53 \$4E \$39 \$38 \$37 \$36 \$35 \$37
15765_11_ECU7_SID\$09_INF\$0F	\$00 \$00 \$07 \$EE \$49 \$0F \$01 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$56 \$41 \$4C \$44 \$45 \$52 \$54 \$41 \$4E \$37
15765_11_ECU8_SID\$09_INF\$00	\$00 \$00 \$07 \$EF \$49 \$00 \$14 \$40 \$00 \$00
15765_11_ECU8_SID\$09_INF\$00_DIESEL	\$00 \$00 \$07 \$EF \$49 \$00 \$14 \$4A \$00 \$00
15765_11_ECU8_SID\$09_INF\$04_9	\$00 \$00 \$07 \$EF \$49 \$04 \$09 \$31 \$38 \$35 \$38 \$39 \$37 \$37 \$38 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$32 \$38 \$35 \$38 \$39 \$37 \$37 \$37 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$33 \$38 \$35 \$38 \$39 \$37 \$37 \$36 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$34 \$38 \$35 \$38 \$39 \$37 \$37 \$35 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$35 \$38 \$35 \$38 \$39 \$37 \$37 \$34 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$36 \$38 \$35 \$38 \$39 \$37 \$37 \$33 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$37 \$38 \$35 \$38 \$39 \$37 \$37 \$32 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$38 \$38 \$35 \$38 \$39 \$37 \$37 \$31 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$39 \$38 \$35 \$38 \$39 \$37 \$37 \$30 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00
15765_11_ECU8_SID\$09_INF\$06_9	\$00 \$00 \$07 \$EF \$49 \$06 \$09 \$87 \$91 \$BC \$88 \$88 \$91 \$BC \$87 \$89 \$91 \$BC \$86 \$8A \$91 \$BC \$85 \$8B \$91 \$BC \$84 \$8C \$91 \$BC \$83 \$8D \$91 \$BC \$82 \$8E \$55 \$AA \$81 \$8F \$FF \$00 \$80
15765_11_ECU7_SID\$09_INF\$0A	\$00 \$00 \$07 \$EF \$49 \$0A \$01 \$54 \$43 \$43 \$32 \$2D \$54 \$72 \$61 \$6E \$73 \$66 \$43 \$61 \$73 \$65 \$43 \$74 \$72 \$6C \$32
15765_11_ECU8_SID\$09_INF\$0D	\$00 \$00 \$07 \$EF \$49 \$0D \$01 \$00 \$00 \$00 \$00 \$56 \$41 \$4C \$44 \$45 \$53 \$4E \$39 \$38 \$37 \$36 \$35 \$38
15765_11_ECU8_SID\$09_INF\$0F	\$00 \$00 \$07 \$EF \$49 \$0F \$01 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$56 \$41 \$4C \$44 \$45 \$52 \$54 \$41 \$4E \$38
15765_11_SID\$0A	\$00 \$00 \$07 \$DF \$0A
15765_11_ECU1_SID\$0A_NoDTC	\$00 \$00 \$07 \$E8 \$4A \$00
15765_11_ECU1_SID\$0A_DTC	\$00 \$00 \$07 \$E8 \$4A \$04 \$01 \$07 \$01 \$10 \$01 \$15 \$01 \$24
15765_11_ECU2_SID\$0A_NoDTC	\$00 \$00 \$07 \$E9 \$4A \$00

The first four bytes are the CAN ID bits 28-24 first, followed by CAN ID bits 23-16, CAN ID bits 15-8, CAN ID bits 7-0.

**Figure 7 - Pre-defined messages for ISO 15765-4 (11-bit)**

The following messages have been pre-defined for 29-bit ISO 15765-4:

Message Reference	Message Bytes
15765 29 SID\$01 PID\$00	\$18 \$DB \$33 \$F1 \$01 \$00
15765_29_SID\$01_PID\$00-\$E0	\$18 \$DB \$33 \$F1 \$01 \$00 \$20 \$40 \$60 \$80 \$A0 \$C0 \$E0
15765 29 SID\$01 PID\$00	\$18 \$DB \$33 \$F1 \$01 \$00
15765 29 SID\$01 PID\$01	\$18 \$DB \$33 \$F1 \$01 \$01
15765 29 SID\$01 PID\$0C	\$18 \$DB \$33 \$F1 \$01 \$0C
15765 29 SID\$01 PID\$20	\$18 \$DB \$33 \$F1 \$01 \$20
15765 29 SID\$01 PID\$40	\$18 \$DB \$33 \$F1 \$01 \$40
15765 29 SID\$01 PID\$41	\$18 \$DB \$33 \$F1 \$01 \$41
15765 29 SID\$01 PID\$01-\$0C-\$41	\$18 \$DB \$33 \$F1 \$01 \$01 \$0C \$41
15765 29 ECU1 SID\$01 PID\$00	\$18 \$DA \$F1 \$10 \$41 \$00 \$80 \$10 \$00 \$00
15765 29 ECU1 SID\$01 PID\$00 20	\$18 \$DA \$F1 \$10 \$41 \$00 \$80 \$10 \$00 \$01
15765_29_ECU1_SID\$01_PID\$00-\$0E	\$18 \$DA \$F1 \$10 \$41 \$00 \$80 \$00 \$00 \$00 \$20 \$00 \$00 \$00 \$01 \$40 \$10 \$00 \$00 \$00
15765 29 ECU1 SID\$01 PID\$01	\$18 \$DA \$F1 \$10 \$41 \$01 \$00 \$07 \$EF \$EF
15765 29 ECU1 SID\$01 PID\$01 SPARK	\$18 \$DA \$F1 \$10 \$41 \$01 \$00 \$07 \$EF \$EF
15765 29 ECU1 SID\$01 PID\$01 COMP	\$18 \$DA \$F1 \$10 \$41 \$01 \$00 \$0F \$EB \$EB
15765 29 ECU1 SID\$01 PID\$0C KOEO	\$18 \$DA \$F1 \$10 \$41 \$0C \$00 \$00
15765 29 ECU1 SID\$01 PID\$20	\$18 \$DA \$F1 \$10 \$41 \$20 \$00 \$00 \$00 \$01
15765 29 ECU1 SID\$01 PID\$40	\$18 \$DA \$F1 \$10 \$41 \$40 \$10 \$00 \$00 \$00
15765 29 ECU1 SID\$01 PID\$41 SPARK	\$18 \$DA \$F1 \$10 \$41 \$41 \$00 \$07 \$EF \$EF
15765_29_ECU1_SID\$01_PID\$01-\$0C-\$41	\$18 \$DA \$F1 \$10 \$41 \$01 \$00 \$07 \$EF \$EF \$0C \$00 \$00 \$41 \$00 \$07 \$EF \$EF
15765 29 ECU1 SID\$01 NRC\$21	\$18 \$DA \$F1 \$10 \$7F \$01 \$21
15765 29 ECU2 SID\$01 PID\$00	\$18 \$DA \$F1 \$11 \$41 \$00 \$80 \$00 \$00 \$00
15765 29 ECU2 SID\$01 PID\$01 SPARK	\$18 \$DA \$F1 \$11 \$41 \$01 \$81 \$44 \$00 \$00
15765 29 ECU2 SID\$01 PID\$01 COMP	\$18 \$DA \$F1 \$11 \$41 \$01 \$81 \$4C \$00 \$00
15765 29 ECU2 SID\$01 PID\$0C KOEO	\$18 \$DA \$F1 \$11 \$41 \$0C \$00 \$00
15765 29 ECU3 SID\$01 PID\$00	\$18 \$DA \$F1 \$12 \$41 \$00 \$00 \$00 \$00 \$00
15765 29 ECU4 SID\$01 PID\$00	\$18 \$DA \$F1 \$13 \$41 \$00 \$00 \$00 \$00 \$00
15765 29 ECU5 SID\$01 PID\$00	\$18 \$DA \$F1 \$14 \$41 \$00 \$00 \$00 \$00 \$00
15765 29 ECU6 SID\$01 PID\$00	\$18 \$DA \$F1 \$15 \$41 \$00 \$00 \$00 \$00 \$00
15765 29 ECU7 SID\$01 PID\$00	\$18 \$DA \$F1 \$16 \$41 \$00 \$00 \$00 \$00 \$00
15765 29 ECU8 SID\$01 PID\$00	\$18 \$DA \$F1 \$17 \$41 \$00 \$00 \$00 \$00 \$00
15765 29 SID\$02 PID\$02	\$18 \$DB \$33 \$F1 \$02 \$02 \$00
15765 29 ECU1 SID\$02 PID\$02 NoDTC	\$18 \$DA \$F1 \$10 \$42 \$02 \$00 \$00 \$00
15765 29 ECU2 SID\$02 PID\$02 NoDTC	\$18 \$DA \$F1 \$11 \$42 \$02 \$00 \$00 \$00
15765 29 ECU1 SID\$02 PID\$02 DTC	\$18 \$DA \$F1 \$10 \$42 \$02 \$00 \$01 \$07
15765 29 SID\$03	\$18 \$DB \$33 \$F1 \$03
15765 29 ECU1 SID\$03 NoDTC	\$18 \$DA \$F1 \$10 \$43 \$00
15765_29_ECU1_SID\$03_DTC	\$18 \$DA \$F1 \$10 \$43 \$04 \$01 \$07 \$01 \$10 \$01 \$15 \$01 \$24
15765 29 ECU2 SID\$03 NoDTC	\$18 \$DA \$F1 \$11 \$43 \$00
15765 29 SID\$04	\$18 \$DB \$33 \$F1 \$04
15765 29 ECU1 SID\$04 DONE	\$18 \$DA \$F1 \$10 \$44
15765 29 ECU1 SID\$04 NRC22	\$18 \$DA \$F1 \$10 \$7F \$44 \$22
15765 29 ECU1 SID\$04 NRC78	\$18 \$DA \$F1 \$10 \$7F \$44 \$78
15765 29 ECU2 SID\$04 DONE	\$18 \$DA \$F1 \$11 \$44
15765 29 ECU2 SID\$04 NRC22	\$18 \$DA \$F1 \$11 \$7F \$44 \$22
15765 29 SID\$06 MID\$00	\$18 \$DB \$33 \$F1 \$06 \$00
15765 29 ECU1 SID\$06 MID\$00	\$18 \$DA \$F1 \$10 \$46 \$00 \$CC \$00 \$00 \$00
15765 29 SID\$07	\$18 \$DB \$33 \$F1 \$07
15765 29 ECU1 SID\$07 NoDTC	\$18 \$DA \$F1 \$10 \$47 \$00
15765_29_ECU1_SID\$07_DTC	\$18 \$DA \$F1 \$10 \$47 \$04 \$01 \$07 \$01 \$10 \$01 \$15 \$01 \$24

15765 29 ECU2 SID\$07 NoDTC	\$18 \$DA \$F1 \$11 \$47 \$00
15765 29 SID\$08 TID\$00	\$18 \$DB \$33 \$F1 \$08
15765 29 ECU1 SID\$08 TID\$00	\$18 \$DA \$F1 \$10 \$48 \$00 \$80 \$00 \$00 \$00
15765 29 SID\$09 INF\$00	\$18 \$DB \$33 \$F1 \$09 \$00
15765 29 SID\$09 INF\$02	\$18 \$DB \$33 \$F1 \$09 \$02
15765 29 SID\$09 INF\$04	\$18 \$DB \$33 \$F1 \$09 \$04
15765 29 SID\$09 INF\$06	\$18 \$DB \$33 \$F1 \$09 \$06
15765 29 SID\$09 INF\$08	\$18 \$DB \$33 \$F1 \$09 \$08
15765 29 SID\$09 INF\$0A	\$18 \$DB \$33 \$F1 \$09 \$0A
15765 29 SID\$09 INF\$0B	\$18 \$DB \$33 \$F1 \$09 \$0B
15765 29 SID\$09 INF\$0D	\$18 \$DB \$33 \$F1 \$09 \$0D
15765 29 SID\$09 INF\$0F	\$18 \$DB \$33 \$F1 \$09 \$0F
15765 29 ECU1 SID\$09 INF\$00	\$18 \$DA \$F1 \$10 \$49 \$00 \$55 \$40 \$00 \$00
15765 29 ECU1 SID\$09 INF\$00 DIESEL	\$18 \$DA \$F1 \$10 \$49 \$00 \$54 \$6A \$00 \$00
15765_29_ECU1_SID\$09_INF\$02	\$18 \$DA \$F1 \$10 \$49 \$02 \$01 \$31 \$47 \$59 \$44 \$45 \$36 \$33 \$41 \$37 \$41 \$30 \$30 \$30 \$30 \$30
15765_29_ECU1_SID\$09_INF\$04	\$18 \$DA \$F1 \$10 \$49 \$04 \$01 \$31 \$32 \$35 \$38 \$39 \$37 \$37 \$32 \$00 \$00 \$00 \$00 \$00 \$00
15765_29_ECU1_SID\$09_INF\$04_9	\$18 \$DA \$F1 \$10 \$49 \$04 \$09 \$31 \$31 \$35 \$38 \$39 \$37 \$37 \$38 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$32 \$31 \$35 \$38 \$39 \$37 \$37 \$37 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$33 \$31 \$35 \$38 \$39 \$37 \$37 \$36 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$34 \$31 \$35 \$38 \$39 \$37 \$37 \$35 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$35 \$31 \$35 \$38 \$39 \$37 \$37 \$34 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$36 \$31 \$35 \$38 \$39 \$37 \$37 \$33 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$37 \$31 \$35 \$38 \$39 \$37 \$37 \$32 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$38 \$31 \$35 \$38 \$39 \$37 \$37 \$31 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$39 \$31 \$35 \$38 \$39 \$37 \$37 \$30 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00
15765 29 ECU1 SID\$09 INF\$06	\$18 \$DA \$F1 \$10 \$49 \$06 \$01 \$17 \$91 \$BC \$88
15765_29_ECU1_SID\$09_INF\$06_9	\$18 \$DA \$F1 \$10 \$49 \$06 \$09 \$17 \$91 \$BC \$88 \$18 \$91 \$BC \$87 \$19 \$91 \$BC \$86 \$1A \$91 \$BC \$85 \$1B \$91 \$BC \$84 \$1C \$91 \$BC \$83 \$1D \$91 \$BC \$82 \$1E \$55 \$AA \$81 \$1F \$FF \$00 \$80
15765_29_ECU1_SID\$09_INF\$08_32	\$18 \$DA \$F1 \$10 \$49 \$08 \$10 \$00 \$64 \$00 \$69 \$00 \$0A \$00 \$0F \$00 \$00 \$00 \$00 \$00 \$1E \$00 \$23 \$00 \$00 \$00 \$00 \$00 \$32 \$00 \$37 \$00 \$3C \$00 \$41 \$00 \$46 \$00 \$4B
15765_29_ECU1_SID\$09_INF\$08_40	\$18 \$DA \$F1 \$10 \$49 \$08 \$14 \$00 \$64 \$00 \$69 \$00 \$0A \$00 \$0F \$00 \$00 \$00 \$00 \$00 \$1E \$00 \$23 \$00 \$00 \$00 \$00 \$00 \$32 \$00 \$37 \$00 \$3C \$00 \$41 \$00 \$46 \$00 \$4B \$00 \$5A \$00 \$5F \$00 \$00 \$00 \$00
15765_29_ECU1_SID\$09_INF\$0A	\$18 \$DA \$F1 \$10 \$49 \$0A \$01 \$45 \$43 \$4D \$31 \$2D \$45 \$6E \$67 \$69 \$6E \$65 \$43 \$6F \$6E \$74 \$72 \$6F \$6C \$31 \$00
15765_29_ECU1_SID\$09_INF\$0B_32	\$18 \$DA \$F1 \$10 \$49 \$0B \$10 \$00 \$64 \$00 \$69 \$00 \$0A \$00 \$0F \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$32 \$00 \$37 \$00 \$3C \$00 \$41 \$00 \$46 \$00 \$4B
15765_29_ECU1_SID\$09_INF\$0B_36	\$18 \$DA \$F1 \$10 \$49 \$0B \$12 \$00 \$64 \$00 \$69 \$00 \$0A \$00 \$0F \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$32 \$00 \$37 \$00 \$3C \$00 \$41 \$00 \$46 \$00 \$4B \$00 \$50 \$00 \$55
15765_29_ECU1_SID\$09_INF\$0D	\$18 \$DA \$F1 \$10 \$49 \$0D \$01 \$00 \$00 \$00 \$00 \$56 \$41 \$4C \$44 \$45 \$53 \$4E \$39 \$38 \$37 \$36 \$35 \$31

15765_29_ECU1_SID\$09_INF\$0F	\$18 SDA \$F1 \$10 \$49 \$0F \$01 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$56 \$41 \$4C \$44 \$45 \$52 \$54 \$41 \$4E \$31
15765_29_ECU1_SID\$09_NRC78	\$18 SDA \$F1 \$10 \$7F \$49 \$78
15765_29_ECU2_SID\$09_INF\$00	\$18 SDA \$F1 \$11 \$49 \$00 \$15 \$40 \$00 \$00
15765_29_ECU2_SID\$09_INF\$00_DIESEL	\$18 SDA \$F1 \$11 \$49 \$00 \$14 \$6A \$00 \$00
15765_29_ECU2_SID\$09_INF\$04	\$18 SDA \$F1 \$11 \$49 \$04 \$01 \$32 \$34 \$32 \$32 \$31 \$34 \$31 \$39 \$00 \$00 \$00 \$00 \$00 \$00 \$00
15765_29_ECU2_SID\$09_INF\$04_9	\$18 SDA \$F1 \$11 \$49 \$04 \$09 \$31 \$32 \$35 \$38 \$39 \$37 \$37 \$38 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$32 \$32 \$35 \$38 \$39 \$37 \$37 \$37 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$33 \$32 \$35 \$38 \$39 \$37 \$37 \$36 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$34 \$32 \$35 \$38 \$39 \$37 \$37 \$35 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$35 \$32 \$35 \$38 \$39 \$37 \$37 \$34 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$36 \$32 \$35 \$38 \$39 \$37 \$37 \$33 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$37 \$32 \$35 \$38 \$39 \$37 \$37 \$32 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$38 \$32 \$35 \$38 \$39 \$37 \$37 \$31 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$39 \$32 \$35 \$38 \$39 \$37 \$37 \$30 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00
15765_29_ECU2_SID\$09_INF\$06	\$18 SDA \$F1 \$11 \$49 \$06 \$02 \$27 \$91 \$BC \$88
15765_29_ECU2_SID\$09_INF\$06_9	\$18 SDA \$F1 \$11 \$49 \$06 \$09 \$27 \$91 \$BC \$88 \$28 \$91 \$BC \$87 \$29 \$91 \$BC \$86 \$2A \$91 \$BC \$85 \$2B \$91 \$BC \$84 \$2C \$91 \$BC \$83 \$2D \$91 \$BC \$82 \$2E \$55 \$AA \$81 \$2F \$FF \$00 \$80
15765_29_ECU2_SID\$09_INF\$08_32	\$18 SDA \$F1 \$11 \$49 \$08 \$10 \$00 \$64 \$00 \$69 \$00 \$00 \$00 \$00 \$00 \$14 \$00 \$19 \$00 \$00 \$00 \$00 \$00 \$28 \$00 \$2D \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00
15765_29_ECU2_SID\$09_INF\$08_40	\$18 SDA \$F1 \$11 \$49 \$08 \$14 \$00 \$64 \$00 \$69 \$00 \$00 \$00 \$00 \$00 \$14 \$00 \$19 \$00 \$00 \$00 \$00 \$00 \$28 \$00 \$2D \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$5A \$00 \$5F
15765_29_ECU2_SID\$09_INF\$0A	\$18 SDA \$F1 \$11 \$49 \$0A \$01 \$45 \$43 \$4D \$32 \$2D \$45 \$6E \$67 \$69 \$6E \$65 \$43 \$6F \$6E \$74 \$72 \$6F \$6C \$32 \$00
15765_29_ECU2_SID\$09_INF\$0B_32	\$18 SDA \$F1 \$11 \$49 \$0B \$10 \$00 \$64 \$00 \$69 \$00 \$00 \$00 \$00 \$00 \$14 \$00 \$19 \$00 \$00 \$00 \$00 \$00 \$28 \$00 \$2D \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00
15765_29_ECU2_SID\$09_INF\$0B_36	\$18 SDA \$F1 \$11 \$49 \$0B \$12 \$00 \$64 \$00 \$69 \$00 \$00 \$00 \$00 \$00 \$14 \$00 \$19 \$00 \$00 \$00 \$00 \$00 \$28 \$00 \$2D \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00
15765_29_ECU2_SID\$09_INF\$0D	\$18 SDA \$F1 \$11 \$49 \$0D \$01 \$00 \$00 \$00 \$00 \$56 \$41 \$4C \$44 \$45 \$53 \$4E \$39 \$38 \$37 \$36 \$35 \$32
15765_29_ECU2_SID\$09_INF\$0F	\$18 SDA \$F1 \$11 \$49 \$0F \$01 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$56 \$41 \$4C \$44 \$45 \$52 \$54 \$41 \$4E \$32
15765_29_ECU3_SID\$09_INF\$00	\$18 SDA \$F1 \$12 \$49 \$00 \$14 \$40 \$00 \$00
15765_29_ECU3_SID\$09_INF\$00_DIESEL	\$18 SDA \$F1 \$12 \$49 \$00 \$14 \$4A \$00 \$00
15765_29_ECU3_SID\$09_INF\$04_9	\$18 SDA \$F1 \$12 \$49 \$04 \$09 \$31 \$33 \$35 \$38 \$39 \$37 \$37 \$38 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$32 \$33 \$35 \$38 \$39 \$37 \$37 \$37 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$33 \$33 \$35 \$38 \$39 \$37 \$37 \$36 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$34 \$33 \$35 \$38 \$39 \$37 \$37 \$35 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$35 \$33 \$35 \$38 \$39 \$37 \$37 \$34 \$00 \$00 \$00 \$00

	\$00 \$00 \$00 \$00 \$36 \$33 \$35 \$38 \$39 \$37 \$37 \$33 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$37 \$33 \$35 \$38 \$39 \$37 \$37 \$32 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$38 \$33 \$35 \$38 \$39 \$37 \$37 \$31 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$39 \$33 \$35 \$38 \$39 \$37 \$37 \$30 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00
15765_29_ECU3_SID\$09_INF\$06_9	\$18 \$DA \$F1 \$12 \$49 \$06 \$09 \$37 \$91 \$BC \$88 \$38 \$91 \$BC \$87 \$39 \$91 \$BC \$86 \$3A \$91 \$BC \$85 \$3B \$91 \$BC \$84 \$3C \$91 \$BC \$83 \$3D \$91 \$BC \$82 \$3E \$55 \$AA \$81 \$3F \$FF \$00 \$80
15765_29_ECU3_SID\$09_INF\$0A	\$18 \$DA \$F1 \$12 \$49 \$0A \$01 \$54 \$43 \$4D \$31 \$2D \$54 \$72 \$61 \$6E \$73 \$6D \$69 \$73 \$43 \$74 \$72 \$6C \$31 \$00 \$00
15765_29_ECU3_SID\$09_INF\$0D	\$18 \$DA \$F1 \$12 \$49 \$0D \$01 \$00 \$00 \$00 \$00 \$56 \$41 \$4C \$44 \$45 \$53 \$4E \$39 \$38 \$37 \$36 \$35 \$33
15765_29_ECU3_SID\$09_INF\$0F	\$18 \$DA \$F1 \$12 \$49 \$0F \$01 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$56 \$41 \$4C \$44 \$45 \$52 \$54 \$41 \$4E \$33
15765_29_ECU4_SID\$09_INF\$00	\$18 \$DA \$F1 \$13 \$49 \$00 \$14 \$40 \$00 \$00
15765_29_ECU4_SID\$09_INF\$00_DIESEL	\$18 \$DA \$F1 \$13 \$49 \$00 \$14 \$4A \$00 \$00
15765_29_ECU4_SID\$09_INF\$04_9	\$18 \$DA \$F1 \$13 \$49 \$04 \$09 \$31 \$34 \$35 \$38 \$39 \$37 \$37 \$38 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$32 \$34 \$35 \$38 \$39 \$37 \$37 \$37 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$33 \$34 \$35 \$38 \$39 \$37 \$37 \$36 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$34 \$34 \$35 \$38 \$39 \$37 \$37 \$35 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$35 \$34 \$35 \$38 \$39 \$37 \$37 \$34 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$36 \$34 \$35 \$38 \$39 \$37 \$37 \$33 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$37 \$34 \$35 \$38 \$39 \$37 \$37 \$32 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$38 \$34 \$35 \$38 \$39 \$37 \$37 \$31 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$39 \$34 \$35 \$38 \$39 \$37 \$37 \$30 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00
15765_29_ECU4_SID\$09_INF\$06_9	\$18 \$DA \$F1 \$13 \$49 \$06 \$09 \$47 \$91 \$BC \$88 \$48 \$91 \$BC \$87 \$49 \$91 \$BC \$86 \$4A \$91 \$BC \$85 \$4B \$91 \$BC \$84 \$4C \$91 \$BC \$83 \$4D \$91 \$BC \$82 \$4E \$55 \$AA \$81 \$4F \$FF \$00 \$80
15765_29_ECU4_SID\$09_INF\$0A	\$18 \$DA \$F1 \$13 \$49 \$0A \$01 \$45 \$43 \$43 \$31 \$2D \$45 \$6D \$69 \$73 \$43 \$72 \$69 \$74 \$49 \$6E \$66 \$6F \$31 \$00 \$00
15765_29_ECU4_SID\$09_INF\$0D	\$18 \$DA \$F1 \$13 \$49 \$0D \$01 \$00 \$00 \$00 \$00 \$56 \$41 \$4C \$44 \$45 \$53 \$4E \$39 \$38 \$37 \$36 \$35 \$34
15765_29_ECU4_SID\$09_INF\$0F	\$18 \$DA \$F1 \$13 \$49 \$0F \$01 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$56 \$41 \$4C \$44 \$45 \$52 \$54 \$41 \$4E \$34
15765_29_ECU5_SID\$09_INF\$00	\$18 \$DA \$F1 \$14 \$49 \$00 \$14 \$40 \$00 \$00
15765_29_ECU5_SID\$09_INF\$00_DIESEL	\$18 \$DA \$F1 \$14 \$49 \$00 \$14 \$4A \$00 \$00
15765_29_ECU5_SID\$09_INF\$04_9	\$18 \$DA \$F1 \$14 \$49 \$04 \$09 \$31 \$35 \$35 \$38 \$39 \$37 \$37 \$38 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$32 \$35 \$35 \$38 \$39 \$37 \$37 \$37 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$33 \$35 \$35 \$38 \$39 \$37 \$37 \$36 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$34 \$35 \$35 \$38 \$39 \$37 \$37 \$35 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$35 \$35 \$35 \$38 \$39 \$37 \$37 \$34 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$36 \$35 \$35 \$38 \$39 \$37 \$37 \$33 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$37 \$35 \$35 \$38 \$39 \$37 \$37 \$32 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$38 \$35 \$35 \$38 \$39 \$37 \$37 \$31 \$00 \$00 \$00 \$00

	\$00 \$00 \$00 \$00 \$39 \$35 \$35 \$38 \$39 \$37 \$37 \$30 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00
15765_29_ECU5_SID\$09_INF\$06_9	\$18 \$DA \$F1 \$14 \$49 \$06 \$09 \$57 \$91 \$BC \$88 \$58 \$91 \$BC \$87 \$59 \$91 \$BC \$86 \$5A \$91 \$BC \$85 \$5B \$91 \$BC \$84 \$5C \$91 \$BC \$83 \$5D \$91 \$BC \$82 \$5E \$55 \$AA \$81 \$5F \$FF \$00 \$80
15765_29_ECU5_SID\$09_INF\$0A	\$18 \$DA \$F1 \$14 \$49 \$0A \$01 \$45 \$43 \$43 \$32 \$2D \$45 \$6D \$69 \$73 \$43 \$72 \$69 \$74 \$49 \$6E \$66 \$6F \$32 \$00 \$00
15765_29_ECU5_SID\$09_INF\$0D	\$18 \$DA \$F1 \$14 \$49 \$0D \$01 \$00 \$00 \$00 \$00 \$56 \$41 \$4C \$44 \$45 \$53 \$4E \$39 \$38 \$37 \$36 \$35 \$35
15765_29_ECU5_SID\$09_INF\$0F	\$18 \$DA \$F1 \$14 \$49 \$0F \$01 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$56 \$41 \$4C \$44 \$45 \$52 \$54 \$41 \$4E \$35
15765_29_ECU6_SID\$09_INF\$00	\$18 \$DA \$F1 \$15 \$49 \$00 \$14 \$40 \$00 \$00
15765_29_ECU6_SID\$09_INF\$00_DIESEL	\$18 \$DA \$F1 \$15 \$49 \$00 \$14 \$4A \$00 \$00
15765_29_ECU6_SID\$09_INF\$04_9	\$18 \$DA \$F1 \$15 \$49 \$04 \$09 \$31 \$36 \$35 \$38 \$39 \$37 \$37 \$38 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$32 \$36 \$35 \$38 \$39 \$37 \$37 \$37 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$33 \$36 \$35 \$38 \$39 \$37 \$37 \$36 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$34 \$36 \$35 \$38 \$39 \$37 \$37 \$35 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$35 \$36 \$35 \$38 \$39 \$37 \$37 \$34 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$36 \$36 \$35 \$38 \$39 \$37 \$37 \$33 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$37 \$36 \$35 \$38 \$39 \$37 \$37 \$32 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$38 \$36 \$35 \$38 \$39 \$37 \$37 \$31 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$39 \$36 \$35 \$38 \$39 \$37 \$37 \$30 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00
15765_29_ECU6_SID\$09_INF\$06_9	\$18 \$DA \$F1 \$15 \$49 \$06 \$09 \$67 \$91 \$BC \$88 \$68 \$91 \$BC \$87 \$69 \$91 \$BC \$86 \$6A \$91 \$BC \$85 \$6B \$91 \$BC \$84 \$6C \$91 \$BC \$83 \$6D \$91 \$BC \$82 \$6E \$55 \$AA \$81 \$6F \$FF \$00 \$80
15765_29_ECU6_SID\$09_INF\$0A	\$18 \$DA \$F1 \$15 \$49 \$0A \$01 \$54 \$43 \$4D \$32 \$2D \$54 \$72 \$61 \$6E \$73 \$6D \$69 \$73 \$43 \$74 \$72 \$6C \$32 \$00 \$00
15765_29_ECU6_SID\$09_INF\$0D	\$18 \$DA \$F1 \$15 \$49 \$0D \$01 \$00 \$00 \$00 \$00 \$56 \$41 \$4C \$44 \$45 \$53 \$4E \$39 \$38 \$37 \$36 \$35 \$36
15765_29_ECU6_SID\$09_INF\$0F	\$18 \$DA \$F1 \$15 \$49 \$0F \$01 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$56 \$41 \$4C \$44 \$45 \$52 \$54 \$41 \$4E \$36
15765_29_ECU7_SID\$09_INF\$00	\$18 \$DA \$F1 \$16 \$49 \$00 \$14 \$40 \$00 \$00
15765_29_ECU7_SID\$09_INF\$00_DIESEL	\$18 \$DA \$F1 \$16 \$49 \$00 \$14 \$4A \$00 \$00
15765_29_ECU7_SID\$09_INF\$04_9	\$18 \$DA \$F1 \$16 \$49 \$04 \$09 \$31 \$37 \$35 \$38 \$39 \$37 \$37 \$38 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$32 \$37 \$35 \$38 \$39 \$37 \$37 \$37 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$33 \$37 \$35 \$38 \$39 \$37 \$37 \$36 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$34 \$37 \$35 \$38 \$39 \$37 \$37 \$35 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$35 \$37 \$35 \$38 \$39 \$37 \$37 \$34 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$36 \$37 \$35 \$38 \$39 \$37 \$37 \$33 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$37 \$37 \$35 \$38 \$39 \$37 \$37 \$32 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$38 \$37 \$35 \$38 \$39 \$37 \$37 \$31 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$39 \$37 \$35 \$38 \$39 \$37 \$37 \$30 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00
15765_29_ECU7_SID\$09_INF\$06_9	\$18 \$DA \$F1 \$16 \$49 \$06 \$09 \$77 \$91 \$BC \$88 \$78 \$91 \$BC \$87 \$79 \$91 \$BC \$86 \$7A \$91 \$BC

	\$85 \$7B \$91 \$BC \$84 \$7C \$91 \$BC \$83 \$7D \$91 \$BC \$82 \$7E \$55 \$AA \$81 \$7F \$FF \$00 \$80
15765_29_ECU7_SID\$09_INF\$0A	\$18 \$DA \$F1 \$16 \$49 \$0A \$01 \$54 \$43 \$43 \$31 \$2D \$54 \$72 \$61 \$6E \$73 \$66 \$43 \$61 \$73 \$65 \$43 \$74 \$72 \$6C \$31
15765_29_ECU7_SID\$09_INF\$0D	\$18 \$DA \$F1 \$16 \$49 \$0D \$01 \$00 \$00 \$00 \$00 \$56 \$41 \$4C \$44 \$45 \$53 \$4E \$39 \$38 \$37 \$36 \$35 \$37
15765_29_ECU7_SID\$09_INF\$0F	\$18 \$DA \$F1 \$16 \$49 \$0F \$01 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$56 \$41 \$4C \$44 \$45 \$52 \$54 \$41 \$4E \$37
15765_29_ECU8_SID\$09_INF\$00	\$18 \$DA \$F1 \$17 \$49 \$00 \$14 \$40 \$00 \$00
15765_29_ECU8_SID\$09_INF\$00_DIESEL	\$18 \$DA \$F1 \$17 \$49 \$00 \$14 \$4A \$00 \$00
15765_29_ECU8_SID\$09_INF\$04_9	\$18 \$DA \$F1 \$17 \$49 \$04 \$09 \$31 \$38 \$35 \$38 \$39 \$37 \$37 \$38 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$32 \$38 \$35 \$38 \$39 \$37 \$37 \$37 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$33 \$38 \$35 \$38 \$39 \$37 \$37 \$36 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$34 \$38 \$35 \$38 \$39 \$37 \$37 \$35 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$35 \$38 \$35 \$38 \$39 \$37 \$37 \$34 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$36 \$38 \$35 \$38 \$39 \$37 \$37 \$33 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$37 \$38 \$35 \$38 \$39 \$37 \$37 \$32 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$38 \$38 \$35 \$38 \$39 \$37 \$37 \$31 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$39 \$38 \$35 \$38 \$39 \$37 \$37 \$30 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$00
15765_29_ECU8_SID\$09_INF\$06_9	\$18 \$DA \$F1 \$17 \$49 \$06 \$09 \$87 \$91 \$BC \$88 \$88 \$91 \$BC \$87 \$89 \$91 \$BC \$86 \$8A \$91 \$BC \$85 \$8B \$91 \$BC \$84 \$8C \$91 \$BC \$83 \$8D \$91 \$BC \$82 \$8E \$55 \$AA \$81 \$8F \$FF \$00 \$80
15765_29_ECU7_SID\$09_INF\$0A	\$18 \$DA \$F1 \$17 \$49 \$0A \$01 \$54 \$43 \$43 \$32 \$2D \$54 \$72 \$61 \$6E \$73 \$66 \$43 \$61 \$73 \$65 \$43 \$74 \$72 \$6C \$32
15765_29_ECU8_SID\$09_INF\$0D	\$18 \$DA \$F1 \$17 \$49 \$0D \$01 \$00 \$00 \$00 \$00 \$56 \$41 \$4C \$44 \$45 \$53 \$4E \$39 \$38 \$37 \$36 \$35 \$38
15765_29_ECU8_SID\$09_INF\$0F	\$18 \$DA \$F1 \$17 \$49 \$0F \$01 \$00 \$00 \$00 \$00 \$00 \$00 \$00 \$56 \$41 \$4C \$44 \$45 \$52 \$54 \$41 \$4E \$38
15765_29_SID\$0A	\$18 \$DB \$33 \$F1 \$0A
15765_29_ECU1_SID\$0A_NoDTC	\$18 \$DA \$F1 \$10 \$4A \$00
15765_29_ECU1_SID\$0A_DTC	\$18 \$DA \$F1 \$10 \$4A \$04 \$01 \$07 \$01 \$10 \$01 \$15 \$01 \$24
15765_29_ECU2_SID\$0A_NoDTC	\$18 \$DA \$F1 \$11 \$4A \$00

The first four bytes are the CAN ID bits 28-24 first, followed by CAN ID bits 23-16, CAN ID bits 15-8, CAN ID bits 7-0.

**Figure 8 - Pre-defined messages for ISO 15765-4 (29-bit)**

## 5.4.3.4 ISO 15765-4 Message Timing

For convenience, the test cases in this document will use the text in the 'Parameter Reference' column when referring to the specific value, detailed in the 'Value' column of the table below. Unless otherwise specified, the following timing values shall be used:

Parameter Reference	Value
P2_MIN	0 ms
P2	5 ms
P2_MAX	50 ms
P2_MIN_78	0 ms
P2_78	2500 ms
P2_MAX_78	5000 ms

**Figure 9 - Pre-defined message timing for ISO 15765-4**

## 5.4.3.5 ISO 15765-4 (11-bit, 500K) Monitor State

The device shall initially be in the 'Power On' state, connected to the CAN network with the appropriate termination (as depicted in 5.4.3.1), and configured for 11-bit CAN Identifiers at a baud rate of 500000. The device shall not generate any In-Frame Acknowledgements on the CAN network, but shall interpret messages according to the ISO 15765-2 format (that is, verify and remove the PCI byte).

## 5.4.3.6 ISO 15765-4 (11-bit, 250K) Connected State

The device shall initially be in the 'Power On' state, connected to the CAN network with the appropriate termination (as depicted in 5.4.3.1), and configured for 11-bit CAN Identifiers at a baud rate of 250000.

## 5.4.3.7 ISO 15765-4 (11-bit, 500K) Connected State

The device shall initially be in the 'Power On' state, connected to the CAN network with the appropriate termination (as depicted in 5.4.3.1), and configured for 11-bit CAN Identifiers at a baud rate of 500000.

## 5.4.3.8 ISO 15765-4 (29-bit, 500K) Connected State

The device shall initially be in the 'Power On' state, connected to the CAN network with the appropriate termination (as depicted in 5.4.3.1), and configured for 29-bit CAN Identifiers at a baud rate of 500000.

## 5.4.3.9 ISO 15765-4 (11-bit, 500K, 2 ECUs) Initialized State

The OBD network shall have determined to be ISO 15765-4 with 11-bit CAN Identifiers at a baud rate of 500000 with two OBD II ECUs having been identified. The DUT may produce a periodic message transmission to keep the network alive. A successful completion of 8.2.2 shall define this state.

## 5.4.3.10 ISO 15765-4 (29-bit, 500K, 2 ECUs) Initialized State

The OBD network shall have determined to be ISO 15765-4 with 29-bit CAN Identifiers at a baud rate of 500000 with two OBD II ECUs having been identified. The DUT may produce a periodic message transmission to keep the network alive. A successful completion of 8.2.3 shall define this state.

## 5.4.3.11 ISO 15765-4 (11-bit, 500K, 8 ECUs) Initialized State

The OBD network shall have determined to be ISO 15765-4 with 11-bit CAN Identifiers at a baud rate of 500000 with eight OBD II ECUs having been identified. The DUT may produce a periodic message transmission to keep the network alive. A successful completion of 8.2.4 shall define this state.

#### 5.4.3.12 ISO 15765-4 (29-bit, 500K, 8 ECUs) Initialized State

The OBD network shall have determined to be ISO 15765-4 with 29-bit CAN Identifiers at a baud rate of 500000 with eight OBD II ECUs having been identified. The DUT may produce a periodic message transmission to keep the network alive. A successful completion of 8.2.5 shall define this state.

#### 5.4.3.13 ISO 15765-4 (11-bit, 500K, 2 ECUs) SID9 Ready State

The SID \$09 INFOTYPES shall be known for each of the two OBD II ECUs on the network. The DUT may produce a periodic message transmission to keep the network alive. A successful completion of 0 shall define this state.

#### 5.4.3.14 ISO 15765-4 (11-bit, 500K, 2 ECUs) SID9 Diesel Ready State

The SID \$09 INFOTYPES shall be known for each of the two OBD II ECUs on the network. The DUT may produce a periodic message transmission to keep the network alive. A successful completion of 9.2.38 shall define this state.

#### 5.4.3.15 ISO 15765-4 (29-bit, 500K, 2 ECUs) SID9 Ready State

The SID \$09 INFOTYPES shall be known for each of the two OBD II ECUs on the network. The DUT may produce a periodic message transmission to keep the network alive. A successful completion of 9.2.39 shall define this state.

#### 5.4.3.16 ISO 15765-4 (29-bit, 500K, 2 ECUs) SID9 Diesel Ready State

The SID \$09 INFOTYPES shall be known for each of the two OBD II ECUs on the network. The DUT may produce a periodic message transmission to keep the network alive. A successful completion of 9.2.40 shall define this state.

#### 5.4.3.17 ISO 15765-4 (11-bit, 500K, 8 ECUs) SID9 Ready State

The SID \$09 INFOTYPES shall be known for each of the eight OBD II ECUs on the network. The DUT may produce a periodic message transmission to keep the network alive. A successful completion of 9.2.41 shall define this state.

#### 5.4.3.18 ISO 15765-4 (11-bit, 500K, 8 ECUs) SID9 Diesel Ready State

The SID \$09 INFOTYPES shall be known for each of the eight OBD II ECUs on the network. The DUT may produce a periodic message transmission to keep the network alive. A successful completion of 0 shall define this state.

#### 5.4.3.19 ISO 15765-4 (29-bit, 500K, 8 ECUs) SID9 Ready State

The SID \$09 INFOTYPES shall be known for each of the eight OBD II ECUs on the network. The DUT may produce a periodic message transmission to keep the network alive. A successful completion of 0 shall define this state.

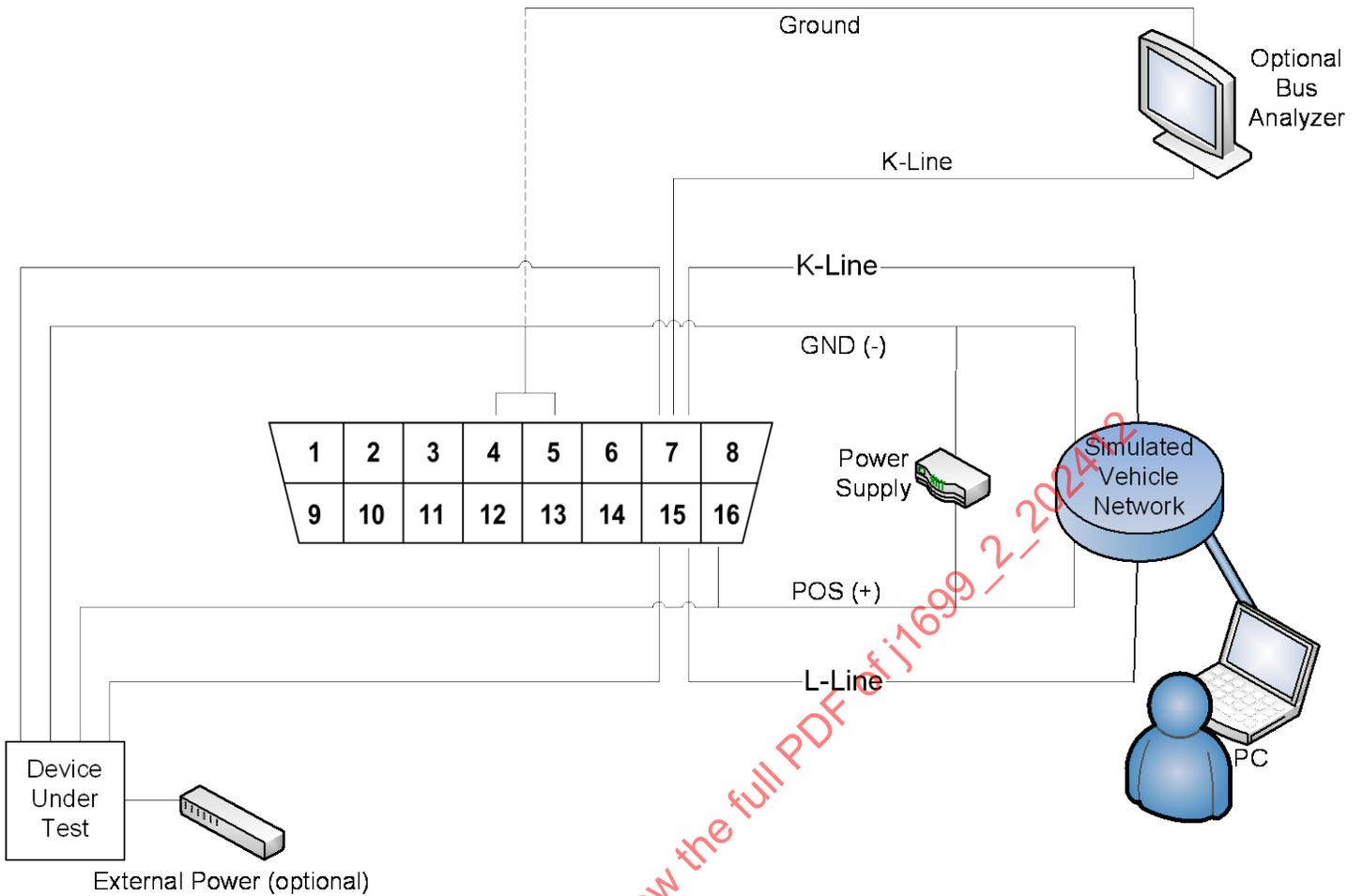
#### 5.4.3.20 ISO 15765-4 (29-bit, 500K, 8 ECUs) SID9 Diesel Ready State

The SID \$09 INFOTYPES shall be known for each of the eight OBD II ECUs on the network. The DUT may produce a periodic message transmission to keep the network alive. A successful completion of 0 shall define this state.

### 5.4.4 ISO 14230-4

#### 5.4.4.1 ISO 14230-4 Electrical Connections

Figure 10 outlines the typical set-up for ISO 14230-4 testing.



**Figure 10 - Test set-up for ISO 14230-4 testing**

5.4.4.2 ISO 14230-4 ECU Addresses

The following address bytes have been pre-defined for ISO 14230-4. For convenience, the test cases in this document will use the text in the 'ECU Reference' column when referring to the specific set of address bytes, detailed in the 'Address Bytes' column of the table below.

ECU Reference	Address Bytes
ECU #1	\$10
ECU #2	\$11
ECU #3	\$12
ECU #4	\$13
ECU #5	\$14
ECU #6	\$15
ECU #7	\$16
ECU #8	\$17

**Figure 11 - ECU reference and associated addresses for ISO 14230-4**

## 5.4.4.3 ISO 14230-4 Messages

The following messages have been pre-defined for ISO 14230-4. For convenience, the test cases in this document will use the text in the 'Message Reference' column when referring to the specific set of message bytes, detailed in the 'Message Bytes' column of the table below.

Message Reference	Message Bytes
14230 START_COMM	\$C1 \$33 \$F1 \$81 CS
14230 ECU1_START_COMM E98F	\$83 \$F1 \$10 \$C1 \$E9 \$8F CS
14230 ECU1_START_COMM 6B8F	\$83 \$F1 \$10 \$C1 \$6B \$8F CS
14230 ECU1_START_COMM 6D8F	\$83 \$F1 \$10 \$C1 \$6D \$8F CS
14230 ECU1_START_COMM EF8F	\$83 \$F1 \$10 \$C1 \$EF \$8F CS
14230 ECU1_START_COMM_NO_KB	\$83 \$F1 \$18 \$C1 CS
14230 ECU1_START_COMM_0000	\$83 \$F1 \$18 \$C1 \$00 \$00 CS
14230 SID\$01_PID\$00	\$C2 \$33 \$F1 \$01 \$00 CS
14230 ECU1_SID\$01_PID\$00	\$86 \$F1 \$10 \$41 \$00 \$80 \$10 \$00 \$00 CS
14230 ECU2_SID\$01_PID\$00	\$86 \$F1 \$11 \$41 \$00 \$80 \$10 \$00 \$00 CS
14230 ECU3_SID\$01_PID\$00	\$86 \$F1 \$12 \$41 \$00 \$00 \$00 \$00 \$00 CS
14230 ECU4_SID\$01_PID\$00	\$86 \$F1 \$13 \$41 \$00 \$00 \$00 \$00 \$00 CS
14230 ECU5_SID\$01_PID\$00	\$86 \$F1 \$14 \$41 \$00 \$00 \$00 \$00 \$00 CS
14230 ECU6_SID\$01_PID\$00	\$86 \$F1 \$15 \$41 \$00 \$00 \$00 \$00 \$00 CS
14230 ECU7_SID\$01_PID\$00	\$86 \$F1 \$16 \$41 \$00 \$00 \$00 \$00 \$00 CS
14230 ECU8_SID\$01_PID\$00	\$86 \$F1 \$17 \$41 \$00 \$00 \$00 \$00 \$00 CS
14230 ECU18_SID\$01_PID\$00	\$86 \$F1 \$18 \$41 \$00 \$00 \$00 \$00 \$00 CS
14230 SID\$01_PID\$01	\$C2 \$33 \$F1 \$01 \$01 CS
14230 SID\$01_PID\$0C	\$C2 \$33 \$F1 \$01 \$0C CS
14230 SID\$01_PID\$20	\$C2 \$33 \$F1 \$01 \$20 CS
14230 SID\$01_PID\$40	\$C2 \$33 \$F1 \$01 \$40 CS
14230 SID\$01_PID\$41	\$C2 \$33 \$F1 \$01 \$41 CS
14230 ECU1_SID\$01_PID\$00_20	\$86 \$F1 \$10 \$41 \$00 \$80 \$10 \$00 \$01 CS
14230 ECU1_SID\$01_PID\$01_SPARK	\$86 \$F1 \$10 \$41 \$01 \$00 \$07 \$EF \$EF CS
14230 ECU1_SID\$01_PID\$01_COMP	\$86 \$F1 \$10 \$41 \$01 \$00 \$0F \$EB \$EB CS
14230 ECU1_SID\$01_PID\$0C_KOEO	\$84 \$F1 \$10 \$41 \$0C \$00 \$00 CS
14230 ECU1_SID\$01_PID\$20	\$86 \$F1 \$10 \$41 \$20 \$00 \$00 \$00 \$01 CS
14230 ECU1_SID\$01_PID\$40	\$86 \$F1 \$10 \$41 \$40 \$10 \$00 \$00 \$00 CS
14230 ECU1_SID\$01_PID\$41_SPARK	\$86 \$F1 \$10 \$41 \$41 \$00 \$07 \$EF \$EF CS
14230 ECU2_SID\$01_PID\$01_SPARK	\$86 \$F1 \$11 \$41 \$01 \$81 \$44 \$00 \$00 CS
14230 ECU2_SID\$01_PID\$01_COMP	\$86 \$F1 \$11 \$41 \$01 \$81 \$4C \$00 \$00 CS
14230 ECU2_SID\$01_PID\$0C_KOEO	\$84 \$F1 \$11 \$41 \$0C \$00 \$00 CS
14230 SID\$02_PID\$02	\$C3 \$33 \$F1 \$02 \$02 \$00 CS
14230 ECU1_SID\$02_PID\$02_NoDTC	\$85 \$F1 \$10 \$42 \$02 \$00 \$00 \$00 CS
14230 ECU2_SID\$02_PID\$02_NoDTC	\$85 \$F1 \$11 \$42 \$02 \$00 \$00 \$00 CS
14230 ECU1_SID\$02_PID\$02_DTC	\$85 \$F1 \$10 \$42 \$02 \$00 \$01 \$07 CS
14230 SID\$03	\$C2 \$33 \$F1 \$03 CS
14230 ECU1_SID\$03_NoDTC	\$82 \$F1 \$10 \$43 \$00 CS
14230 ECU1_SID\$03_DTC_M1	\$8A \$F1 \$10 \$43 \$01 \$07 \$01 \$10 \$01 \$15 CS
14230 ECU1_SID\$03_DTC_M2	\$8A \$F1 \$10 \$43 \$01 \$24 \$00 \$00 \$00 \$00 CS
14230 ECU2_SID\$03_NoDTC	\$82 \$F1 \$11 \$43 \$00 CS
14230 SID\$04	\$C2 \$33 \$F1 \$04CS
14230 ECU1_SID\$04_DONE	\$81 \$F1 \$10 \$44 CS
14230 ECU1_SID\$04_NRC22	\$83 \$F1 \$10 \$7F \$44 \$22 CS
14230 ECU1_SID\$04_NRC78	\$83 \$F1 \$10 \$7F \$44 \$78 CS
14230 ECU2_SID\$04_DONE	\$81 \$F1 \$11 \$44 CS
14230 ECU2_SID\$04_NRC22	\$83 \$F1 \$11 \$7F \$44 \$22 CS
14230 SID\$05_TID\$01	\$C3 \$33 \$F1 \$05 \$01 \$02 CS
14230 ECU1_SID\$05_TID\$01	\$86 \$F1 \$10 \$45 \$01 \$02 \$5A CS
14230 SID\$06_MID\$00	\$C2 \$33 \$F1 \$06 \$00 CS
14230 ECU1_SID\$06_MID\$00	\$86 \$F1 \$10 \$46 \$00 \$CC \$00 \$00 \$00 CS

14230	SID\$07	\$C1 \$33 \$F1 \$07 CS
14230	ECU1 SID\$07 NoDTC	\$82 \$F1 \$10 \$47 \$00 CS
14230	ECU1 SID\$07 DTC M1	\$8A \$F1 \$10 \$47 \$01 \$07 \$01 \$10 \$01 \$15 CS
14230	ECU1 SID\$07 DTC M2	\$8A \$F1 \$10 \$47 \$01 \$24 \$00 \$00 \$00 \$00 CS
14230	ECU2 SID\$07 NoDTC	\$82 \$F1 \$11 \$47 \$00 CS
14230	SID\$08 TID\$00	\$C1 \$33 \$F1 \$08 CS
14230	ECU1 SID\$08 TID\$00	\$86 \$F1 \$10 \$48 \$00 \$80 \$00 \$00 \$00 CS
14230	SID\$09 INF\$00	\$C2 \$33 \$F1 \$09 \$00 CS
14230	SID\$09 INF\$01	\$C2 \$33 \$F1 \$09 \$01 CS
14230	SID\$09 INF\$02	\$C2 \$33 \$F1 \$09 \$02 CS
14230	SID\$09 INF\$03	\$C2 \$33 \$F1 \$09 \$03 CS
14230	SID\$09 INF\$04	\$C2 \$33 \$F1 \$09 \$04 CS
14230	SID\$09 INF\$05	\$C2 \$33 \$F1 \$09 \$05 CS
14230	SID\$09 INF\$06	\$C2 \$33 \$F1 \$09 \$06 CS
14230	SID\$09 INF\$07	\$C2 \$33 \$F1 \$09 \$07 CS
14230	SID\$09 INF\$08	\$C2 \$33 \$F1 \$09 \$08 CS
14230	SID\$09 INF\$0B	\$C2 \$33 \$F1 \$09 \$0B CS
14230	ECU1 SID\$09 INF\$00	\$86 \$F1 \$10 \$49 \$00 \$FF \$00 \$00 \$00 CS
14230	ECU1 SID\$09 INF\$00 DIESEL	\$86 \$F1 \$10 \$49 \$00 \$FE \$20 \$00 \$00 CS
14230	ECU1 SID\$09 INF\$01	\$83 \$F1 \$10 \$49 \$01 \$05 CS
14230	ECU1 SID\$09 INF\$02 M1	\$87 \$F1 \$10 \$49 \$02 \$01 \$00 \$00 \$00 \$31 CS
14230	ECU1 SID\$09 INF\$02 M2	\$87 \$F1 \$10 \$49 \$02 \$02 \$47 \$59 \$44 \$45 CS
14230	ECU1 SID\$09 INF\$02 M3	\$87 \$F1 \$10 \$49 \$02 \$03 \$36 \$33 \$41 \$37 CS
14230	ECU1 SID\$09 INF\$02 M4	\$87 \$F1 \$10 \$49 \$02 \$04 \$41 \$30 \$30 \$30 CS
14230	ECU1 SID\$09 INF\$02 M5	\$87 \$F1 \$10 \$49 \$02 \$05 \$30 \$30 \$30 \$30 CS
14230	ECU1 SID\$09 INF\$03	\$83 \$F1 \$10 \$49 \$03 \$04 CS
14230	ECU1 SID\$09 INF\$03 9	\$83 \$F1 \$10 \$49 \$03 \$24 CS
14230	ECU1 SID\$09 INF\$04 M1	\$87 \$F1 \$10 \$49 \$04 \$01 \$31 \$32 \$35 \$38 CS
14230	ECU1 SID\$09 INF\$04 M2	\$87 \$F1 \$10 \$49 \$04 \$02 \$39 \$37 \$37 \$32 CS
14230	ECU1 SID\$09 INF\$04 M3	\$87 \$F1 \$10 \$49 \$04 \$03 \$00 \$00 \$00 \$00 CS
14230	ECU1 SID\$09 INF\$04 M4	\$87 \$F1 \$10 \$49 \$04 \$04 \$00 \$00 \$00 \$00 CS
14230	ECU1 SID\$09 INF\$04 9 M1	\$87 \$F1 \$10 \$49 \$04 \$01 \$31 \$31 \$35 \$38 CS
14230	ECU1 SID\$09 INF\$04 9 M2	\$87 \$F1 \$10 \$49 \$04 \$02 \$39 \$37 \$37 \$38 CS
14230	ECU1 SID\$09 INF\$04 9 M3	\$87 \$F1 \$10 \$49 \$04 \$03 \$00 \$00 \$00 \$00 CS
14230	ECU1 SID\$09 INF\$04 9 M4	\$87 \$F1 \$10 \$49 \$04 \$04 \$00 \$00 \$00 \$00 CS
14230	ECU1 SID\$09 INF\$04 9 M5	\$87 \$F1 \$10 \$49 \$04 \$05 \$32 \$31 \$35 \$38 CS
14230	ECU1 SID\$09 INF\$04 9 M6	\$87 \$F1 \$10 \$49 \$04 \$06 \$39 \$37 \$37 \$37 CS
14230	ECU1 SID\$09 INF\$04 9 M7	\$87 \$F1 \$10 \$49 \$04 \$07 \$00 \$00 \$00 \$00 CS
14230	ECU1 SID\$09 INF\$04 9 M8	\$87 \$F1 \$10 \$49 \$04 \$08 \$00 \$00 \$00 \$00 CS
14230	ECU1 SID\$09 INF\$04 9 M9	\$87 \$F1 \$10 \$49 \$04 \$09 \$33 \$31 \$35 \$38 CS
14230	ECU1 SID\$09 INF\$04 9 M10	\$87 \$F1 \$10 \$49 \$04 \$0A \$39 \$37 \$37 \$36 CS
14230	ECU1 SID\$09 INF\$04 9 M11	\$87 \$F1 \$10 \$49 \$04 \$0B \$00 \$00 \$00 \$00 CS
14230	ECU1 SID\$09 INF\$04 9 M12	\$87 \$F1 \$10 \$49 \$04 \$0C \$00 \$00 \$00 \$00 CS
14230	ECU1 SID\$09 INF\$04 9 M13	\$87 \$F1 \$10 \$49 \$04 \$0D \$34 \$31 \$35 \$38 CS
14230	ECU1 SID\$09 INF\$04 9 M14	\$87 \$F1 \$10 \$49 \$04 \$0E \$39 \$37 \$37 \$35 CS
14230	ECU1 SID\$09 INF\$04 9 M15	\$87 \$F1 \$10 \$49 \$04 \$0F \$00 \$00 \$00 \$00 CS
14230	ECU1 SID\$09 INF\$04 9 M16	\$87 \$F1 \$10 \$49 \$04 \$10 \$00 \$00 \$00 \$00 CS
14230	ECU1 SID\$09 INF\$04 9 M17	\$87 \$F1 \$10 \$49 \$04 \$11 \$35 \$31 \$35 \$38 CS
14230	ECU1 SID\$09 INF\$04 9 M18	\$87 \$F1 \$10 \$49 \$04 \$12 \$39 \$37 \$37 \$34 CS
14230	ECU1 SID\$09 INF\$04 9 M19	\$87 \$F1 \$10 \$49 \$04 \$13 \$00 \$00 \$00 \$00 CS
14230	ECU1 SID\$09 INF\$04 9 M20	\$87 \$F1 \$10 \$49 \$04 \$14 \$00 \$00 \$00 \$00 CS
14230	ECU1 SID\$09 INF\$04 9 M21	\$87 \$F1 \$10 \$49 \$04 \$15 \$36 \$31 \$35 \$38 CS
14230	ECU1 SID\$09 INF\$04 9 M22	\$87 \$F1 \$10 \$49 \$04 \$16 \$39 \$37 \$37 \$33 CS
14230	ECU1 SID\$09 INF\$04 9 M23	\$87 \$F1 \$10 \$49 \$04 \$17 \$00 \$00 \$00 \$00 CS
14230	ECU1 SID\$09 INF\$04 9 M24	\$87 \$F1 \$10 \$49 \$04 \$18 \$00 \$00 \$00 \$00 CS
14230	ECU1 SID\$09 INF\$04 9 M25	\$87 \$F1 \$10 \$49 \$04 \$19 \$37 \$31 \$35 \$38 CS
14230	ECU1 SID\$09 INF\$04 9 M26	\$87 \$F1 \$10 \$49 \$04 \$1A \$39 \$37 \$37 \$32 CS
14230	ECU1 SID\$09 INF\$04 9 M27	\$87 \$F1 \$10 \$49 \$04 \$1B \$00 \$00 \$00 \$00 CS

14230 ECU1 SID\$09 INF\$04 9 M28	\$87 \$F1 \$10 \$49 \$04 \$1C \$00 \$00 \$00 \$00 CS
14230 ECU1 SID\$09 INF\$04 9 M29	\$87 \$F1 \$10 \$49 \$04 \$1D \$38 \$31 \$35 \$38 CS
14230 ECU1 SID\$09 INF\$04 9 M30	\$87 \$F1 \$10 \$49 \$04 \$1E \$39 \$37 \$37 \$31 CS
14230 ECU1 SID\$09 INF\$04 9 M31	\$87 \$F1 \$10 \$49 \$04 \$1F \$00 \$00 \$00 \$00 CS
14230 ECU1 SID\$09 INF\$04 9 M32	\$87 \$F1 \$10 \$49 \$04 \$20 \$00 \$00 \$00 \$00 CS
14230 ECU1 SID\$09 INF\$04 9 M33	\$87 \$F1 \$10 \$49 \$04 \$21 \$39 \$31 \$35 \$38 CS
14230 ECU1 SID\$09 INF\$04 9 M34	\$87 \$F1 \$10 \$49 \$04 \$22 \$39 \$37 \$37 \$30 CS
14230 ECU1 SID\$09 INF\$04 9 M35	\$87 \$F1 \$10 \$49 \$04 \$23 \$00 \$00 \$00 \$00 CS
14230 ECU1 SID\$09 INF\$04 9 M36	\$87 \$F1 \$10 \$49 \$04 \$24 \$00 \$00 \$00 \$00 CS
14230 ECU1 SID\$09 INF\$05	\$83 \$F1 \$10 \$49 \$05 \$01 CS
14230 ECU1 SID\$09 INF\$05 9	\$83 \$F1 \$10 \$49 \$05 \$09 CS
14230 ECU1 SID\$09 INF\$06	\$87 \$F1 \$10 \$49 \$06 \$01 \$17 \$91 \$BC \$88 CS
14230 ECU1 SID\$09 INF\$06 9 M1	\$87 \$F1 \$10 \$49 \$06 \$01 \$17 \$91 \$BC \$88 CS
14230 ECU1 SID\$09 INF\$06 9 M2	\$87 \$F1 \$10 \$49 \$06 \$02 \$18 \$91 \$BC \$87 CS
14230 ECU1 SID\$09 INF\$06 9 M3	\$87 \$F1 \$10 \$49 \$06 \$03 \$19 \$91 \$BC \$86 CS
14230 ECU1 SID\$09 INF\$06 9 M4	\$87 \$F1 \$10 \$49 \$06 \$04 \$1A \$91 \$BC \$85 CS
14230 ECU1 SID\$09 INF\$06 9 M5	\$87 \$F1 \$10 \$49 \$06 \$05 \$1B \$91 \$BC \$84 CS
14230 ECU1 SID\$09 INF\$06 9 M6	\$87 \$F1 \$10 \$49 \$06 \$06 \$1C \$91 \$BC \$83 CS
14230 ECU1 SID\$09 INF\$06 9 M7	\$87 \$F1 \$10 \$49 \$06 \$07 \$1D \$91 \$BC \$82 CS
14230 ECU1 SID\$09 INF\$06 9 M8	\$87 \$F1 \$10 \$49 \$06 \$08 \$1E \$55 \$AA \$81 CS
14230 ECU1 SID\$09 INF\$06 9 M9	\$87 \$F1 \$10 \$49 \$06 \$09 \$1F \$FF \$00 \$80 CS
14230 ECU1 SID\$09 INF\$07 832	\$83 \$F1 \$10 \$49 \$07 \$08 CS
14230 ECU1 SID\$09 INF\$07 840	\$83 \$F1 \$10 \$49 \$07 \$0A CS
14230 ECU1 SID\$09 INF\$07 B32	\$83 \$F1 \$10 \$49 \$07 \$08 CS
14230 ECU1 SID\$09 INF\$07 B36	\$83 \$F1 \$10 \$49 \$07 \$09 CS
14230 ECU1 SID\$09 INF\$08 32 M1	\$87 \$F1 \$10 \$49 \$08 \$01 \$00 \$64 \$00 \$69 CS
14230 ECU1 SID\$09 INF\$08 32 M2	\$87 \$F1 \$10 \$49 \$08 \$02 \$00 \$0A \$00 \$0F CS
14230 ECU1 SID\$09 INF\$08 32 M3	\$87 \$F1 \$10 \$49 \$08 \$03 \$00 \$00 \$00 \$00 CS
14230 ECU1 SID\$09 INF\$08 32 M4	\$87 \$F1 \$10 \$49 \$08 \$04 \$00 \$1E \$00 \$23 CS
14230 ECU1 SID\$09 INF\$08 32 M5	\$87 \$F1 \$10 \$49 \$08 \$05 \$00 \$00 \$00 \$00 CS
14230 ECU1 SID\$09 INF\$08 32 M6	\$87 \$F1 \$10 \$49 \$08 \$06 \$00 \$32 \$00 \$37 CS
14230 ECU1 SID\$09 INF\$08 32 M7	\$87 \$F1 \$10 \$49 \$08 \$07 \$00 \$3C \$00 \$41 CS
14230 ECU1 SID\$09 INF\$08 32 M8	\$87 \$F1 \$10 \$49 \$08 \$08 \$00 \$46 \$00 \$4B CS
14230 ECU1 SID\$09 INF\$08 40 M1	\$87 \$F1 \$10 \$49 \$08 \$01 \$00 \$64 \$00 \$69 CS
14230 ECU1 SID\$09 INF\$08 40 M2	\$87 \$F1 \$10 \$49 \$08 \$02 \$00 \$0A \$00 \$0F CS
14230 ECU1 SID\$09 INF\$08 40 M3	\$87 \$F1 \$10 \$49 \$08 \$03 \$00 \$00 \$00 \$00 CS
14230 ECU1 SID\$09 INF\$08 40 M4	\$87 \$F1 \$10 \$49 \$08 \$04 \$00 \$1E \$00 \$23 CS
14230 ECU1 SID\$09 INF\$08 40 M5	\$87 \$F1 \$10 \$49 \$08 \$05 \$00 \$00 \$00 \$00 CS
14230 ECU1 SID\$09 INF\$08 40 M6	\$87 \$F1 \$10 \$49 \$08 \$06 \$00 \$32 \$00 \$37 CS
14230 ECU1 SID\$09 INF\$08 40 M7	\$87 \$F1 \$10 \$49 \$08 \$07 \$00 \$3C \$00 \$41 CS
14230 ECU1 SID\$09 INF\$08 40 M8	\$87 \$F1 \$10 \$49 \$08 \$08 \$00 \$46 \$00 \$4B CS
14230 ECU1 SID\$09 INF\$08 40 M9	\$87 \$F1 \$10 \$49 \$08 \$09 \$00 \$5A \$00 \$5F CS
14230 ECU1 SID\$09 INF\$08 40 M10	\$87 \$F1 \$10 \$49 \$08 \$0A \$00 \$00 \$00 \$00 CS
14230 ECU1 SID\$09 INF\$0B 32 M1	\$87 \$F1 \$10 \$49 \$0B \$01 \$00 \$64 \$00 \$69 CS
14230 ECU1 SID\$09 INF\$0B 32 M2	\$87 \$F1 \$10 \$49 \$0B \$02 \$00 \$0A \$00 \$0F CS
14230 ECU1 SID\$09 INF\$0B 32 M3	\$87 \$F1 \$10 \$49 \$0B \$03 \$00 \$00 \$00 \$00 CS
14230 ECU1 SID\$09 INF\$0B 32 M4	\$87 \$F1 \$10 \$49 \$0B \$04 \$00 \$00 \$00 \$00 CS
14230 ECU1 SID\$09 INF\$0B 32 M5	\$87 \$F1 \$10 \$49 \$0B \$05 \$00 \$00 \$00 \$00 CS
14230 ECU1 SID\$09 INF\$0B 32 M6	\$87 \$F1 \$10 \$49 \$0B \$06 \$00 \$32 \$00 \$37 CS
14230 ECU1 SID\$09 INF\$0B 32 M7	\$87 \$F1 \$10 \$49 \$0B \$07 \$00 \$3C \$00 \$41 CS
14230 ECU1 SID\$09 INF\$0B 32 M8	\$87 \$F1 \$10 \$49 \$0B \$08 \$00 \$46 \$00 \$4B CS
14230 ECU1 SID\$09 INF\$0B 36 M1	\$87 \$F1 \$10 \$49 \$0B \$01 \$00 \$64 \$00 \$69 CS
14230 ECU1 SID\$09 INF\$0B 36 M2	\$87 \$F1 \$10 \$49 \$0B \$02 \$00 \$0A \$00 \$0F CS
14230 ECU1 SID\$09 INF\$0B 36 M3	\$87 \$F1 \$10 \$49 \$0B \$03 \$00 \$00 \$00 \$00 CS
14230 ECU1 SID\$09 INF\$0B 36 M4	\$87 \$F1 \$10 \$49 \$0B \$04 \$00 \$00 \$00 \$00 CS
14230 ECU1 SID\$09 INF\$0B 36 M5	\$87 \$F1 \$10 \$49 \$0B \$05 \$00 \$00 \$00 \$00 CS
14230 ECU1 SID\$09 INF\$0B 36 M6	\$87 \$F1 \$10 \$49 \$0B \$06 \$00 \$32 \$00 \$37 CS
14230 ECU1 SID\$09 INF\$0B 36 M7	\$87 \$F1 \$10 \$49 \$0B \$07 \$00 \$3C \$00 \$41 CS

14230 ECU1 SID\$09 INF\$0B 36 M8	\$87 \$F1 \$10 \$49 \$0B \$08 \$00 \$46 \$00 \$4B CS
14230 ECU1 SID\$09 INF\$0B 36 M9	\$87 \$F1 \$10 \$49 \$0B \$09 \$00 \$50 \$00 \$55 CS
14230 ECU1 SID\$09 NRC78	\$83 \$F1 \$10 \$7F \$49 \$78 CS
14230 ECU2 SID\$09 INF\$00	\$86 \$F1 \$11 \$49 \$00 \$3F \$00 \$00 \$00 CS
14230 ECU2 SID\$09 INF\$00 DIESEL	\$86 \$F1 \$11 \$49 \$00 \$3E \$20 \$00 \$00 CS
14230 ECU2 SID\$09 INF\$03	\$83 \$F1 \$11 \$49 \$03 \$04 CS
14230 ECU2 SID\$09 INF\$03 9	\$83 \$F1 \$11 \$49 \$03 \$24 CS
14230 ECU2 SID\$09 INF\$04 M1	\$87 \$F1 \$11 \$49 \$04 \$01 \$32 \$34 \$32 \$32 CS
14230 ECU2 SID\$09 INF\$04 M2	\$87 \$F1 \$11 \$49 \$04 \$02 \$31 \$34 \$31 \$39 CS
14230 ECU2 SID\$09 INF\$04 M3	\$87 \$F1 \$11 \$49 \$04 \$03 \$00 \$00 \$00 \$00 CS
14230 ECU2 SID\$09 INF\$04 M4	\$87 \$F1 \$11 \$49 \$04 \$04 \$00 \$00 \$00 \$00 CS
14230 ECU2 SID\$09 INF\$04 9 M1	\$87 \$F1 \$11 \$49 \$04 \$01 \$31 \$32 \$35 \$38 CS
14230 ECU2 SID\$09 INF\$04 9 M2	\$87 \$F1 \$11 \$49 \$04 \$02 \$39 \$37 \$37 \$38 CS
14230 ECU2 SID\$09 INF\$04 9 M3	\$87 \$F1 \$11 \$49 \$04 \$03 \$00 \$00 \$00 \$00 CS
14230 ECU2 SID\$09 INF\$04 9 M4	\$87 \$F1 \$11 \$49 \$04 \$04 \$00 \$00 \$00 \$00 CS
14230 ECU2 SID\$09 INF\$04 9 M5	\$87 \$F1 \$11 \$49 \$04 \$05 \$32 \$32 \$35 \$38 CS
14230 ECU2 SID\$09 INF\$04 9 M6	\$87 \$F1 \$11 \$49 \$04 \$06 \$39 \$37 \$37 \$37 CS
14230 ECU2 SID\$09 INF\$04 9 M7	\$87 \$F1 \$11 \$49 \$04 \$07 \$00 \$00 \$00 \$00 CS
14230 ECU2 SID\$09 INF\$04 9 M8	\$87 \$F1 \$11 \$49 \$04 \$08 \$00 \$00 \$00 \$00 CS
14230 ECU2 SID\$09 INF\$04 9 M9	\$87 \$F1 \$11 \$49 \$04 \$09 \$33 \$32 \$35 \$38 CS
14230 ECU2 SID\$09 INF\$04 9 M10	\$87 \$F1 \$11 \$49 \$04 \$0A \$39 \$37 \$37 \$36 CS
14230 ECU2 SID\$09 INF\$04 9 M11	\$87 \$F1 \$11 \$49 \$04 \$0B \$00 \$00 \$00 \$00 CS
14230 ECU2 SID\$09 INF\$04 9 M12	\$87 \$F1 \$11 \$49 \$04 \$0C \$00 \$00 \$00 \$00 CS
14230 ECU2 SID\$09 INF\$04 9 M13	\$87 \$F1 \$11 \$49 \$04 \$0D \$34 \$32 \$35 \$38 CS
14230 ECU2 SID\$09 INF\$04 9 M14	\$87 \$F1 \$11 \$49 \$04 \$0E \$39 \$37 \$37 \$35 CS
14230 ECU2 SID\$09 INF\$04 9 M15	\$87 \$F1 \$11 \$49 \$04 \$0F \$00 \$00 \$00 \$00 CS
14230 ECU2 SID\$09 INF\$04 9 M16	\$87 \$F1 \$11 \$49 \$04 \$10 \$00 \$00 \$00 \$00 CS
14230 ECU2 SID\$09 INF\$04 9 M17	\$87 \$F1 \$11 \$49 \$04 \$11 \$35 \$32 \$35 \$38 CS
14230 ECU2 SID\$09 INF\$04 9 M18	\$87 \$F1 \$11 \$49 \$04 \$12 \$39 \$37 \$37 \$34 CS
14230 ECU2 SID\$09 INF\$04 9 M19	\$87 \$F1 \$11 \$49 \$04 \$13 \$00 \$00 \$00 \$00 CS
14230 ECU2 SID\$09 INF\$04 9 M20	\$87 \$F1 \$11 \$49 \$04 \$14 \$00 \$00 \$00 \$00 CS
14230 ECU2 SID\$09 INF\$04 9 M21	\$87 \$F1 \$11 \$49 \$04 \$15 \$36 \$32 \$35 \$38 CS
14230 ECU2 SID\$09 INF\$04 9 M22	\$87 \$F1 \$11 \$49 \$04 \$16 \$39 \$37 \$37 \$33 CS
14230 ECU2 SID\$09 INF\$04 9 M23	\$87 \$F1 \$11 \$49 \$04 \$17 \$00 \$00 \$00 \$00 CS
14230 ECU2 SID\$09 INF\$04 9 M24	\$87 \$F1 \$11 \$49 \$04 \$18 \$00 \$00 \$00 \$00 CS
14230 ECU2 SID\$09 INF\$04 9 M25	\$87 \$F1 \$11 \$49 \$04 \$19 \$37 \$32 \$35 \$38 CS
14230 ECU2 SID\$09 INF\$04 9 M26	\$87 \$F1 \$11 \$49 \$04 \$1A \$39 \$37 \$37 \$32 CS
14230 ECU2 SID\$09 INF\$04 9 M27	\$87 \$F1 \$11 \$49 \$04 \$1B \$00 \$00 \$00 \$00 CS
14230 ECU2 SID\$09 INF\$04 9 M28	\$87 \$F1 \$11 \$49 \$04 \$1C \$00 \$00 \$00 \$00 CS
14230 ECU2 SID\$09 INF\$04 9 M29	\$87 \$F1 \$11 \$49 \$04 \$1D \$38 \$32 \$35 \$38 CS
14230 ECU2 SID\$09 INF\$04 9 M30	\$87 \$F1 \$11 \$49 \$04 \$1E \$39 \$37 \$37 \$31 CS
14230 ECU2 SID\$09 INF\$04 9 M31	\$87 \$F1 \$11 \$49 \$04 \$1F \$00 \$00 \$00 \$00 CS
14230 ECU2 SID\$09 INF\$04 9 M32	\$87 \$F1 \$11 \$49 \$04 \$20 \$00 \$00 \$00 \$00 CS
14230 ECU2 SID\$09 INF\$04 9 M33	\$87 \$F1 \$11 \$49 \$04 \$21 \$39 \$32 \$35 \$38 CS
14230 ECU2 SID\$09 INF\$04 9 M34	\$87 \$F1 \$11 \$49 \$04 \$22 \$39 \$37 \$37 \$30 CS
14230 ECU2 SID\$09 INF\$04 9 M35	\$87 \$F1 \$11 \$49 \$04 \$23 \$00 \$00 \$00 \$00 CS
14230 ECU2 SID\$09 INF\$04 9 M36	\$87 \$F1 \$11 \$49 \$04 \$24 \$00 \$00 \$00 \$00 CS
14230 ECU2 SID\$09 INF\$05	\$83 \$F1 \$11 \$49 \$05 \$01 CS
14230 ECU2 SID\$09 INF\$05 9	\$83 \$F1 \$11 \$49 \$05 \$09 CS
14230 ECU2 SID\$09 INF\$06	\$87 \$F1 \$11 \$49 \$06 \$01 \$27 \$91 \$BC \$88 CS
14230 ECU2 SID\$09 INF\$06 9 M1	\$87 \$F1 \$11 \$49 \$06 \$01 \$27 \$91 \$BC \$88 CS
14230 ECU2 SID\$09 INF\$06 9 M2	\$87 \$F1 \$11 \$49 \$06 \$02 \$28 \$91 \$BC \$87 CS
14230 ECU2 SID\$09 INF\$06 9 M3	\$87 \$F1 \$11 \$49 \$06 \$03 \$29 \$91 \$BC \$86 CS
14230 ECU2 SID\$09 INF\$06 9 M4	\$87 \$F1 \$11 \$49 \$06 \$04 \$2A \$91 \$BC \$85 CS
14230 ECU2 SID\$09 INF\$06 9 M5	\$87 \$F1 \$11 \$49 \$06 \$05 \$2B \$91 \$BC \$84 CS
14230 ECU2 SID\$09 INF\$06 9 M6	\$87 \$F1 \$11 \$49 \$06 \$06 \$2C \$91 \$BC \$83 CS
14230 ECU2 SID\$09 INF\$06 9 M7	\$87 \$F1 \$11 \$49 \$06 \$07 \$2D \$91 \$BC \$82 CS
14230 ECU2 SID\$09 INF\$06 9 M8	\$87 \$F1 \$11 \$49 \$06 \$08 \$2E \$55 \$AA \$81 CS

14230 ECU2 SID\$09 INF\$06 9 M9	\$87 \$F1 \$11 \$49 \$06 \$09 \$2F \$FF \$00 \$80 CS
14230 ECU2 SID\$09 INF\$07 832	\$83 \$F1 \$11 \$49 \$07 \$08 CS
14230 ECU2 SID\$09 INF\$07 840	\$83 \$F1 \$11 \$49 \$07 \$0A CS
14230 ECU2 SID\$09 INF\$07 B32	\$83 \$F1 \$11 \$49 \$07 \$08 CS
14230 ECU2 SID\$09 INF\$07 B36	\$83 \$F1 \$11 \$49 \$07 \$09 CS
14230 ECU2 SID\$09 INF\$08 32 M1	\$87 \$F1 \$11 \$49 \$08 \$01 \$00 \$64 \$00 \$69 CS
14230 ECU2 SID\$09 INF\$08 32 M2	\$87 \$F1 \$11 \$49 \$08 \$02 \$00 \$00 \$00 \$00 CS
14230 ECU2 SID\$09 INF\$08 32 M3	\$87 \$F1 \$11 \$49 \$08 \$03 \$00 \$14 \$00 \$19 CS
14230 ECU2 SID\$09 INF\$08 32 M4	\$87 \$F1 \$11 \$49 \$08 \$04 \$00 \$00 \$00 \$00 CS
14230 ECU2 SID\$09 INF\$08 32 M5	\$87 \$F1 \$11 \$49 \$08 \$05 \$00 \$28 \$00 \$2D CS
14230 ECU2 SID\$09 INF\$08 32 M6	\$87 \$F1 \$11 \$49 \$08 \$06 \$00 \$00 \$00 \$00 CS
14230 ECU2 SID\$09 INF\$08 32 M7	\$87 \$F1 \$11 \$49 \$08 \$07 \$00 \$00 \$00 \$00 CS
14230 ECU2 SID\$09 INF\$08 32 M8	\$87 \$F1 \$11 \$49 \$08 \$08 \$00 \$00 \$00 \$00 CS
14230 ECU2 SID\$09 INF\$08 40 M1	\$87 \$F1 \$11 \$49 \$08 \$01 \$00 \$64 \$00 \$69 CS
14230 ECU2 SID\$09 INF\$08 40 M2	\$87 \$F1 \$11 \$49 \$08 \$02 \$00 \$00 \$00 \$00 CS
14230 ECU2 SID\$09 INF\$08 40 M3	\$87 \$F1 \$11 \$49 \$08 \$03 \$00 \$14 \$00 \$19 CS
14230 ECU2 SID\$09 INF\$08 40 M4	\$87 \$F1 \$11 \$49 \$08 \$04 \$00 \$00 \$00 \$00 CS
14230 ECU2 SID\$09 INF\$08 40 M5	\$87 \$F1 \$11 \$49 \$08 \$05 \$00 \$28 \$00 \$2D CS
14230 ECU2 SID\$09 INF\$08 40 M6	\$87 \$F1 \$11 \$49 \$08 \$06 \$00 \$00 \$00 \$00 CS
14230 ECU2 SID\$09 INF\$08 40 M7	\$87 \$F1 \$11 \$49 \$08 \$07 \$00 \$00 \$00 \$00 CS
14230 ECU2 SID\$09 INF\$08 40 M8	\$87 \$F1 \$11 \$49 \$08 \$08 \$00 \$00 \$00 \$00 CS
14230 ECU2 SID\$09 INF\$08 40 M9	\$87 \$F1 \$11 \$49 \$08 \$09 \$00 \$00 \$00 \$00 CS
14230 ECU2 SID\$09 INF\$08 40 M10	\$87 \$F1 \$11 \$49 \$08 \$0A \$00 \$5A \$00 \$5F CS
14230 ECU2 SID\$09 INF\$0B 32 M1	\$87 \$F1 \$11 \$49 \$0B \$01 \$00 \$64 \$00 \$69 CS
14230 ECU2 SID\$09 INF\$0B 32 M2	\$87 \$F1 \$11 \$49 \$0B \$02 \$00 \$00 \$00 \$00 CS
14230 ECU2 SID\$09 INF\$0B 32 M3	\$87 \$F1 \$11 \$49 \$0B \$03 \$00 \$14 \$00 \$19 CS
14230 ECU2 SID\$09 INF\$0B 32 M4	\$87 \$F1 \$11 \$49 \$0B \$04 \$00 \$00 \$00 \$00 CS
14230 ECU2 SID\$09 INF\$0B 32 M5	\$87 \$F1 \$11 \$49 \$0B \$05 \$00 \$28 \$00 \$2D CS
14230 ECU2 SID\$09 INF\$0B 32 M6	\$87 \$F1 \$11 \$49 \$0B \$06 \$00 \$00 \$00 \$00 CS
14230 ECU2 SID\$09 INF\$0B 32 M7	\$87 \$F1 \$11 \$49 \$0B \$07 \$00 \$00 \$00 \$00 CS
14230 ECU2 SID\$09 INF\$0B 32 M8	\$87 \$F1 \$11 \$49 \$0B \$08 \$00 \$00 \$00 \$00 CS
14230 ECU2 SID\$09 INF\$0B 36 M1	\$87 \$F1 \$11 \$49 \$0B \$01 \$00 \$64 \$00 \$69 CS
14230 ECU2 SID\$09 INF\$0B 36 M2	\$87 \$F1 \$11 \$49 \$0B \$02 \$00 \$00 \$00 \$00 CS
14230 ECU2 SID\$09 INF\$0B 36 M3	\$87 \$F1 \$11 \$49 \$0B \$03 \$00 \$14 \$00 \$19 CS
14230 ECU2 SID\$09 INF\$0B 36 M4	\$87 \$F1 \$11 \$49 \$0B \$04 \$00 \$00 \$00 \$00 CS
14230 ECU2 SID\$09 INF\$0B 36 M5	\$87 \$F1 \$11 \$49 \$0B \$05 \$00 \$28 \$00 \$2D CS
14230 ECU2 SID\$09 INF\$0B 36 M6	\$87 \$F1 \$11 \$49 \$0B \$06 \$00 \$00 \$00 \$00 CS
14230 ECU2 SID\$09 INF\$0B 36 M7	\$87 \$F1 \$11 \$49 \$0B \$07 \$00 \$00 \$00 \$00 CS
14230 ECU2 SID\$09 INF\$0B 36 M8	\$87 \$F1 \$11 \$49 \$0B \$08 \$00 \$00 \$00 \$00 CS
14230 ECU2 SID\$09 INF\$0B 36 M9	\$87 \$F1 \$11 \$49 \$0B \$09 \$00 \$00 \$00 \$00 CS
14230 ECU3 SID\$09 INF\$00	\$86 \$F1 \$12 \$49 \$00 \$3C \$00 \$00 \$00 CS
14230 ECU3 SID\$09 INF\$03 9	\$83 \$F1 \$12 \$49 \$03 \$24 CS
14230 ECU3 SID\$09 INF\$04 9 M1	\$87 \$F1 \$12 \$49 \$04 \$01 \$31 \$33 \$35 \$38 CS
14230 ECU3 SID\$09 INF\$04 9 M2	\$87 \$F1 \$12 \$49 \$04 \$02 \$39 \$37 \$37 \$38 CS
14230 ECU3 SID\$09 INF\$04 9 M3	\$87 \$F1 \$12 \$49 \$04 \$03 \$00 \$00 \$00 \$00 CS
14230 ECU3 SID\$09 INF\$04 9 M4	\$87 \$F1 \$12 \$49 \$04 \$04 \$00 \$00 \$00 \$00 CS
14230 ECU3 SID\$09 INF\$04 9 M5	\$87 \$F1 \$12 \$49 \$04 \$05 \$32 \$33 \$35 \$38 CS
14230 ECU3 SID\$09 INF\$04 9 M6	\$87 \$F1 \$12 \$49 \$04 \$06 \$39 \$37 \$37 \$37 CS
14230 ECU3 SID\$09 INF\$04 9 M7	\$87 \$F1 \$12 \$49 \$04 \$07 \$00 \$00 \$00 \$00 CS
14230 ECU3 SID\$09 INF\$04 9 M8	\$87 \$F1 \$12 \$49 \$04 \$08 \$00 \$00 \$00 \$00 CS
14230 ECU3 SID\$09 INF\$04 9 M9	\$87 \$F1 \$12 \$49 \$04 \$09 \$33 \$33 \$35 \$38 CS
14230 ECU3 SID\$09 INF\$04 9 M10	\$87 \$F1 \$12 \$49 \$04 \$0A \$39 \$37 \$37 \$36 CS
14230 ECU3 SID\$09 INF\$04 9 M11	\$87 \$F1 \$12 \$49 \$04 \$0B \$00 \$00 \$00 \$00 CS
14230 ECU3 SID\$09 INF\$04 9 M12	\$87 \$F1 \$12 \$49 \$04 \$0C \$00 \$00 \$00 \$00 CS
14230 ECU3 SID\$09 INF\$04 9 M13	\$87 \$F1 \$12 \$49 \$04 \$0D \$34 \$33 \$35 \$38 CS
14230 ECU3 SID\$09 INF\$04 9 M14	\$87 \$F1 \$12 \$49 \$04 \$0E \$39 \$37 \$37 \$35 CS
14230 ECU3 SID\$09 INF\$04 9 M15	\$87 \$F1 \$12 \$49 \$04 \$0F \$00 \$00 \$00 \$00 CS
14230 ECU3 SID\$09 INF\$04 9 M16	\$87 \$F1 \$12 \$49 \$04 \$10 \$00 \$00 \$00 \$00 CS

14230 ECU3 SID\$09 INF\$04 9 M17	\$87 \$F1 \$12 \$49 \$04 \$11 \$35 \$33 \$35 \$38 CS
14230 ECU3 SID\$09 INF\$04 9 M18	\$87 \$F1 \$12 \$49 \$04 \$12 \$39 \$37 \$37 \$34 CS
14230 ECU3 SID\$09 INF\$04 9 M19	\$87 \$F1 \$12 \$49 \$04 \$13 \$00 \$00 \$00 \$00 CS
14230 ECU3 SID\$09 INF\$04 9 M20	\$87 \$F1 \$12 \$49 \$04 \$14 \$00 \$00 \$00 \$00 CS
14230 ECU3 SID\$09 INF\$04 9 M21	\$87 \$F1 \$12 \$49 \$04 \$15 \$36 \$33 \$35 \$38 CS
14230 ECU3 SID\$09 INF\$04 9 M22	\$87 \$F1 \$12 \$49 \$04 \$16 \$39 \$37 \$37 \$33 CS
14230 ECU3 SID\$09 INF\$04 9 M23	\$87 \$F1 \$12 \$49 \$04 \$17 \$00 \$00 \$00 \$00 CS
14230 ECU3 SID\$09 INF\$04 9 M24	\$87 \$F1 \$12 \$49 \$04 \$18 \$00 \$00 \$00 \$00 CS
14230 ECU3 SID\$09 INF\$04 9 M25	\$87 \$F1 \$12 \$49 \$04 \$19 \$37 \$33 \$35 \$38 CS
14230 ECU3 SID\$09 INF\$04 9 M26	\$87 \$F1 \$12 \$49 \$04 \$1A \$39 \$37 \$37 \$32 CS
14230 ECU3 SID\$09 INF\$04 9 M27	\$87 \$F1 \$12 \$49 \$04 \$1B \$00 \$00 \$00 \$00 CS
14230 ECU3 SID\$09 INF\$04 9 M28	\$87 \$F1 \$12 \$49 \$04 \$1C \$00 \$00 \$00 \$00 CS
14230 ECU3 SID\$09 INF\$04 9 M29	\$87 \$F1 \$12 \$49 \$04 \$1D \$38 \$33 \$35 \$38 CS
14230 ECU3 SID\$09 INF\$04 9 M30	\$87 \$F1 \$12 \$49 \$04 \$1E \$39 \$37 \$37 \$31 CS
14230 ECU3 SID\$09 INF\$04 9 M31	\$87 \$F1 \$12 \$49 \$04 \$1F \$00 \$00 \$00 \$00 CS
14230 ECU3 SID\$09 INF\$04 9 M32	\$87 \$F1 \$12 \$49 \$04 \$20 \$00 \$00 \$00 \$00 CS
14230 ECU3 SID\$09 INF\$04 9 M33	\$87 \$F1 \$12 \$49 \$04 \$21 \$39 \$33 \$35 \$38 CS
14230 ECU3 SID\$09 INF\$04 9 M34	\$87 \$F1 \$12 \$49 \$04 \$22 \$39 \$37 \$37 \$30 CS
14230 ECU3 SID\$09 INF\$04 9 M35	\$87 \$F1 \$12 \$49 \$04 \$23 \$00 \$00 \$00 \$00 CS
14230 ECU3 SID\$09 INF\$04 9 M36	\$87 \$F1 \$12 \$49 \$04 \$24 \$00 \$00 \$00 \$00 CS
14230 ECU3 SID\$09 INF\$05 9	\$83 \$F1 \$12 \$49 \$05 \$09 CS
14230 ECU3 SID\$09 INF\$06 9 M1	\$87 \$F1 \$12 \$49 \$06 \$01 \$37 \$91 \$BC \$88 CS
14230 ECU3 SID\$09 INF\$06 9 M2	\$87 \$F1 \$12 \$49 \$06 \$02 \$38 \$91 \$BC \$87 CS
14230 ECU3 SID\$09 INF\$06 9 M3	\$87 \$F1 \$12 \$49 \$06 \$03 \$39 \$91 \$BC \$86 CS
14230 ECU3 SID\$09 INF\$06 9 M4	\$87 \$F1 \$12 \$49 \$06 \$04 \$3A \$91 \$BC \$85 CS
14230 ECU3 SID\$09 INF\$06 9 M5	\$87 \$F1 \$12 \$49 \$06 \$05 \$3B \$91 \$BC \$84 CS
14230 ECU3 SID\$09 INF\$06 9 M6	\$87 \$F1 \$12 \$49 \$06 \$06 \$3C \$91 \$BC \$83 CS
14230 ECU3 SID\$09 INF\$06 9 M7	\$87 \$F1 \$12 \$49 \$06 \$07 \$3D \$91 \$BC \$82 CS
14230 ECU3 SID\$09 INF\$06 9 M8	\$87 \$F1 \$12 \$49 \$06 \$08 \$3E \$55 \$AA \$81 CS
14230 ECU3 SID\$09 INF\$06 9 M9	\$87 \$F1 \$12 \$49 \$06 \$09 \$3F \$FF \$00 \$80 CS
14230 ECU4 SID\$09 INF\$00	\$86 \$F1 \$13 \$49 \$00 \$3C \$00 \$00 \$00 CS
14230 ECU4 SID\$09 INF\$03 9	\$83 \$F1 \$13 \$49 \$03 \$24 CS
14230 ECU4 SID\$09 INF\$04 9 M1	\$87 \$F1 \$13 \$49 \$04 \$01 \$31 \$34 \$35 \$38 CS
14230 ECU4 SID\$09 INF\$04 9 M2	\$87 \$F1 \$13 \$49 \$04 \$02 \$39 \$37 \$37 \$38 CS
14230 ECU4 SID\$09 INF\$04 9 M3	\$87 \$F1 \$13 \$49 \$04 \$03 \$00 \$00 \$00 \$00 CS
14230 ECU4 SID\$09 INF\$04 9 M4	\$87 \$F1 \$13 \$49 \$04 \$04 \$00 \$00 \$00 \$00 CS
14230 ECU4 SID\$09 INF\$04 9 M5	\$87 \$F1 \$13 \$49 \$04 \$05 \$32 \$34 \$35 \$38 CS
14230 ECU4 SID\$09 INF\$04 9 M6	\$87 \$F1 \$13 \$49 \$04 \$06 \$39 \$37 \$37 \$37 CS
14230 ECU4 SID\$09 INF\$04 9 M7	\$87 \$F1 \$13 \$49 \$04 \$07 \$00 \$00 \$00 \$00 CS
14230 ECU4 SID\$09 INF\$04 9 M8	\$87 \$F1 \$13 \$49 \$04 \$08 \$00 \$00 \$00 \$00 CS
14230 ECU4 SID\$09 INF\$04 9 M9	\$87 \$F1 \$13 \$49 \$04 \$09 \$33 \$34 \$35 \$38 CS
14230 ECU4 SID\$09 INF\$04 9 M10	\$87 \$F1 \$13 \$49 \$04 \$0A \$39 \$37 \$37 \$36 CS
14230 ECU4 SID\$09 INF\$04 9 M11	\$87 \$F1 \$13 \$49 \$04 \$0B \$00 \$00 \$00 \$00 CS
14230 ECU4 SID\$09 INF\$04 9 M12	\$87 \$F1 \$13 \$49 \$04 \$0C \$00 \$00 \$00 \$00 CS
14230 ECU4 SID\$09 INF\$04 9 M13	\$87 \$F1 \$13 \$49 \$04 \$0D \$34 \$34 \$35 \$38 CS
14230 ECU4 SID\$09 INF\$04 9 M14	\$87 \$F1 \$13 \$49 \$04 \$0E \$39 \$37 \$37 \$35 CS
14230 ECU4 SID\$09 INF\$04 9 M15	\$87 \$F1 \$13 \$49 \$04 \$0F \$00 \$00 \$00 \$00 CS
14230 ECU4 SID\$09 INF\$04 9 M16	\$87 \$F1 \$13 \$49 \$04 \$10 \$00 \$00 \$00 \$00 CS
14230 ECU4 SID\$09 INF\$04 9 M17	\$87 \$F1 \$13 \$49 \$04 \$11 \$35 \$34 \$35 \$38 CS
14230 ECU4 SID\$09 INF\$04 9 M18	\$87 \$F1 \$13 \$49 \$04 \$12 \$39 \$37 \$37 \$34 CS
14230 ECU4 SID\$09 INF\$04 9 M19	\$87 \$F1 \$13 \$49 \$04 \$13 \$00 \$00 \$00 \$00 CS
14230 ECU4 SID\$09 INF\$04 9 M20	\$87 \$F1 \$13 \$49 \$04 \$14 \$00 \$00 \$00 \$00 CS
14230 ECU4 SID\$09 INF\$04 9 M21	\$87 \$F1 \$13 \$49 \$04 \$15 \$36 \$34 \$35 \$38 CS
14230 ECU4 SID\$09 INF\$04 9 M22	\$87 \$F1 \$13 \$49 \$04 \$16 \$39 \$37 \$37 \$33 CS
14230 ECU4 SID\$09 INF\$04 9 M23	\$87 \$F1 \$13 \$49 \$04 \$17 \$00 \$00 \$00 \$00 CS
14230 ECU4 SID\$09 INF\$04 9 M24	\$87 \$F1 \$13 \$49 \$04 \$18 \$00 \$00 \$00 \$00 CS
14230 ECU4 SID\$09 INF\$04 9 M25	\$87 \$F1 \$13 \$49 \$04 \$19 \$37 \$34 \$35 \$38 CS
14230 ECU4 SID\$09 INF\$04 9 M26	\$87 \$F1 \$13 \$49 \$04 \$1A \$39 \$37 \$37 \$32 CS

14230 ECU4 SID\$09 INF\$04 9 M27	\$87 \$F1 \$13 \$49 \$04 \$1B \$00 \$00 \$00 \$00 CS
14230 ECU4 SID\$09 INF\$04 9 M28	\$87 \$F1 \$13 \$49 \$04 \$1C \$00 \$00 \$00 \$00 CS
14230 ECU4 SID\$09 INF\$04 9 M29	\$87 \$F1 \$13 \$49 \$04 \$1D \$38 \$34 \$35 \$38 CS
14230 ECU4 SID\$09 INF\$04 9 M30	\$87 \$F1 \$13 \$49 \$04 \$1E \$39 \$37 \$37 \$31 CS
14230 ECU4 SID\$09 INF\$04 9 M31	\$87 \$F1 \$13 \$49 \$04 \$1F \$00 \$00 \$00 \$00 CS
14230 ECU4 SID\$09 INF\$04 9 M32	\$87 \$F1 \$13 \$49 \$04 \$20 \$00 \$00 \$00 \$00 CS
14230 ECU4 SID\$09 INF\$04 9 M33	\$87 \$F1 \$13 \$49 \$04 \$21 \$39 \$34 \$35 \$38 CS
14230 ECU4 SID\$09 INF\$04 9 M34	\$87 \$F1 \$13 \$49 \$04 \$22 \$39 \$37 \$37 \$30 CS
14230 ECU4 SID\$09 INF\$04 9 M35	\$87 \$F1 \$13 \$49 \$04 \$23 \$00 \$00 \$00 \$00 CS
14230 ECU4 SID\$09 INF\$04 9 M36	\$87 \$F1 \$13 \$49 \$04 \$24 \$00 \$00 \$00 \$00 CS
14230 ECU4 SID\$09 INF\$05 9	\$83 \$F1 \$13 \$49 \$05 \$09 CS
14230 ECU4 SID\$09 INF\$06 9 M1	\$87 \$F1 \$13 \$49 \$06 \$01 \$47 \$91 \$BC \$88 CS
14230 ECU4 SID\$09 INF\$06 9 M2	\$87 \$F1 \$13 \$49 \$06 \$02 \$48 \$91 \$BC \$87 CS
14230 ECU4 SID\$09 INF\$06 9 M3	\$87 \$F1 \$13 \$49 \$06 \$03 \$49 \$91 \$BC \$86 CS
14230 ECU4 SID\$09 INF\$06 9 M4	\$87 \$F1 \$13 \$49 \$06 \$04 \$4A \$91 \$BC \$85 CS
14230 ECU4 SID\$09 INF\$06 9 M5	\$87 \$F1 \$13 \$49 \$06 \$05 \$4B \$91 \$BC \$84 CS
14230 ECU4 SID\$09 INF\$06 9 M6	\$87 \$F1 \$13 \$49 \$06 \$06 \$4C \$91 \$BC \$83 CS
14230 ECU4 SID\$09 INF\$06 9 M7	\$87 \$F1 \$13 \$49 \$06 \$07 \$4D \$91 \$BC \$82 CS
14230 ECU4 SID\$09 INF\$06 9 M8	\$87 \$F1 \$13 \$49 \$06 \$08 \$4E \$55 \$AA \$81 CS
14230 ECU4 SID\$09 INF\$06 9 M9	\$87 \$F1 \$13 \$49 \$06 \$09 \$4F \$FF \$00 \$80 CS
14230 ECU5 SID\$09 INF\$00	\$86 \$F1 \$14 \$49 \$00 \$3C \$00 \$00 \$00 CS
14230 ECU5 SID\$09 INF\$03 9	\$83 \$F1 \$14 \$49 \$03 \$24 CS
14230 ECU5 SID\$09 INF\$04 9 M1	\$87 \$F1 \$14 \$49 \$04 \$01 \$31 \$35 \$35 \$38 CS
14230 ECU5 SID\$09 INF\$04 9 M2	\$87 \$F1 \$14 \$49 \$04 \$02 \$39 \$37 \$37 \$38 CS
14230 ECU5 SID\$09 INF\$04 9 M3	\$87 \$F1 \$14 \$49 \$04 \$03 \$00 \$00 \$00 \$00 CS
14230 ECU5 SID\$09 INF\$04 9 M4	\$87 \$F1 \$14 \$49 \$04 \$04 \$00 \$00 \$00 \$00 CS
14230 ECU5 SID\$09 INF\$04 9 M5	\$87 \$F1 \$14 \$49 \$04 \$05 \$32 \$35 \$35 \$38 CS
14230 ECU5 SID\$09 INF\$04 9 M6	\$87 \$F1 \$14 \$49 \$04 \$06 \$39 \$37 \$37 \$37 CS
14230 ECU5 SID\$09 INF\$04 9 M7	\$87 \$F1 \$14 \$49 \$04 \$07 \$00 \$00 \$00 \$00 CS
14230 ECU5 SID\$09 INF\$04 9 M8	\$87 \$F1 \$14 \$49 \$04 \$08 \$00 \$00 \$00 \$00 CS
14230 ECU5 SID\$09 INF\$04 9 M9	\$87 \$F1 \$14 \$49 \$04 \$09 \$33 \$35 \$35 \$38 CS
14230 ECU5 SID\$09 INF\$04 9 M10	\$87 \$F1 \$14 \$49 \$04 \$0A \$39 \$37 \$37 \$36 CS
14230 ECU5 SID\$09 INF\$04 9 M11	\$87 \$F1 \$14 \$49 \$04 \$0B \$00 \$00 \$00 \$00 CS
14230 ECU5 SID\$09 INF\$04 9 M12	\$87 \$F1 \$14 \$49 \$04 \$0C \$00 \$00 \$00 \$00 CS
14230 ECU5 SID\$09 INF\$04 9 M13	\$87 \$F1 \$14 \$49 \$04 \$0D \$34 \$35 \$35 \$38 CS
14230 ECU5 SID\$09 INF\$04 9 M14	\$87 \$F1 \$14 \$49 \$04 \$0E \$39 \$37 \$37 \$35 CS
14230 ECU5 SID\$09 INF\$04 9 M15	\$87 \$F1 \$14 \$49 \$04 \$0F \$00 \$00 \$00 \$00 CS
14230 ECU5 SID\$09 INF\$04 9 M16	\$87 \$F1 \$14 \$49 \$04 \$10 \$00 \$00 \$00 \$00 CS
14230 ECU5 SID\$09 INF\$04 9 M17	\$87 \$F1 \$14 \$49 \$04 \$11 \$35 \$35 \$35 \$38 CS
14230 ECU5 SID\$09 INF\$04 9 M18	\$87 \$F1 \$14 \$49 \$04 \$12 \$39 \$37 \$37 \$34 CS
14230 ECU5 SID\$09 INF\$04 9 M19	\$87 \$F1 \$14 \$49 \$04 \$13 \$00 \$00 \$00 \$00 CS
14230 ECU5 SID\$09 INF\$04 9 M20	\$87 \$F1 \$14 \$49 \$04 \$14 \$00 \$00 \$00 \$00 CS
14230 ECU5 SID\$09 INF\$04 9 M21	\$87 \$F1 \$14 \$49 \$04 \$15 \$36 \$35 \$35 \$38 CS
14230 ECU5 SID\$09 INF\$04 9 M22	\$87 \$F1 \$14 \$49 \$04 \$16 \$39 \$37 \$37 \$33 CS
14230 ECU5 SID\$09 INF\$04 9 M23	\$87 \$F1 \$14 \$49 \$04 \$17 \$00 \$00 \$00 \$00 CS
14230 ECU5 SID\$09 INF\$04 9 M24	\$87 \$F1 \$14 \$49 \$04 \$18 \$00 \$00 \$00 \$00 CS
14230 ECU5 SID\$09 INF\$04 9 M25	\$87 \$F1 \$14 \$49 \$04 \$19 \$37 \$35 \$35 \$38 CS
14230 ECU5 SID\$09 INF\$04 9 M26	\$87 \$F1 \$14 \$49 \$04 \$1A \$39 \$37 \$37 \$32 CS
14230 ECU5 SID\$09 INF\$04 9 M27	\$87 \$F1 \$14 \$49 \$04 \$1B \$00 \$00 \$00 \$00 CS
14230 ECU5 SID\$09 INF\$04 9 M28	\$87 \$F1 \$14 \$49 \$04 \$1C \$00 \$00 \$00 \$00 CS
14230 ECU5 SID\$09 INF\$04 9 M29	\$87 \$F1 \$14 \$49 \$04 \$1D \$38 \$35 \$35 \$38 CS
14230 ECU5 SID\$09 INF\$04 9 M30	\$87 \$F1 \$14 \$49 \$04 \$1E \$39 \$37 \$37 \$31 CS
14230 ECU5 SID\$09 INF\$04 9 M31	\$87 \$F1 \$14 \$49 \$04 \$1F \$00 \$00 \$00 \$00 CS
14230 ECU5 SID\$09 INF\$04 9 M32	\$87 \$F1 \$14 \$49 \$04 \$20 \$00 \$00 \$00 \$00 CS
14230 ECU5 SID\$09 INF\$04 9 M33	\$87 \$F1 \$14 \$49 \$04 \$21 \$39 \$35 \$35 \$38 CS
14230 ECU5 SID\$09 INF\$04 9 M34	\$87 \$F1 \$14 \$49 \$04 \$22 \$39 \$37 \$37 \$30 CS
14230 ECU5 SID\$09 INF\$04 9 M35	\$87 \$F1 \$14 \$49 \$04 \$23 \$00 \$00 \$00 \$00 CS
14230 ECU5 SID\$09 INF\$04 9 M36	\$87 \$F1 \$14 \$49 \$04 \$24 \$00 \$00 \$00 \$00 CS

14230 ECU5 SID\$09 INF\$05 9	\$83 \$F1 \$14 \$49 \$05 \$09 CS
14230 ECU5 SID\$09 INF\$06 9 M1	\$87 \$F1 \$14 \$49 \$06 \$01 \$57 \$91 \$BC \$88 CS
14230 ECU5 SID\$09 INF\$06 9 M2	\$87 \$F1 \$14 \$49 \$06 \$02 \$58 \$91 \$BC \$87 CS
14230 ECU5 SID\$09 INF\$06 9 M3	\$87 \$F1 \$14 \$49 \$06 \$03 \$59 \$91 \$BC \$86 CS
14230 ECU5 SID\$09 INF\$06 9 M4	\$87 \$F1 \$14 \$49 \$06 \$04 \$5A \$91 \$BC \$85 CS
14230 ECU5 SID\$09 INF\$06 9 M5	\$87 \$F1 \$14 \$49 \$06 \$05 \$5B \$91 \$BC \$84 CS
14230 ECU5 SID\$09 INF\$06 9 M6	\$87 \$F1 \$14 \$49 \$06 \$06 \$5C \$91 \$BC \$83 CS
14230 ECU5 SID\$09 INF\$06 9 M7	\$87 \$F1 \$14 \$49 \$06 \$07 \$5D \$91 \$BC \$82 CS
14230 ECU5 SID\$09 INF\$06 9 M8	\$87 \$F1 \$14 \$49 \$06 \$08 \$5E \$55 \$AA \$81 CS
14230 ECU5 SID\$09 INF\$06 9 M9	\$87 \$F1 \$14 \$49 \$06 \$09 \$5F \$FF \$00 \$80 CS
14230 ECU6 SID\$09 INF\$00	\$86 \$F1 \$15 \$49 \$00 \$3C \$00 \$00 \$00 CS
14230 ECU6 SID\$09 INF\$03 9	\$83 \$F1 \$15 \$49 \$03 \$24 CS
14230 ECU6 SID\$09 INF\$04 9 M1	\$87 \$F1 \$15 \$49 \$04 \$01 \$31 \$36 \$35 \$38 CS
14230 ECU6 SID\$09 INF\$04 9 M2	\$87 \$F1 \$15 \$49 \$04 \$02 \$39 \$37 \$37 \$38 CS
14230 ECU6 SID\$09 INF\$04 9 M3	\$87 \$F1 \$15 \$49 \$04 \$03 \$00 \$00 \$00 \$00 CS
14230 ECU6 SID\$09 INF\$04 9 M4	\$87 \$F1 \$15 \$49 \$04 \$04 \$00 \$00 \$00 \$00 CS
14230 ECU6 SID\$09 INF\$04 9 M5	\$87 \$F1 \$15 \$49 \$04 \$05 \$32 \$36 \$35 \$38 CS
14230 ECU6 SID\$09 INF\$04 9 M6	\$87 \$F1 \$15 \$49 \$04 \$06 \$39 \$37 \$37 \$37 CS
14230 ECU6 SID\$09 INF\$04 9 M7	\$87 \$F1 \$15 \$49 \$04 \$07 \$00 \$00 \$00 \$00 CS
14230 ECU6 SID\$09 INF\$04 9 M8	\$87 \$F1 \$15 \$49 \$04 \$08 \$00 \$00 \$00 \$00 CS
14230 ECU6 SID\$09 INF\$04 9 M9	\$87 \$F1 \$15 \$49 \$04 \$09 \$33 \$36 \$35 \$38 CS
14230 ECU6 SID\$09 INF\$04 9 M10	\$87 \$F1 \$15 \$49 \$04 \$0A \$39 \$37 \$37 \$36 CS
14230 ECU6 SID\$09 INF\$04 9 M11	\$87 \$F1 \$15 \$49 \$04 \$0B \$00 \$00 \$00 \$00 CS
14230 ECU6 SID\$09 INF\$04 9 M12	\$87 \$F1 \$15 \$49 \$04 \$0C \$00 \$00 \$00 \$00 CS
14230 ECU6 SID\$09 INF\$04 9 M13	\$87 \$F1 \$15 \$49 \$04 \$0D \$34 \$36 \$35 \$38 CS
14230 ECU6 SID\$09 INF\$04 9 M14	\$87 \$F1 \$15 \$49 \$04 \$0E \$39 \$37 \$37 \$35 CS
14230 ECU6 SID\$09 INF\$04 9 M15	\$87 \$F1 \$15 \$49 \$04 \$0F \$00 \$00 \$00 \$00 CS
14230 ECU6 SID\$09 INF\$04 9 M16	\$87 \$F1 \$15 \$49 \$04 \$10 \$00 \$00 \$00 \$00 CS
14230 ECU6 SID\$09 INF\$04 9 M17	\$87 \$F1 \$15 \$49 \$04 \$11 \$35 \$36 \$35 \$38 CS
14230 ECU6 SID\$09 INF\$04 9 M18	\$87 \$F1 \$15 \$49 \$04 \$12 \$39 \$37 \$37 \$34 CS
14230 ECU6 SID\$09 INF\$04 9 M19	\$87 \$F1 \$15 \$49 \$04 \$13 \$00 \$00 \$00 \$00 CS
14230 ECU6 SID\$09 INF\$04 9 M20	\$87 \$F1 \$15 \$49 \$04 \$14 \$00 \$00 \$00 \$00 CS
14230 ECU6 SID\$09 INF\$04 9 M21	\$87 \$F1 \$15 \$49 \$04 \$15 \$36 \$36 \$35 \$38 CS
14230 ECU6 SID\$09 INF\$04 9 M22	\$87 \$F1 \$15 \$49 \$04 \$16 \$39 \$37 \$37 \$33 CS
14230 ECU6 SID\$09 INF\$04 9 M23	\$87 \$F1 \$15 \$49 \$04 \$17 \$00 \$00 \$00 \$00 CS
14230 ECU6 SID\$09 INF\$04 9 M24	\$87 \$F1 \$15 \$49 \$04 \$18 \$00 \$00 \$00 \$00 CS
14230 ECU6 SID\$09 INF\$04 9 M25	\$87 \$F1 \$15 \$49 \$04 \$19 \$37 \$36 \$35 \$38 CS
14230 ECU6 SID\$09 INF\$04 9 M26	\$87 \$F1 \$15 \$49 \$04 \$1A \$39 \$37 \$37 \$32 CS
14230 ECU6 SID\$09 INF\$04 9 M27	\$87 \$F1 \$15 \$49 \$04 \$1B \$00 \$00 \$00 \$00 CS
14230 ECU6 SID\$09 INF\$04 9 M28	\$87 \$F1 \$15 \$49 \$04 \$1C \$00 \$00 \$00 \$00 CS
14230 ECU6 SID\$09 INF\$04 9 M29	\$87 \$F1 \$15 \$49 \$04 \$1D \$38 \$36 \$35 \$38 CS
14230 ECU6 SID\$09 INF\$04 9 M30	\$87 \$F1 \$15 \$49 \$04 \$1E \$39 \$37 \$37 \$31 CS
14230 ECU6 SID\$09 INF\$04 9 M31	\$87 \$F1 \$15 \$49 \$04 \$1F \$00 \$00 \$00 \$00 CS
14230 ECU6 SID\$09 INF\$04 9 M32	\$87 \$F1 \$15 \$49 \$04 \$20 \$00 \$00 \$00 \$00 CS
14230 ECU6 SID\$09 INF\$04 9 M33	\$87 \$F1 \$15 \$49 \$04 \$21 \$39 \$36 \$35 \$38 CS
14230 ECU6 SID\$09 INF\$04 9 M34	\$87 \$F1 \$15 \$49 \$04 \$22 \$39 \$37 \$37 \$30 CS
14230 ECU6 SID\$09 INF\$04 9 M35	\$87 \$F1 \$15 \$49 \$04 \$23 \$00 \$00 \$00 \$00 CS
14230 ECU6 SID\$09 INF\$04 9 M36	\$87 \$F1 \$15 \$49 \$04 \$24 \$00 \$00 \$00 \$00 CS
14230 ECU6 SID\$09 INF\$05 9	\$83 \$F1 \$15 \$49 \$05 \$09 CS
14230 ECU6 SID\$09 INF\$06 9 M1	\$87 \$F1 \$15 \$49 \$06 \$01 \$67 \$91 \$BC \$88 CS
14230 ECU6 SID\$09 INF\$06 9 M2	\$87 \$F1 \$15 \$49 \$06 \$02 \$68 \$91 \$BC \$87 CS
14230 ECU6 SID\$09 INF\$06 9 M3	\$87 \$F1 \$15 \$49 \$06 \$03 \$69 \$91 \$BC \$86 CS
14230 ECU6 SID\$09 INF\$06 9 M4	\$87 \$F1 \$15 \$49 \$06 \$04 \$6A \$91 \$BC \$85 CS
14230 ECU6 SID\$09 INF\$06 9 M5	\$87 \$F1 \$15 \$49 \$06 \$05 \$6B \$91 \$BC \$84 CS
14230 ECU6 SID\$09 INF\$06 9 M6	\$87 \$F1 \$15 \$49 \$06 \$06 \$6C \$91 \$BC \$83 CS
14230 ECU6 SID\$09 INF\$06 9 M7	\$87 \$F1 \$15 \$49 \$06 \$07 \$6D \$91 \$BC \$82 CS
14230 ECU6 SID\$09 INF\$06 9 M8	\$87 \$F1 \$15 \$49 \$06 \$08 \$6E \$55 \$AA \$81 CS
14230 ECU6 SID\$09 INF\$06 9 M9	\$87 \$F1 \$15 \$49 \$06 \$09 \$6F \$FF \$00 \$80 CS

14230 ECU7 SID\$09 INF\$00	\$86 \$F1 \$16 \$49 \$00 \$3C \$00 \$00 \$00 CS
14230 ECU7 SID\$09 INF\$03 9	\$83 \$F1 \$16 \$49 \$03 \$24 CS
14230 ECU7 SID\$09 INF\$04 9 M1	\$87 \$F1 \$16 \$49 \$04 \$01 \$31 \$37 \$35 \$38 CS
14230 ECU7 SID\$09 INF\$04 9 M2	\$87 \$F1 \$16 \$49 \$04 \$02 \$39 \$37 \$37 \$38 CS
14230 ECU7 SID\$09 INF\$04 9 M3	\$87 \$F1 \$16 \$49 \$04 \$03 \$00 \$00 \$00 \$00 CS
14230 ECU7 SID\$09 INF\$04 9 M4	\$87 \$F1 \$16 \$49 \$04 \$04 \$00 \$00 \$00 \$00 CS
14230 ECU7 SID\$09 INF\$04 9 M5	\$87 \$F1 \$16 \$49 \$04 \$05 \$32 \$37 \$35 \$38 CS
14230 ECU7 SID\$09 INF\$04 9 M6	\$87 \$F1 \$16 \$49 \$04 \$06 \$39 \$37 \$37 \$37 CS
14230 ECU7 SID\$09 INF\$04 9 M7	\$87 \$F1 \$16 \$49 \$04 \$07 \$00 \$00 \$00 \$00 CS
14230 ECU7 SID\$09 INF\$04 9 M8	\$87 \$F1 \$16 \$49 \$04 \$08 \$00 \$00 \$00 \$00 CS
14230 ECU7 SID\$09 INF\$04 9 M9	\$87 \$F1 \$16 \$49 \$04 \$09 \$33 \$37 \$35 \$38 CS
14230 ECU7 SID\$09 INF\$04 9 M10	\$87 \$F1 \$16 \$49 \$04 \$0A \$39 \$37 \$37 \$36 CS
14230 ECU7 SID\$09 INF\$04 9 M11	\$87 \$F1 \$16 \$49 \$04 \$0B \$00 \$00 \$00 \$00 CS
14230 ECU7 SID\$09 INF\$04 9 M12	\$87 \$F1 \$16 \$49 \$04 \$0C \$00 \$00 \$00 \$00 CS
14230 ECU7 SID\$09 INF\$04 9 M13	\$87 \$F1 \$16 \$49 \$04 \$0D \$34 \$37 \$35 \$38 CS
14230 ECU7 SID\$09 INF\$04 9 M14	\$87 \$F1 \$16 \$49 \$04 \$0E \$39 \$37 \$37 \$35 CS
14230 ECU7 SID\$09 INF\$04 9 M15	\$87 \$F1 \$16 \$49 \$04 \$0F \$00 \$00 \$00 \$00 CS
14230 ECU7 SID\$09 INF\$04 9 M16	\$87 \$F1 \$16 \$49 \$04 \$10 \$00 \$00 \$00 \$00 CS
14230 ECU7 SID\$09 INF\$04 9 M17	\$87 \$F1 \$16 \$49 \$04 \$11 \$35 \$37 \$35 \$38 CS
14230 ECU7 SID\$09 INF\$04 9 M18	\$87 \$F1 \$16 \$49 \$04 \$12 \$39 \$37 \$37 \$34 CS
14230 ECU7 SID\$09 INF\$04 9 M19	\$87 \$F1 \$16 \$49 \$04 \$13 \$00 \$00 \$00 \$00 CS
14230 ECU7 SID\$09 INF\$04 9 M20	\$87 \$F1 \$16 \$49 \$04 \$14 \$00 \$00 \$00 \$00 CS
14230 ECU7 SID\$09 INF\$04 9 M21	\$87 \$F1 \$16 \$49 \$04 \$15 \$36 \$37 \$35 \$38 CS
14230 ECU7 SID\$09 INF\$04 9 M22	\$87 \$F1 \$16 \$49 \$04 \$16 \$39 \$37 \$37 \$33 CS
14230 ECU7 SID\$09 INF\$04 9 M23	\$87 \$F1 \$16 \$49 \$04 \$17 \$00 \$00 \$00 \$00 CS
14230 ECU7 SID\$09 INF\$04 9 M24	\$87 \$F1 \$16 \$49 \$04 \$18 \$00 \$00 \$00 \$00 CS
14230 ECU7 SID\$09 INF\$04 9 M25	\$87 \$F1 \$16 \$49 \$04 \$19 \$37 \$37 \$35 \$38 CS
14230 ECU7 SID\$09 INF\$04 9 M26	\$87 \$F1 \$16 \$49 \$04 \$1A \$39 \$37 \$37 \$32 CS
14230 ECU7 SID\$09 INF\$04 9 M26	\$87 \$F1 \$16 \$49 \$04 \$1B \$00 \$00 \$00 \$00 CS
14230 ECU7 SID\$09 INF\$04 9 M28	\$87 \$F1 \$16 \$49 \$04 \$1C \$00 \$00 \$00 \$00 CS
14230 ECU7 SID\$09 INF\$04 9 M29	\$87 \$F1 \$16 \$49 \$04 \$1D \$38 \$37 \$35 \$38 CS
14230 ECU7 SID\$09 INF\$04 9 M30	\$87 \$F1 \$16 \$49 \$04 \$1E \$39 \$37 \$37 \$31 CS
14230 ECU7 SID\$09 INF\$04 9 M31	\$87 \$F1 \$16 \$49 \$04 \$1F \$00 \$00 \$00 \$00 CS
14230 ECU7 SID\$09 INF\$04 9 M32	\$87 \$F1 \$16 \$49 \$04 \$20 \$00 \$00 \$00 \$00 CS
14230 ECU7 SID\$09 INF\$04 9 M33	\$87 \$F1 \$16 \$49 \$04 \$21 \$39 \$37 \$35 \$38 CS
14230 ECU7 SID\$09 INF\$04 9 M34	\$87 \$F1 \$16 \$49 \$04 \$22 \$39 \$37 \$37 \$30 CS
14230 ECU7 SID\$09 INF\$04 9 M35	\$87 \$F1 \$16 \$49 \$04 \$23 \$00 \$00 \$00 \$00 CS
14230 ECU7 SID\$09 INF\$04 9 M36	\$87 \$F1 \$16 \$49 \$04 \$24 \$00 \$00 \$00 \$00 CS
14230 ECU7 SID\$09 INF\$05 9	\$83 \$F1 \$16 \$49 \$05 \$09 CS
14230 ECU7 SID\$09 INF\$06 9 M1	\$87 \$F1 \$16 \$49 \$06 \$01 \$77 \$91 \$BC \$88 CS
14230 ECU7 SID\$09 INF\$06 9 M2	\$87 \$F1 \$16 \$49 \$06 \$02 \$78 \$91 \$BC \$87 CS
14230 ECU7 SID\$09 INF\$06 9 M3	\$87 \$F1 \$16 \$49 \$06 \$03 \$79 \$91 \$BC \$86 CS
14230 ECU7 SID\$09 INF\$06 9 M4	\$87 \$F1 \$16 \$49 \$06 \$04 \$7A \$91 \$BC \$85 CS
14230 ECU7 SID\$09 INF\$06 9 M5	\$87 \$F1 \$16 \$49 \$06 \$05 \$7B \$91 \$BC \$84 CS
14230 ECU7 SID\$09 INF\$06 9 M6	\$87 \$F1 \$16 \$49 \$06 \$06 \$7C \$91 \$BC \$83 CS
14230 ECU7 SID\$09 INF\$06 9 M7	\$87 \$F1 \$16 \$49 \$06 \$07 \$7D \$91 \$BC \$82 CS
14230 ECU7 SID\$09 INF\$06 9 M8	\$87 \$F1 \$16 \$49 \$06 \$08 \$7E \$55 \$AA \$81 CS
14230 ECU7 SID\$09 INF\$06 9 M9	\$87 \$F1 \$16 \$49 \$06 \$09 \$7F \$FF \$00 \$80 CS
14230 ECU8 SID\$09 INF\$00	\$86 \$F1 \$17 \$49 \$00 \$3C \$00 \$00 \$00 CS
14230 ECU8 SID\$09 INF\$03 9	\$83 \$F1 \$17 \$49 \$03 \$24 CS
14230 ECU8 SID\$09 INF\$04 9 M1	\$87 \$F1 \$17 \$49 \$04 \$01 \$31 \$38 \$35 \$38 CS
14230 ECU8 SID\$09 INF\$04 9 M2	\$87 \$F1 \$17 \$49 \$04 \$02 \$39 \$37 \$37 \$38 CS
14230 ECU8 SID\$09 INF\$04 9 M3	\$87 \$F1 \$17 \$49 \$04 \$03 \$00 \$00 \$00 \$00 CS
14230 ECU8 SID\$09 INF\$04 9 M4	\$87 \$F1 \$17 \$49 \$04 \$04 \$00 \$00 \$00 \$00 CS
14230 ECU8 SID\$09 INF\$04 9 M5	\$87 \$F1 \$17 \$49 \$04 \$05 \$32 \$38 \$35 \$38 CS
14230 ECU8 SID\$09 INF\$04 9 M6	\$87 \$F1 \$17 \$49 \$04 \$06 \$39 \$37 \$37 \$37 CS
14230 ECU8 SID\$09 INF\$04 9 M7	\$87 \$F1 \$17 \$49 \$04 \$07 \$00 \$00 \$00 \$00 CS
14230 ECU8 SID\$09 INF\$04 9 M8	\$87 \$F1 \$17 \$49 \$04 \$08 \$00 \$00 \$00 \$00 CS

14230	ECU8	SID\$09	INF\$04	9	M9	\$87	\$F1	\$17	\$49	\$04	\$09	\$33	\$38	\$35	\$38	CS
14230	ECU8	SID\$09	INF\$04	9	M10	\$87	\$F1	\$17	\$49	\$04	\$0A	\$39	\$37	\$37	\$36	CS
14230	ECU8	SID\$09	INF\$04	9	M11	\$87	\$F1	\$17	\$49	\$04	\$0B	\$00	\$00	\$00	\$00	CS
14230	ECU8	SID\$09	INF\$04	9	M12	\$87	\$F1	\$17	\$49	\$04	\$0C	\$00	\$00	\$00	\$00	CS
14230	ECU8	SID\$09	INF\$04	9	M13	\$87	\$F1	\$17	\$49	\$04	\$0D	\$34	\$38	\$35	\$38	CS
14230	ECU8	SID\$09	INF\$04	9	M14	\$87	\$F1	\$17	\$49	\$04	\$0E	\$39	\$37	\$37	\$35	CS
14230	ECU8	SID\$09	INF\$04	9	M15	\$87	\$F1	\$17	\$49	\$04	\$0F	\$00	\$00	\$00	\$00	CS
14230	ECU8	SID\$09	INF\$04	9	M16	\$87	\$F1	\$17	\$49	\$04	\$10	\$00	\$00	\$00	\$00	CS
14230	ECU8	SID\$09	INF\$04	9	M17	\$87	\$F1	\$17	\$49	\$04	\$11	\$35	\$38	\$35	\$38	CS
14230	ECU8	SID\$09	INF\$04	9	M18	\$87	\$F1	\$17	\$49	\$04	\$12	\$39	\$37	\$37	\$34	CS
14230	ECU8	SID\$09	INF\$04	9	M19	\$87	\$F1	\$17	\$49	\$04	\$13	\$00	\$00	\$00	\$00	CS
14230	ECU8	SID\$09	INF\$04	9	M20	\$87	\$F1	\$17	\$49	\$04	\$14	\$00	\$00	\$00	\$00	CS
14230	ECU8	SID\$09	INF\$04	9	M21	\$87	\$F1	\$17	\$49	\$04	\$15	\$36	\$38	\$35	\$38	CS
14230	ECU8	SID\$09	INF\$04	9	M22	\$87	\$F1	\$17	\$49	\$04	\$16	\$39	\$37	\$37	\$33	CS
14230	ECU8	SID\$09	INF\$04	9	M23	\$87	\$F1	\$17	\$49	\$04	\$17	\$00	\$00	\$00	\$00	CS
14230	ECU8	SID\$09	INF\$04	9	M24	\$87	\$F1	\$17	\$49	\$04	\$17	\$00	\$00	\$00	\$00	CS
14230	ECU8	SID\$09	INF\$04	9	M25	\$87	\$F1	\$17	\$49	\$04	\$19	\$37	\$38	\$35	\$38	CS
14230	ECU8	SID\$09	INF\$04	9	M26	\$87	\$F1	\$17	\$49	\$04	\$1A	\$39	\$37	\$37	\$32	CS
14230	ECU8	SID\$09	INF\$04	9	M27	\$87	\$F1	\$17	\$49	\$04	\$1B	\$00	\$00	\$00	\$00	CS
14230	ECU8	SID\$09	INF\$04	9	M28	\$87	\$F1	\$17	\$49	\$04	\$1C	\$00	\$00	\$00	\$00	CS
14230	ECU8	SID\$09	INF\$04	9	M29	\$87	\$F1	\$17	\$49	\$04	\$1D	\$38	\$38	\$35	\$38	CS
14230	ECU8	SID\$09	INF\$04	9	M30	\$87	\$F1	\$17	\$49	\$04	\$1E	\$39	\$37	\$37	\$31	CS
14230	ECU8	SID\$09	INF\$04	9	M31	\$87	\$F1	\$17	\$49	\$04	\$1F	\$00	\$00	\$00	\$00	CS
14230	ECU8	SID\$09	INF\$04	9	M32	\$87	\$F1	\$17	\$49	\$04	\$20	\$00	\$00	\$00	\$00	CS
14230	ECU8	SID\$09	INF\$04	9	M33	\$87	\$F1	\$17	\$49	\$04	\$21	\$39	\$38	\$35	\$38	CS
14230	ECU8	SID\$09	INF\$04	9	M34	\$87	\$F1	\$17	\$49	\$04	\$22	\$39	\$37	\$37	\$30	CS
14230	ECU8	SID\$09	INF\$04	9	M35	\$87	\$F1	\$17	\$49	\$04	\$23	\$00	\$00	\$00	\$00	CS
14230	ECU8	SID\$09	INF\$04	9	M36	\$87	\$F1	\$17	\$49	\$04	\$24	\$00	\$00	\$00	\$00	CS
14230	ECU8	SID\$09	INF\$05	9		\$83	\$F1	\$17	\$49	\$05	\$09					CS
14230	ECU8	SID\$09	INF\$06	9	M1	\$87	\$F1	\$17	\$49	\$06	\$01	\$87	\$91	\$BC	\$88	CS
14230	ECU8	SID\$09	INF\$06	9	M2	\$87	\$F1	\$17	\$49	\$06	\$02	\$88	\$91	\$BC	\$87	CS
14230	ECU8	SID\$09	INF\$06	9	M3	\$87	\$F1	\$17	\$49	\$06	\$03	\$89	\$91	\$BC	\$86	CS
14230	ECU8	SID\$09	INF\$06	9	M4	\$87	\$F1	\$17	\$49	\$06	\$04	\$8A	\$91	\$BC	\$85	CS
14230	ECU8	SID\$09	INF\$06	9	M5	\$87	\$F1	\$17	\$49	\$06	\$05	\$8B	\$91	\$BC	\$84	CS
14230	ECU8	SID\$09	INF\$06	9	M6	\$87	\$F1	\$17	\$49	\$06	\$06	\$8C	\$91	\$BC	\$83	CS
14230	ECU8	SID\$09	INF\$06	9	M7	\$87	\$F1	\$17	\$49	\$06	\$07	\$8D	\$91	\$BC	\$82	CS
14230	ECU8	SID\$09	INF\$06	9	M8	\$87	\$F1	\$17	\$49	\$06	\$08	\$8E	\$55	\$AA	\$81	CS
14230	ECU8	SID\$09	INF\$06	9	M9	\$87	\$F1	\$17	\$49	\$06	\$09	\$8F	\$FF	\$00	\$80	CS

Where CS indicates a correctly computed Checksum as defined by ISO 14230-4.

**Figure 12 - Pre-defined messages for ISO 14230-4**

## 5.4.4.4 ISO 14230-4 Message Timing

For convenience, the test cases in this document will use the text in the 'Parameter Reference' column when referring to the specific value, detailed in the 'Value' column of the table below. Unless otherwise specified, the following timing values shall be used:

Parameter Reference	Value
W1_MIN	60 ms
W1	100 ms
W1_MAX	300 ms
W2_MIN	5 ms
W2	15 ms
W2_MAX	20 ms
W3_MIN	0 ms
W3	15 ms
W3_MAX	20 ms
W4_MIN	25 ms
W4	40 ms
W4_MAX	50 ms
W5	At least 300 ms
W5	5 ms
TiniL	24 ms ( $\pm 1$ ms)
Twup	50 ms ( $\pm 1$ ms)
P1_MIN	0 ms
P1	5 ms
P1_MAX	20 ms
P2_MIN	0 ms
P2	30 ms
P2_MAX	50 ms
P3_MIN	0 ms
P3	25 ms
P3_MAX	5000 ms
P4_MIN	0 ms
P4	25 ms
P4_MAX	20 ms

**Figure 13 - Pre-defined message timing for ISO 14230-4**

## 5.4.4.5 ISO 14230-4 Connected State

The device shall initially be in the 'Power On' state with the K and L Lines connected and the appropriate pull-up applied (as depicted in 5.4.4.1).

## 5.4.4.6 ISO 14230-4 (2 ECUs) Initialized State

The OBD network shall have determined to be ISO 14230-4 at a baud rate of 10400 with two OBD II ECUs shall have been identified. The DUT may produce a periodic message transmission to keep the network alive. A successful completion of any Section from 8.3.2 to 8.3.9 shall define this state.

## 5.4.4.7 ISO 14230-4 (8 ECUs) Initialized State

The OBD network shall have determined to be ISO 14230-4 at a baud rate of 10400 with eight OBD II ECUs having been identified. The DUT may produce a periodic message transmission to keep the network alive. A successful completion of 8.3.10 shall define this state.

## 5.4.4.8 ISO 14230-4 (2 ECUs) SID9 Ready State

The SID \$09 INFOTYPES shall be known for each of the two OBD II ECUs on the network. The DUT may produce a periodic message transmission to keep the network alive. A successful completion of 9.3.17 shall define this state.

## 5.4.4.9 ISO 14230-4 (2 ECUs) SID9 Diesel Ready State

The SID \$09 INFOTYPES shall be known for each of the two OBD II ECUs on the network. The DUT may produce a periodic message transmission to keep the network alive. A successful completion of 0 shall define this state.

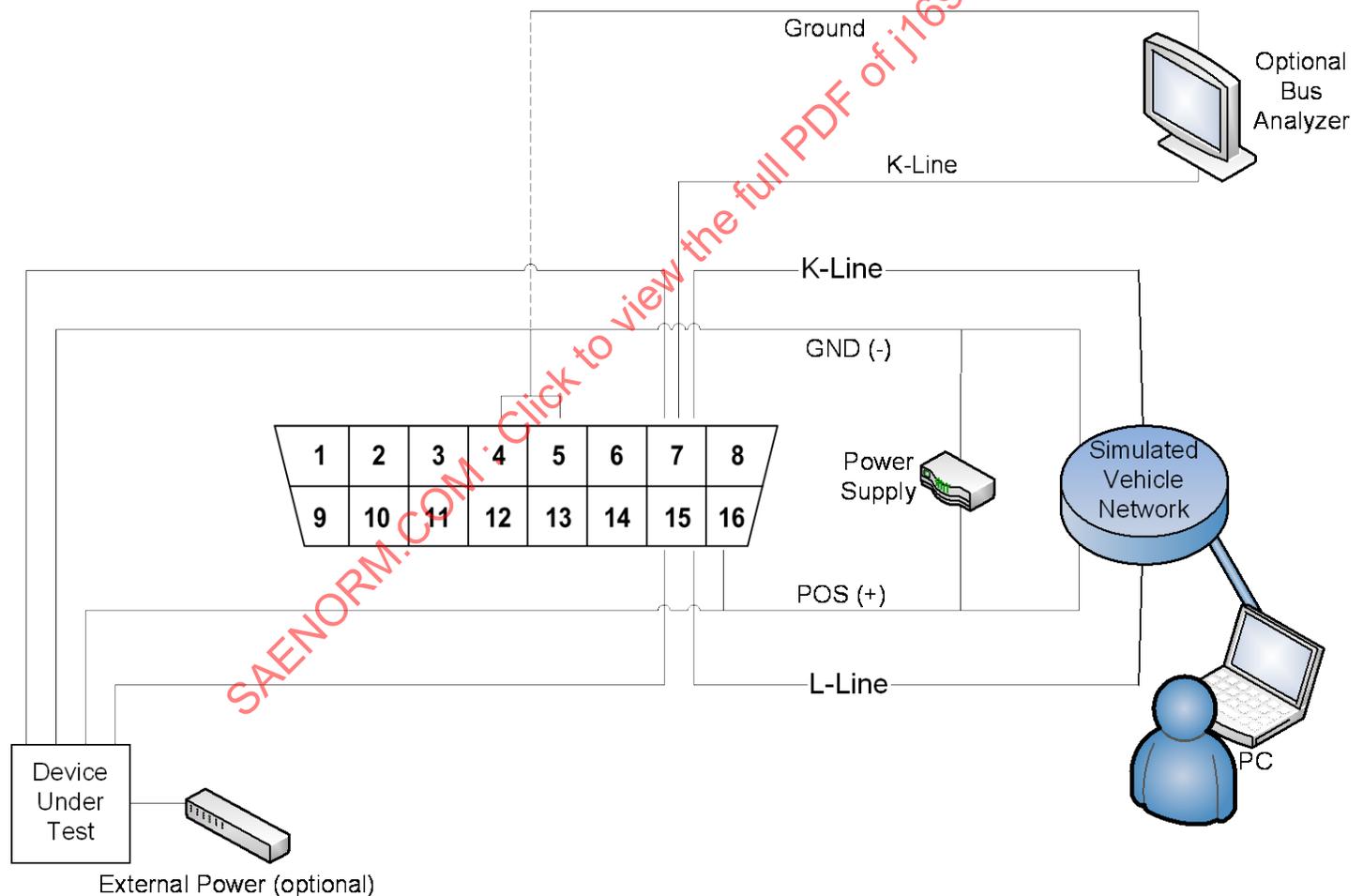
## 5.4.4.10 ISO 14230-4 (8 ECUs) SID9 Ready State

The SID \$09 INFOTYPES shall be known for each of the eight OBD II ECUs on the network. The DUT may produce a periodic message transmission to keep the network alive. A successful completion of 9.3.19 shall define this state.

## 5.4.5 ISO 9141-2

## 5.4.5.1 ISO 9141-2 Electrical Connections

Figure 14 outlines the typical set-up for ISO 9141-2 testing.



**Figure 14 - Test set-up for ISO 9141-2 testing**

## 5.4.5.2 ISO 9141-2 ECU Addresses

The following address bytes have been pre-defined for ISO 9141-2. For convenience, the test cases in this document will use the text in the 'ECU Reference' column when referring to the specific set of address bytes, detailed in the 'Address Bytes' column of the table below.

ECU Reference	Address Bytes
ECU #1	\$10
ECU #2	\$11
ECU #3	\$12
ECU #4	\$13
ECU #5	\$14
ECU #6	\$15
ECU #7	\$16
ECU #8	\$17

**Figure 15 - ECU reference and associated addresses for ISO 9141-2**

## 5.4.5.3 ISO 9141-2 Messages

The following messages have been pre-defined for ISO 9141-2. For convenience, the test cases in this document will use the text in the 'Message Reference' column when referring to the specific set of message bytes, detailed in the 'Message Bytes' column of the table below.

Message Reference	Message Bytes
9141_SID\$01_PID\$00	\$68 \$6A \$F1 \$01 \$00 CS
9141_ECU1_SID\$01_PID\$00_BAD	\$48 \$6B \$10 \$72 \$33
9141_ECU1_SID\$01_PID\$00	\$48 \$6B \$10 \$41 \$00 \$80 \$10 \$00 \$00 CS
9141_ECU1_SID\$01_PID\$00	\$48 \$6B \$10 \$41 \$00 \$80 \$10 \$00 \$00 CS
9141_ECU2_SID\$01_PID\$00	\$48 \$6B \$11 \$41 \$00 \$80 \$10 \$00 \$00 CS
9141_ECU3_SID\$01_PID\$00	\$48 \$6B \$12 \$41 \$00 \$00 \$00 \$00 \$00 CS
9141_ECU4_SID\$01_PID\$00	\$48 \$6B \$13 \$41 \$00 \$00 \$00 \$00 \$00 CS
9141_ECU5_SID\$01_PID\$00	\$48 \$6B \$14 \$41 \$00 \$00 \$00 \$00 \$00 CS
9141_ECU6_SID\$01_PID\$00	\$48 \$6B \$15 \$41 \$00 \$00 \$00 \$00 \$00 CS
9141_ECU7_SID\$01_PID\$00	\$48 \$6B \$16 \$41 \$00 \$00 \$00 \$00 \$00 CS
9141_ECU8_SID\$01_PID\$00	\$48 \$6B \$17 \$41 \$00 \$00 \$00 \$00 \$00 CS
9141_SID\$01_PID\$01	\$68 \$6A \$F1 \$01 \$01 CS
9141_SID\$01_PID\$0C	\$68 \$6A \$F1 \$01 \$0C CS
9141_SID\$01_PID\$20	\$68 \$6A \$F1 \$01 \$20 CS
9141_SID\$01_PID\$40	\$68 \$6A \$F1 \$01 \$40 CS
9141_SID\$01_PID\$41	\$68 \$6A \$F1 \$01 \$41 CS
9141_ECU1_SID\$01_PID\$00_20	\$48 \$6B \$10 \$41 \$00 \$80 \$10 \$00 \$01 CS
9141_ECU1_SID\$01_PID\$01_SPARK	\$48 \$6B \$10 \$41 \$01 \$00 \$07 \$EF \$EF CS
9141_ECU1_SID\$01_PID\$01_COMP	\$48 \$6B \$10 \$41 \$01 \$00 \$0F \$EB \$EB CS
9141_ECU1_SID\$01_PID\$0C_KOEO	\$48 \$6B \$10 \$41 \$0C \$00 \$00 CS
9141_ECU1_SID\$01_PID\$20	\$48 \$6B \$10 \$41 \$20 \$00 \$00 \$00 \$01 CS
9141_ECU1_SID\$01_PID\$40	\$48 \$6B \$10 \$41 \$40 \$10 \$00 \$00 \$00 CS
9141_ECU1_SID\$01_PID\$41_SPARK	\$48 \$6B \$10 \$41 \$41 \$00 \$07 \$EF \$EF CS
9141_ECU2_SID\$01_PID\$01_SPARK	\$48 \$6B \$11 \$41 \$01 \$81 \$44 \$00 \$00 CS
9141_ECU2_SID\$01_PID\$01_COMP	\$48 \$6B \$11 \$41 \$01 \$81 \$4C \$00 \$00 CS
9141_ECU2_SID\$01_PID\$0C_KOEO	\$48 \$6B \$11 \$41 \$0C \$00 \$00 CS
9141_SID\$02_PID\$02	\$68 \$6A \$F1 \$02 \$02 \$00 CS
9141_ECU1_SID\$02_PID\$02_NoDTC	\$48 \$6B \$10 \$42 \$02 \$00 \$00 \$00 CS
9141_ECU2_SID\$02_PID\$02_NoDTC	\$48 \$6B \$11 \$42 \$02 \$00 \$00 \$00 CS
9141_ECU1_SID\$02_PID\$02_DTC	\$48 \$6B \$10 \$42 \$02 \$00 \$01 \$07 CS
9141_SID\$03	\$68 \$6A \$F1 \$03 CS
9141_ECU1_SID\$03_NoDTC	\$48 \$6B \$10 \$43 \$00 CS
9141_ECU1_SID\$03_DTC_M1	\$48 \$6B \$10 \$43 \$01 \$07 \$01 \$10 \$01 \$15 CS
9141_ECU1_SID\$03_DTC_M2	\$48 \$6B \$10 \$43 \$01 \$24 \$00 \$00 \$00 \$00 CS

9141 ECU2 SID\$03 NoDTC	\$48 \$6B \$11 \$43 \$00 CS
9141 SID\$04	\$68 \$6A \$F1 \$04CS
9141 ECU1 SID\$04 DONE	\$48 \$6B \$10 \$44 CS
9141 ECU1 SID\$04 NRC22	\$48 \$6B \$10 \$7F \$44 \$22 CS
9141 ECU1 SID\$04 NRC78	\$48 \$6B \$10 \$7F \$44 \$78 CS
9141 ECU2 SID\$04 DONE	\$48 \$6B \$11 \$44 CS
9141 ECU2 SID\$04 NRC22	\$48 \$6B \$11 \$7F \$44 \$22 CS
9141 SID\$05 TID\$01	\$68 \$6A \$F1 \$05 \$01 \$02 CS
9141 ECU1 SID\$05 TID\$01	\$48 \$6B \$10 \$45 \$01 \$02 \$00 \$5A CS
9141 SID\$06 MID\$00	\$68 \$6A \$F1 \$06 \$00 CS
9141 ECU1 SID\$06 MID\$00	\$48 \$6B \$10 \$46 \$00 \$CC \$00 \$00 \$00 CS
9141 SID\$07	\$68 \$6A \$F1 \$07 CS
9141 ECU1 SID\$07 NoDTC	\$48 \$6B \$10 \$47 \$00 CS
9141 ECU1 SID\$07 DTC M1	\$48 \$6B \$10 \$47 \$01 \$07 \$01 \$10 \$01 \$15 CS
9141 ECU1 SID\$07 DTC M2	\$48 \$6B \$10 \$47 \$01 \$24 \$00 \$00 \$00 \$00 CS
9141 ECU2 SID\$07 NoDTC	\$48 \$6B \$11 \$47 \$00 CS
9141 SID\$08 TID\$00	\$68 \$6A \$F1 \$08 CS
9141 ECU1 SID\$08 TID\$00	\$48 \$6B \$10 \$48 \$00 \$80 \$00 \$00 \$00 CS
9141 SID\$09 INF\$00	\$68 \$6A \$F1 \$09 \$00 CS
9141 SID\$09 INF\$01	\$68 \$6A \$F1 \$09 \$01 CS
9141 SID\$09 INF\$02	\$68 \$6A \$F1 \$09 \$02 CS
9141 SID\$09 INF\$03	\$68 \$6A \$F1 \$09 \$03 CS
9141 SID\$09 INF\$04	\$68 \$6A \$F1 \$09 \$04 CS
9141 SID\$09 INF\$05	\$68 \$6A \$F1 \$09 \$05 CS
9141 SID\$09 INF\$06	\$68 \$6A \$F1 \$09 \$06 CS
9141 SID\$09 INF\$07	\$68 \$6A \$F1 \$09 \$07 CS
9141 SID\$09 INF\$08	\$68 \$6A \$F1 \$09 \$08 CS
9141 SID\$09 INF\$0B	\$68 \$6A \$F1 \$09 \$0B CS
9141 ECU1 SID\$09 INF\$00	\$48 \$6B \$10 \$49 \$00 \$FF \$00 \$00 \$00 CS
9141 ECU1 SID\$09 INF\$00 DIESEL	\$48 \$6B \$10 \$49 \$00 \$FE \$20 \$00 \$00 CS
9141 ECU1 SID\$09 INF\$01	\$48 \$6B \$10 \$49 \$01 \$05 CS
9141 ECU1 SID\$09 INF\$02 M1	\$48 \$6B \$10 \$49 \$02 \$01 \$00 \$00 \$00 \$31 CS
9141 ECU1 SID\$09 INF\$02 M2	\$48 \$6B \$10 \$49 \$02 \$02 \$47 \$59 \$44 \$45 CS
9141 ECU1 SID\$09 INF\$02 M3	\$48 \$6B \$10 \$49 \$02 \$03 \$36 \$33 \$41 \$37 CS
9141 ECU1 SID\$09 INF\$02 M4	\$48 \$6B \$10 \$49 \$02 \$04 \$41 \$30 \$30 \$30 CS
9141 ECU1 SID\$09 INF\$02 M5	\$48 \$6B \$10 \$49 \$02 \$05 \$30 \$30 \$30 \$30 CS
9141 ECU1 SID\$09 INF\$03	\$48 \$6B \$10 \$49 \$03 \$04 CS
9141 ECU1 SID\$09 INF\$03 9	\$48 \$6B \$10 \$49 \$03 \$24 CS
9141 ECU1 SID\$09 INF\$04 M1	\$48 \$6B \$10 \$49 \$04 \$01 \$31 \$32 \$35 \$38 CS
9141 ECU1 SID\$09 INF\$04 M2	\$48 \$6B \$10 \$49 \$04 \$02 \$39 \$37 \$37 \$32 CS
9141 ECU1 SID\$09 INF\$04 M3	\$48 \$6B \$10 \$49 \$04 \$03 \$00 \$00 \$00 \$00 CS
9141 ECU1 SID\$09 INF\$04 M4	\$48 \$6B \$10 \$49 \$04 \$04 \$00 \$00 \$00 \$00 CS
9141 ECU1 SID\$09 INF\$04 9 M1	\$48 \$6B \$10 \$49 \$04 \$01 \$31 \$31 \$35 \$38 CS
9141 ECU1 SID\$09 INF\$04 9 M2	\$48 \$6B \$10 \$49 \$04 \$02 \$39 \$37 \$37 \$38 CS
9141 ECU1 SID\$09 INF\$04 9 M3	\$48 \$6B \$10 \$49 \$04 \$03 \$00 \$00 \$00 \$00 CS
9141 ECU1 SID\$09 INF\$04 9 M4	\$48 \$6B \$10 \$49 \$04 \$04 \$00 \$00 \$00 \$00 CS
9141 ECU1 SID\$09 INF\$04 9 M5	\$48 \$6B \$10 \$49 \$04 \$05 \$32 \$31 \$35 \$38 CS
9141 ECU1 SID\$09 INF\$04 9 M6	\$48 \$6B \$10 \$49 \$04 \$06 \$39 \$37 \$37 \$37 CS
9141 ECU1 SID\$09 INF\$04 9 M7	\$48 \$6B \$10 \$49 \$04 \$07 \$00 \$00 \$00 \$00 CS
9141 ECU1 SID\$09 INF\$04 9 M8	\$48 \$6B \$10 \$49 \$04 \$08 \$00 \$00 \$00 \$00 CS
9141 ECU1 SID\$09 INF\$04 9 M9	\$48 \$6B \$10 \$49 \$04 \$09 \$33 \$31 \$35 \$38 CS
9141 ECU1 SID\$09 INF\$04 9 M10	\$48 \$6B \$10 \$49 \$04 \$0A \$39 \$37 \$37 \$36 CS
9141 ECU1 SID\$09 INF\$04 9 M11	\$48 \$6B \$10 \$49 \$04 \$0B \$00 \$00 \$00 \$00 CS
9141 ECU1 SID\$09 INF\$04 9 M12	\$48 \$6B \$10 \$49 \$04 \$0C \$00 \$00 \$00 \$00 CS
9141 ECU1 SID\$09 INF\$04 9 M13	\$48 \$6B \$10 \$49 \$04 \$0D \$34 \$31 \$35 \$38 CS
9141 ECU1 SID\$09 INF\$04 9 M14	\$48 \$6B \$10 \$49 \$04 \$0E \$39 \$37 \$37 \$35 CS
9141 ECU1 SID\$09 INF\$04 9 M15	\$48 \$6B \$10 \$49 \$04 \$0F \$00 \$00 \$00 \$00 CS
9141 ECU1 SID\$09 INF\$04 9 M16	\$48 \$6B \$10 \$49 \$04 \$10 \$00 \$00 \$00 \$00 CS

9141 ECU1 SID\$09 INF\$04 9 M17	\$48 \$6B \$10 \$49 \$04 \$11 \$35 \$31 \$35 \$38 CS
9141 ECU1 SID\$09 INF\$04 9 M18	\$48 \$6B \$10 \$49 \$04 \$12 \$39 \$37 \$37 \$34 CS
9141 ECU1 SID\$09 INF\$04 9 M19	\$48 \$6B \$10 \$49 \$04 \$13 \$00 \$00 \$00 \$00 CS
9141 ECU1 SID\$09 INF\$04 9 M20	\$48 \$6B \$10 \$49 \$04 \$14 \$00 \$00 \$00 \$00 CS
9141 ECU1 SID\$09 INF\$04 9 M21	\$48 \$6B \$10 \$49 \$04 \$15 \$36 \$31 \$35 \$38 CS
9141 ECU1 SID\$09 INF\$04 9 M22	\$48 \$6B \$10 \$49 \$04 \$16 \$39 \$37 \$37 \$33 CS
9141 ECU1 SID\$09 INF\$04 9 M23	\$48 \$6B \$10 \$49 \$04 \$17 \$00 \$00 \$00 \$00 CS
9141 ECU1 SID\$09 INF\$04 9 M24	\$48 \$6B \$10 \$49 \$04 \$18 \$00 \$00 \$00 \$00 CS
9141 ECU1 SID\$09 INF\$04 9 M25	\$48 \$6B \$10 \$49 \$04 \$19 \$37 \$31 \$35 \$38 CS
9141 ECU1 SID\$09 INF\$04 9 M26	\$48 \$6B \$10 \$49 \$04 \$1A \$39 \$37 \$37 \$32 CS
9141 ECU1 SID\$09 INF\$04 9 M27	\$48 \$6B \$10 \$49 \$04 \$1B \$00 \$00 \$00 \$00 CS
9141 ECU1 SID\$09 INF\$04 9 M28	\$48 \$6B \$10 \$49 \$04 \$1C \$00 \$00 \$00 \$00 CS
9141 ECU1 SID\$09 INF\$04 9 M29	\$48 \$6B \$10 \$49 \$04 \$1D \$38 \$31 \$35 \$38 CS
9141 ECU1 SID\$09 INF\$04 9 M30	\$48 \$6B \$10 \$49 \$04 \$1E \$39 \$37 \$37 \$31 CS
9141 ECU1 SID\$09 INF\$04 9 M31	\$48 \$6B \$10 \$49 \$04 \$1F \$00 \$00 \$00 \$00 CS
9141 ECU1 SID\$09 INF\$04 9 M32	\$48 \$6B \$10 \$49 \$04 \$20 \$00 \$00 \$00 \$00 CS
9141 ECU1 SID\$09 INF\$04 9 M33	\$48 \$6B \$10 \$49 \$04 \$21 \$39 \$31 \$35 \$38 CS
9141 ECU1 SID\$09 INF\$04 9 M34	\$48 \$6B \$10 \$49 \$04 \$22 \$39 \$37 \$37 \$30 CS
9141 ECU1 SID\$09 INF\$04 9 M35	\$48 \$6B \$10 \$49 \$04 \$23 \$00 \$00 \$00 \$00 CS
9141 ECU1 SID\$09 INF\$04 9 M36	\$48 \$6B \$10 \$49 \$04 \$24 \$00 \$00 \$00 \$00 CS
9141 ECU1 SID\$09 INF\$05	\$48 \$6B \$10 \$49 \$05 \$01 CS
9141 ECU1 SID\$09 INF\$05 9	\$48 \$6B \$10 \$49 \$05 \$09 CS
9141 ECU1 SID\$09 INF\$06	\$48 \$6B \$10 \$49 \$06 \$01 \$17 \$91 \$BC \$88 CS
9141 ECU1 SID\$09 INF\$06 9 M1	\$48 \$6B \$10 \$49 \$06 \$01 \$17 \$91 \$BC \$88 CS
9141 ECU1 SID\$09 INF\$06 9 M2	\$48 \$6B \$10 \$49 \$06 \$02 \$18 \$91 \$BC \$87 CS
9141 ECU1 SID\$09 INF\$06 9 M3	\$48 \$6B \$10 \$49 \$06 \$03 \$19 \$91 \$BC \$86 CS
9141 ECU1 SID\$09 INF\$06 9 M4	\$48 \$6B \$10 \$49 \$06 \$04 \$1A \$91 \$BC \$85 CS
9141 ECU1 SID\$09 INF\$06 9 M5	\$48 \$6B \$10 \$49 \$06 \$05 \$1B \$91 \$BC \$84 CS
9141 ECU1 SID\$09 INF\$06 9 M6	\$48 \$6B \$10 \$49 \$06 \$06 \$1C \$91 \$BC \$83 CS
9141 ECU1 SID\$09 INF\$06 9 M7	\$48 \$6B \$10 \$49 \$06 \$07 \$1D \$91 \$BC \$82 CS
9141 ECU1 SID\$09 INF\$06 9 M8	\$48 \$6B \$10 \$49 \$06 \$08 \$1E \$55 \$AA \$81 CS
9141 ECU1 SID\$09 INF\$06 9 M9	\$48 \$6B \$10 \$49 \$06 \$09 \$1F \$FF \$00 \$80 CS
9141 ECU1 SID\$09 INF\$07 832	\$48 \$6B \$10 \$49 \$07 \$08 CS
9141 ECU1 SID\$09 INF\$07 840	\$48 \$6B \$10 \$49 \$07 \$0A CS
9141 ECU1 SID\$09 INF\$07 B32	\$48 \$6B \$10 \$49 \$07 \$08 CS
9141 ECU1 SID\$09 INF\$07 B36	\$48 \$6B \$10 \$49 \$07 \$09 CS
9141 ECU1 SID\$09 INF\$08 32 M1	\$48 \$6B \$10 \$49 \$08 \$01 \$00 \$64 \$00 \$69 CS
9141 ECU1 SID\$09 INF\$08 32 M2	\$48 \$6B \$10 \$49 \$08 \$02 \$00 \$0A \$00 \$0F CS
9141 ECU1 SID\$09 INF\$08 32 M3	\$48 \$6B \$10 \$49 \$08 \$03 \$00 \$00 \$00 \$00 CS
9141 ECU1 SID\$09 INF\$08 32 M4	\$48 \$6B \$10 \$49 \$08 \$04 \$00 \$1E \$00 \$23 CS
9141 ECU1 SID\$09 INF\$08 32 M5	\$48 \$6B \$10 \$49 \$08 \$05 \$00 \$00 \$00 \$00 CS
9141 ECU1 SID\$09 INF\$08 32 M6	\$48 \$6B \$10 \$49 \$08 \$06 \$00 \$32 \$00 \$37 CS
9141 ECU1 SID\$09 INF\$08 32 M7	\$48 \$6B \$10 \$49 \$08 \$07 \$00 \$3C \$00 \$41 CS
9141 ECU1 SID\$09 INF\$08 32 M8	\$48 \$6B \$10 \$49 \$08 \$08 \$00 \$46 \$00 \$4B CS
9141 ECU1 SID\$09 INF\$08 40 M1	\$48 \$6B \$10 \$49 \$08 \$01 \$00 \$64 \$00 \$69 CS
9141 ECU1 SID\$09 INF\$08 40 M2	\$48 \$6B \$10 \$49 \$08 \$02 \$00 \$0A \$00 \$0F CS
9141 ECU1 SID\$09 INF\$08 40 M3	\$48 \$6B \$10 \$49 \$08 \$03 \$00 \$00 \$00 \$00 CS
9141 ECU1 SID\$09 INF\$08 40 M4	\$48 \$6B \$10 \$49 \$08 \$04 \$00 \$1E \$00 \$23 CS
9141 ECU1 SID\$09 INF\$08 40 M5	\$48 \$6B \$10 \$49 \$08 \$05 \$00 \$00 \$00 \$00 CS
9141 ECU1 SID\$09 INF\$08 40 M6	\$48 \$6B \$10 \$49 \$08 \$06 \$00 \$32 \$00 \$37 CS
9141 ECU1 SID\$09 INF\$08 40 M7	\$48 \$6B \$10 \$49 \$08 \$07 \$00 \$3C \$00 \$41 CS
9141 ECU1 SID\$09 INF\$08 40 M8	\$48 \$6B \$10 \$49 \$08 \$08 \$00 \$46 \$00 \$4B CS
9141 ECU1 SID\$09 INF\$08 40 M9	\$48 \$6B \$10 \$49 \$08 \$09 \$00 \$5A \$00 \$5F CS
9141 ECU1 SID\$09 INF\$08 40 M10	\$48 \$6B \$10 \$49 \$08 \$0A \$00 \$00 \$00 \$00 CS
9141 ECU1 SID\$09 INF\$0B 32 M1	\$48 \$6B \$10 \$49 \$0B \$01 \$00 \$64 \$00 \$69 CS
9141 ECU1 SID\$09 INF\$0B 32 M2	\$48 \$6B \$10 \$49 \$0B \$02 \$00 \$0A \$00 \$0F CS
9141 ECU1 SID\$09 INF\$0B 32 M3	\$48 \$6B \$10 \$49 \$0B \$03 \$00 \$00 \$00 \$00 CS
9141 ECU1 SID\$09 INF\$0B 32 M4	\$48 \$6B \$10 \$49 \$0B \$04 \$00 \$00 \$00 \$00 CS

9141 ECU1 SID\$09 INF\$0B 32 M5	\$48 \$6B \$10 \$49 \$0B \$05 \$00 \$00 \$00 \$00 CS
9141 ECU1 SID\$09 INF\$0B 32 M6	\$48 \$6B \$10 \$49 \$0B \$06 \$00 \$32 \$00 \$37 CS
9141 ECU1 SID\$09 INF\$0B 32 M7	\$48 \$6B \$10 \$49 \$0B \$07 \$00 \$3C \$00 \$41 CS
9141 ECU1 SID\$09 INF\$0B 32 M8	\$48 \$6B \$10 \$49 \$0B \$08 \$00 \$46 \$00 \$4B CS
9141 ECU1 SID\$09 INF\$0B 36 M1	\$48 \$6B \$10 \$49 \$0B \$01 \$00 \$64 \$00 \$69 CS
9141 ECU1 SID\$09 INF\$0B 36 M2	\$48 \$6B \$10 \$49 \$0B \$02 \$00 \$0A \$00 \$0F CS
9141 ECU1 SID\$09 INF\$0B 36 M3	\$48 \$6B \$10 \$49 \$0B \$03 \$00 \$00 \$00 \$00 CS
9141 ECU1 SID\$09 INF\$0B 36 M4	\$48 \$6B \$10 \$49 \$0B \$04 \$00 \$00 \$00 \$00 CS
9141 ECU1 SID\$09 INF\$0B 36 M5	\$48 \$6B \$10 \$49 \$0B \$05 \$00 \$00 \$00 \$00 CS
9141 ECU1 SID\$09 INF\$0B 36 M6	\$48 \$6B \$10 \$49 \$0B \$06 \$00 \$32 \$00 \$37 CS
9141 ECU1 SID\$09 INF\$0B 36 M7	\$48 \$6B \$10 \$49 \$0B \$07 \$00 \$3C \$00 \$41 CS
9141 ECU1 SID\$09 INF\$0B 36 M8	\$48 \$6B \$10 \$49 \$0B \$08 \$00 \$46 \$00 \$4B CS
9141 ECU1 SID\$09 INF\$0B 36 M9	\$48 \$6B \$10 \$49 \$0B \$09 \$00 \$50 \$00 \$55 CS
9141 ECU1 SID\$09 NRC78	\$48 \$6B \$10 \$7F \$49 \$78 CS
9141 ECU2 SID\$09 INF\$00	\$48 \$6B \$11 \$49 \$00 \$3F \$00 \$00 \$00 CS
9141 ECU2 SID\$09 INF\$00 DIESEL	\$48 \$6B \$11 \$49 \$00 \$3E \$20 \$00 \$00 CS
9141 ECU2 SID\$09 INF\$03	\$48 \$6B \$11 \$49 \$03 \$04 CS
9141 ECU2 SID\$09 INF\$03 9	\$48 \$6B \$11 \$49 \$03 \$24 CS
9141 ECU2 SID\$09 INF\$04 M1	\$48 \$6B \$11 \$49 \$04 \$01 \$32 \$34 \$32 \$32 CS
9141 ECU2 SID\$09 INF\$04 M2	\$48 \$6B \$11 \$49 \$04 \$02 \$31 \$34 \$31 \$39 CS
9141 ECU2 SID\$09 INF\$04 M3	\$48 \$6B \$11 \$49 \$04 \$03 \$00 \$00 \$00 \$00 CS
9141 ECU2 SID\$09 INF\$04 M4	\$48 \$6B \$11 \$49 \$04 \$04 \$00 \$00 \$00 \$00 CS
9141 ECU2 SID\$09 INF\$04 9 M1	\$48 \$6B \$11 \$49 \$04 \$01 \$31 \$32 \$35 \$38 CS
9141 ECU2 SID\$09 INF\$04 9 M2	\$48 \$6B \$11 \$49 \$04 \$02 \$39 \$37 \$37 \$38 CS
9141 ECU2 SID\$09 INF\$04 9 M3	\$48 \$6B \$11 \$49 \$04 \$03 \$00 \$00 \$00 \$00 CS
9141 ECU2 SID\$09 INF\$04 9 M4	\$48 \$6B \$11 \$49 \$04 \$04 \$00 \$00 \$00 \$00 CS
9141 ECU2 SID\$09 INF\$04 9 M5	\$48 \$6B \$11 \$49 \$04 \$05 \$32 \$32 \$35 \$38 CS
9141 ECU2 SID\$09 INF\$04 9 M6	\$48 \$6B \$11 \$49 \$04 \$06 \$39 \$37 \$37 \$37 CS
9141 ECU2 SID\$09 INF\$04 9 M7	\$48 \$6B \$11 \$49 \$04 \$07 \$00 \$00 \$00 \$00 CS
9141 ECU2 SID\$09 INF\$04 9 M8	\$48 \$6B \$11 \$49 \$04 \$08 \$00 \$00 \$00 \$00 CS
9141 ECU2 SID\$09 INF\$04 9 M9	\$48 \$6B \$11 \$49 \$04 \$09 \$33 \$32 \$35 \$38 CS
9141 ECU2 SID\$09 INF\$04 9 M10	\$48 \$6B \$11 \$49 \$04 \$0A \$39 \$37 \$37 \$36 CS
9141 ECU2 SID\$09 INF\$04 9 M11	\$48 \$6B \$11 \$49 \$04 \$0B \$00 \$00 \$00 \$00 CS
9141 ECU2 SID\$09 INF\$04 9 M12	\$48 \$6B \$11 \$49 \$04 \$0C \$00 \$00 \$00 \$00 CS
9141 ECU2 SID\$09 INF\$04 9 M13	\$48 \$6B \$11 \$49 \$04 \$0D \$34 \$32 \$35 \$38 CS
9141 ECU2 SID\$09 INF\$04 9 M14	\$48 \$6B \$11 \$49 \$04 \$0E \$39 \$37 \$37 \$35 CS
9141 ECU2 SID\$09 INF\$04 9 M15	\$48 \$6B \$11 \$49 \$04 \$0F \$00 \$00 \$00 \$00 CS
9141 ECU2 SID\$09 INF\$04 9 M16	\$48 \$6B \$11 \$49 \$04 \$10 \$00 \$00 \$00 \$00 CS
9141 ECU2 SID\$09 INF\$04 9 M17	\$48 \$6B \$11 \$49 \$04 \$11 \$35 \$32 \$35 \$38 CS
9141 ECU2 SID\$09 INF\$04 9 M18	\$48 \$6B \$11 \$49 \$04 \$12 \$39 \$37 \$37 \$34 CS
9141 ECU2 SID\$09 INF\$04 9 M19	\$48 \$6B \$11 \$49 \$04 \$13 \$00 \$00 \$00 \$00 CS
9141 ECU2 SID\$09 INF\$04 9 M20	\$48 \$6B \$11 \$49 \$04 \$14 \$00 \$00 \$00 \$00 CS
9141 ECU2 SID\$09 INF\$04 9 M21	\$48 \$6B \$11 \$49 \$04 \$15 \$36 \$32 \$35 \$38 CS
9141 ECU2 SID\$09 INF\$04 9 M22	\$48 \$6B \$11 \$49 \$04 \$16 \$39 \$37 \$37 \$33 CS
9141 ECU2 SID\$09 INF\$04 9 M23	\$48 \$6B \$11 \$49 \$04 \$17 \$00 \$00 \$00 \$00 CS
9141 ECU2 SID\$09 INF\$04 9 M24	\$48 \$6B \$11 \$49 \$04 \$18 \$00 \$00 \$00 \$00 CS
9141 ECU2 SID\$09 INF\$04 9 M25	\$48 \$6B \$11 \$49 \$04 \$19 \$37 \$32 \$35 \$38 CS
9141 ECU2 SID\$09 INF\$04 9 M26	\$48 \$6B \$11 \$49 \$04 \$1A \$39 \$37 \$37 \$32 CS
9141 ECU2 SID\$09 INF\$04 9 M27	\$48 \$6B \$11 \$49 \$04 \$1B \$00 \$00 \$00 \$00 CS
9141 ECU2 SID\$09 INF\$04 9 M28	\$48 \$6B \$11 \$49 \$04 \$1C \$00 \$00 \$00 \$00 CS
9141 ECU2 SID\$09 INF\$04 9 M29	\$48 \$6B \$11 \$49 \$04 \$1D \$38 \$32 \$35 \$38 CS
9141 ECU2 SID\$09 INF\$04 9 M30	\$48 \$6B \$11 \$49 \$04 \$1E \$39 \$37 \$37 \$31 CS
9141 ECU2 SID\$09 INF\$04 9 M31	\$48 \$6B \$11 \$49 \$04 \$1F \$00 \$00 \$00 \$00 CS
9141 ECU2 SID\$09 INF\$04 9 M32	\$48 \$6B \$11 \$49 \$04 \$20 \$00 \$00 \$00 \$00 CS
9141 ECU2 SID\$09 INF\$04 9 M33	\$48 \$6B \$11 \$49 \$04 \$21 \$39 \$32 \$35 \$38 CS
9141 ECU2 SID\$09 INF\$04 9 M34	\$48 \$6B \$11 \$49 \$04 \$22 \$39 \$37 \$37 \$30 CS
9141 ECU2 SID\$09 INF\$04 9 M35	\$48 \$6B \$11 \$49 \$04 \$23 \$00 \$00 \$00 \$00 CS
9141 ECU2 SID\$09 INF\$04 9 M36	\$48 \$6B \$11 \$49 \$04 \$24 \$00 \$00 \$00 \$00 CS

9141 ECU2 SID\$09 INF\$05	\$48 \$6B \$11 \$49 \$05 \$01 CS
9141 ECU2 SID\$09 INF\$05 9	\$48 \$6B \$11 \$49 \$05 \$09 CS
9141 ECU1 SID\$09 INF\$06	\$48 \$6B \$11 \$49 \$06 \$01 \$27 \$91 \$BC \$88 CS
9141 ECU2 SID\$09 INF\$06 9 M1	\$48 \$6B \$11 \$49 \$06 \$01 \$27 \$91 \$BC \$88 CS
9141 ECU2 SID\$09 INF\$06 9 M2	\$48 \$6B \$11 \$49 \$06 \$02 \$28 \$91 \$BC \$87 CS
9141 ECU2 SID\$09 INF\$06 9 M3	\$48 \$6B \$11 \$49 \$06 \$03 \$29 \$91 \$BC \$86 CS
9141 ECU2 SID\$09 INF\$06 9 M4	\$48 \$6B \$11 \$49 \$06 \$04 \$2A \$91 \$BC \$85 CS
9141 ECU2 SID\$09 INF\$06 9 M5	\$48 \$6B \$11 \$49 \$06 \$05 \$2B \$91 \$BC \$84 CS
9141 ECU2 SID\$09 INF\$06 9 M6	\$48 \$6B \$11 \$49 \$06 \$06 \$2C \$91 \$BC \$83 CS
9141 ECU2 SID\$09 INF\$06 9 M7	\$48 \$6B \$11 \$49 \$06 \$07 \$2D \$91 \$BC \$82 CS
9141 ECU2 SID\$09 INF\$06 9 M8	\$48 \$6B \$11 \$49 \$06 \$08 \$2E \$55 \$AA \$81 CS
9141 ECU2 SID\$09 INF\$06 9 M9	\$48 \$6B \$11 \$49 \$06 \$09 \$2F \$FF \$00 \$80 CS
9141 ECU2 SID\$09 INF\$07 832	\$48 \$6B \$11 \$49 \$07 \$08 CS
9141 ECU2 SID\$09 INF\$07 840	\$48 \$6B \$11 \$49 \$07 \$0A CS
9141 ECU2 SID\$09 INF\$07 B32	\$48 \$6B \$11 \$49 \$07 \$08 CS
9141 ECU2 SID\$09 INF\$07 B36	\$48 \$6B \$11 \$49 \$07 \$09 CS
9141 ECU2 SID\$09 INF\$08 32 M1	\$48 \$6B \$11 \$49 \$08 \$01 \$00 \$64 \$00 \$69 CS
9141 ECU2 SID\$09 INF\$08 32 M2	\$48 \$6B \$11 \$49 \$08 \$02 \$00 \$00 \$00 \$00 CS
9141 ECU2 SID\$09 INF\$08 32 M3	\$48 \$6B \$11 \$49 \$08 \$03 \$00 \$14 \$00 \$19 CS
9141 ECU2 SID\$09 INF\$08 32 M4	\$48 \$6B \$11 \$49 \$08 \$04 \$00 \$00 \$00 \$00 CS
9141 ECU2 SID\$09 INF\$08 32 M5	\$48 \$6B \$11 \$49 \$08 \$05 \$00 \$28 \$00 \$2D CS
9141 ECU2 SID\$09 INF\$08 32 M6	\$48 \$6B \$11 \$49 \$08 \$06 \$00 \$00 \$00 \$00 CS
9141 ECU2 SID\$09 INF\$08 32 M7	\$48 \$6B \$11 \$49 \$08 \$07 \$00 \$00 \$00 \$00 CS
9141 ECU2 SID\$09 INF\$08 32 M8	\$48 \$6B \$11 \$49 \$08 \$08 \$00 \$00 \$00 \$00 CS
9141 ECU2 SID\$09 INF\$08 40 M1	\$48 \$6B \$11 \$49 \$08 \$01 \$00 \$64 \$00 \$69 CS
9141 ECU2 SID\$09 INF\$08 40 M2	\$48 \$6B \$11 \$49 \$08 \$02 \$00 \$00 \$00 \$00 CS
9141 ECU2 SID\$09 INF\$08 40 M3	\$48 \$6B \$11 \$49 \$08 \$03 \$00 \$14 \$00 \$19 CS
9141 ECU2 SID\$09 INF\$08 40 M4	\$48 \$6B \$11 \$49 \$08 \$04 \$00 \$00 \$00 \$00 CS
9141 ECU2 SID\$09 INF\$08 40 M5	\$48 \$6B \$11 \$49 \$08 \$05 \$00 \$28 \$00 \$2D CS
9141 ECU2 SID\$09 INF\$08 40 M6	\$48 \$6B \$11 \$49 \$08 \$06 \$00 \$00 \$00 \$00 CS
9141 ECU2 SID\$09 INF\$08 40 M7	\$48 \$6B \$11 \$49 \$08 \$07 \$00 \$00 \$00 \$00 CS
9141 ECU2 SID\$09 INF\$08 40 M8	\$48 \$6B \$11 \$49 \$08 \$08 \$00 \$00 \$00 \$00 CS
9141 ECU2 SID\$09 INF\$08 40 M9	\$48 \$6B \$11 \$49 \$08 \$09 \$00 \$00 \$00 \$00 CS
9141 ECU2 SID\$09 INF\$08 40 M10	\$48 \$6B \$11 \$49 \$08 \$0A \$00 \$5A \$00 \$5F CS
9141 ECU2 SID\$09 INF\$0B 32 M1	\$48 \$6B \$11 \$49 \$0B \$01 \$00 \$64 \$00 \$69 CS
9141 ECU2 SID\$09 INF\$0B 32 M2	\$48 \$6B \$11 \$49 \$0B \$02 \$00 \$00 \$00 \$00 CS
9141 ECU2 SID\$09 INF\$0B 32 M3	\$48 \$6B \$11 \$49 \$0B \$03 \$00 \$14 \$00 \$19 CS
9141 ECU2 SID\$09 INF\$0B 32 M4	\$48 \$6B \$11 \$49 \$0B \$04 \$00 \$00 \$00 \$00 CS
9141 ECU2 SID\$09 INF\$0B 32 M5	\$48 \$6B \$11 \$49 \$0B \$05 \$00 \$28 \$00 \$2D CS
9141 ECU2 SID\$09 INF\$0B 32 M6	\$48 \$6B \$11 \$49 \$0B \$06 \$00 \$00 \$00 \$00 CS
9141 ECU2 SID\$09 INF\$0B 32 M7	\$48 \$6B \$11 \$49 \$0B \$07 \$00 \$00 \$00 \$00 CS
9141 ECU2 SID\$09 INF\$0B 32 M8	\$48 \$6B \$11 \$49 \$0B \$08 \$00 \$00 \$00 \$00 CS
9141 ECU2 SID\$09 INF\$0B 36 M1	\$48 \$6B \$11 \$49 \$0B \$01 \$00 \$64 \$00 \$69 CS
9141 ECU2 SID\$09 INF\$0B 36 M2	\$48 \$6B \$11 \$49 \$0B \$02 \$00 \$00 \$00 \$00 CS
9141 ECU2 SID\$09 INF\$0B 36 M3	\$48 \$6B \$11 \$49 \$0B \$03 \$00 \$14 \$00 \$19 CS
9141 ECU2 SID\$09 INF\$0B 36 M4	\$48 \$6B \$11 \$49 \$0B \$04 \$00 \$00 \$00 \$00 CS
9141 ECU2 SID\$09 INF\$0B 36 M5	\$48 \$6B \$11 \$49 \$0B \$05 \$00 \$28 \$00 \$2D CS
9141 ECU2 SID\$09 INF\$0B 36 M6	\$48 \$6B \$11 \$49 \$0B \$06 \$00 \$00 \$00 \$00 CS
9141 ECU2 SID\$09 INF\$0B 36 M7	\$48 \$6B \$11 \$49 \$0B \$07 \$00 \$00 \$00 \$00 CS
9141 ECU2 SID\$09 INF\$0B 36 M8	\$48 \$6B \$11 \$49 \$0B \$08 \$00 \$00 \$00 \$00 CS
9141 ECU2 SID\$09 INF\$0B 36 M9	\$48 \$6B \$11 \$49 \$0B \$09 \$00 \$00 \$00 \$00 CS
9141 ECU3 SID\$09 INF\$00	\$48 \$6B \$12 \$49 \$00 \$3C \$00 \$00 \$00 CS
9141 ECU3 SID\$09 INF\$03 9	\$48 \$6B \$12 \$49 \$03 \$24 CS
9141 ECU3 SID\$09 INF\$04 9 M1	\$48 \$6B \$12 \$49 \$04 \$01 \$31 \$33 \$35 \$38 CS
9141 ECU3 SID\$09 INF\$04 9 M2	\$48 \$6B \$12 \$49 \$04 \$02 \$39 \$37 \$37 \$38 CS
9141 ECU3 SID\$09 INF\$04 9 M3	\$48 \$6B \$12 \$49 \$04 \$03 \$00 \$00 \$00 \$00 CS
9141 ECU3 SID\$09 INF\$04 9 M4	\$48 \$6B \$12 \$49 \$04 \$04 \$00 \$00 \$00 \$00 CS
9141 ECU3 SID\$09 INF\$04 9 M5	\$48 \$6B \$12 \$49 \$04 \$05 \$32 \$33 \$35 \$38 CS

9141 ECU3 SID\$09 INF\$04 9 M6	\$48 \$6B \$12 \$49 \$04 \$06 \$39 \$37 \$37 \$37 CS
9141 ECU3 SID\$09 INF\$04 9 M7	\$48 \$6B \$12 \$49 \$04 \$07 \$00 \$00 \$00 \$00 CS
9141 ECU3 SID\$09 INF\$04 9 M8	\$48 \$6B \$12 \$49 \$04 \$08 \$00 \$00 \$00 \$00 CS
9141 ECU3 SID\$09 INF\$04 9 M9	\$48 \$6B \$12 \$49 \$04 \$09 \$33 \$33 \$35 \$38 CS
9141 ECU3 SID\$09 INF\$04 9 M10	\$48 \$6B \$12 \$49 \$04 \$0A \$39 \$37 \$37 \$36 CS
9141 ECU3 SID\$09 INF\$04 9 M11	\$48 \$6B \$12 \$49 \$04 \$0B \$00 \$00 \$00 \$00 CS
9141 ECU3 SID\$09 INF\$04 9 M12	\$48 \$6B \$12 \$49 \$04 \$0C \$00 \$00 \$00 \$00 CS
9141 ECU3 SID\$09 INF\$04 9 M13	\$48 \$6B \$12 \$49 \$04 \$0D \$34 \$33 \$35 \$38 CS
9141 ECU3 SID\$09 INF\$04 9 M14	\$48 \$6B \$12 \$49 \$04 \$0E \$39 \$37 \$37 \$35 CS
9141 ECU3 SID\$09 INF\$04 9 M15	\$48 \$6B \$12 \$49 \$04 \$0F \$00 \$00 \$00 \$00 CS
9141 ECU3 SID\$09 INF\$04 9 M16	\$48 \$6B \$12 \$49 \$04 \$10 \$00 \$00 \$00 \$00 CS
9141 ECU3 SID\$09 INF\$04 9 M17	\$48 \$6B \$12 \$49 \$04 \$11 \$35 \$33 \$35 \$38 CS
9141 ECU3 SID\$09 INF\$04 9 M18	\$48 \$6B \$12 \$49 \$04 \$12 \$39 \$37 \$37 \$34 CS
9141 ECU3 SID\$09 INF\$04 9 M19	\$48 \$6B \$12 \$49 \$04 \$13 \$00 \$00 \$00 \$00 CS
9141 ECU3 SID\$09 INF\$04 9 M20	\$48 \$6B \$12 \$49 \$04 \$14 \$00 \$00 \$00 \$00 CS
9141 ECU3 SID\$09 INF\$04 9 M21	\$48 \$6B \$12 \$49 \$04 \$15 \$36 \$33 \$35 \$38 CS
9141 ECU3 SID\$09 INF\$04 9 M22	\$48 \$6B \$12 \$49 \$04 \$16 \$39 \$37 \$37 \$33 CS
9141 ECU3 SID\$09 INF\$04 9 M23	\$48 \$6B \$12 \$49 \$04 \$17 \$00 \$00 \$00 \$00 CS
9141 ECU3 SID\$09 INF\$04 9 M24	\$48 \$6B \$12 \$49 \$04 \$18 \$00 \$00 \$00 \$00 CS
9141 ECU3 SID\$09 INF\$04 9 M25	\$48 \$6B \$12 \$49 \$04 \$19 \$37 \$33 \$35 \$38 CS
9141 ECU3 SID\$09 INF\$04 9 M26	\$48 \$6B \$12 \$49 \$04 \$1A \$39 \$37 \$37 \$32 CS
9141 ECU3 SID\$09 INF\$04 9 M27	\$48 \$6B \$12 \$49 \$04 \$1B \$00 \$00 \$00 \$00 CS
9141 ECU3 SID\$09 INF\$04 9 M28	\$48 \$6B \$12 \$49 \$04 \$1C \$00 \$00 \$00 \$00 CS
9141 ECU3 SID\$09 INF\$04 9 M29	\$48 \$6B \$12 \$49 \$04 \$1D \$38 \$33 \$35 \$38 CS
9141 ECU3 SID\$09 INF\$04 9 M30	\$48 \$6B \$12 \$49 \$04 \$1E \$39 \$37 \$37 \$31 CS
9141 ECU3 SID\$09 INF\$04 9 M31	\$48 \$6B \$12 \$49 \$04 \$1F \$00 \$00 \$00 \$00 CS
9141 ECU3 SID\$09 INF\$04 9 M32	\$48 \$6B \$12 \$49 \$04 \$20 \$00 \$00 \$00 \$00 CS
9141 ECU3 SID\$09 INF\$04 9 M33	\$48 \$6B \$12 \$49 \$04 \$21 \$39 \$33 \$35 \$38 CS
9141 ECU3 SID\$09 INF\$04 9 M34	\$48 \$6B \$12 \$49 \$04 \$22 \$39 \$37 \$37 \$30 CS
9141 ECU3 SID\$09 INF\$04 9 M35	\$48 \$6B \$12 \$49 \$04 \$23 \$00 \$00 \$00 \$00 CS
9141 ECU3 SID\$09 INF\$04 9 M36	\$48 \$6B \$12 \$49 \$04 \$24 \$00 \$00 \$00 \$00 CS
9141 ECU3 SID\$09 INF\$05 9	\$48 \$6B \$12 \$49 \$05 \$09 CS
9141 ECU3 SID\$09 INF\$06 9 M1	\$48 \$6B \$12 \$49 \$06 \$01 \$37 \$91 \$BC \$88 CS
9141 ECU3 SID\$09 INF\$06 9 M2	\$48 \$6B \$12 \$49 \$06 \$02 \$38 \$91 \$BC \$87 CS
9141 ECU3 SID\$09 INF\$06 9 M3	\$48 \$6B \$12 \$49 \$06 \$03 \$39 \$91 \$BC \$86 CS
9141 ECU3 SID\$09 INF\$06 9 M4	\$48 \$6B \$12 \$49 \$06 \$04 \$3A \$91 \$BC \$85 CS
9141 ECU3 SID\$09 INF\$06 9 M5	\$48 \$6B \$12 \$49 \$06 \$05 \$3B \$91 \$BC \$84 CS
9141 ECU3 SID\$09 INF\$06 9 M6	\$48 \$6B \$12 \$49 \$06 \$06 \$3C \$91 \$BC \$83 CS
9141 ECU3 SID\$09 INF\$06 9 M7	\$48 \$6B \$12 \$49 \$06 \$07 \$3D \$91 \$BC \$82 CS
9141 ECU3 SID\$09 INF\$06 9 M8	\$48 \$6B \$12 \$49 \$06 \$08 \$3E \$55 \$AA \$81 CS
9141 ECU3 SID\$09 INF\$06 9 M9	\$48 \$6B \$12 \$49 \$06 \$09 \$3F \$FF \$00 \$80 CS
9141 ECU4 SID\$09 INF\$00	\$48 \$6B \$13 \$49 \$00 \$3C \$00 \$00 \$00 CS
9141 ECU4 SID\$09 INF\$03 9	\$48 \$6B \$13 \$49 \$03 \$24 CS
9141 ECU4 SID\$09 INF\$04 9 M1	\$48 \$6B \$13 \$49 \$04 \$01 \$31 \$34 \$35 \$38 CS
9141 ECU4 SID\$09 INF\$04 9 M2	\$48 \$6B \$13 \$49 \$04 \$02 \$39 \$37 \$37 \$38 CS
9141 ECU4 SID\$09 INF\$04 9 M3	\$48 \$6B \$13 \$49 \$04 \$03 \$00 \$00 \$00 \$00 CS
9141 ECU4 SID\$09 INF\$04 9 M4	\$48 \$6B \$13 \$49 \$04 \$04 \$00 \$00 \$00 \$00 CS
9141 ECU4 SID\$09 INF\$04 9 M5	\$48 \$6B \$13 \$49 \$04 \$05 \$32 \$34 \$35 \$38 CS
9141 ECU4 SID\$09 INF\$04 9 M6	\$48 \$6B \$13 \$49 \$04 \$06 \$39 \$37 \$37 \$37 CS
9141 ECU4 SID\$09 INF\$04 9 M7	\$48 \$6B \$13 \$49 \$04 \$07 \$00 \$00 \$00 \$00 CS
9141 ECU4 SID\$09 INF\$04 9 M8	\$48 \$6B \$13 \$49 \$04 \$08 \$00 \$00 \$00 \$00 CS
9141 ECU4 SID\$09 INF\$04 9 M9	\$48 \$6B \$13 \$49 \$04 \$09 \$33 \$34 \$35 \$38 CS
9141 ECU4 SID\$09 INF\$04 9 M10	\$48 \$6B \$13 \$49 \$04 \$0A \$39 \$37 \$37 \$36 CS
9141 ECU4 SID\$09 INF\$04 9 M11	\$48 \$6B \$13 \$49 \$04 \$0B \$00 \$00 \$00 \$00 CS
9141 ECU4 SID\$09 INF\$04 9 M12	\$48 \$6B \$13 \$49 \$04 \$0C \$00 \$00 \$00 \$00 CS
9141 ECU4 SID\$09 INF\$04 9 M13	\$48 \$6B \$13 \$49 \$04 \$0D \$34 \$34 \$35 \$38 CS
9141 ECU4 SID\$09 INF\$04 9 M14	\$48 \$6B \$13 \$49 \$04 \$0E \$39 \$37 \$37 \$35 CS
9141 ECU4 SID\$09 INF\$04 9 M15	\$48 \$6B \$13 \$49 \$04 \$0F \$00 \$00 \$00 \$00 CS

9141 ECU4 SID\$09 INF\$04 9 M16	\$48 \$6B \$13 \$49 \$04 \$10 \$00 \$00 \$00 \$00 CS
9141 ECU4 SID\$09 INF\$04 9 M17	\$48 \$6B \$13 \$49 \$04 \$11 \$35 \$34 \$35 \$38 CS
9141 ECU4 SID\$09 INF\$04 9 M18	\$48 \$6B \$13 \$49 \$04 \$12 \$39 \$37 \$37 \$34 CS
9141 ECU4 SID\$09 INF\$04 9 M19	\$48 \$6B \$13 \$49 \$04 \$13 \$00 \$00 \$00 \$00 CS
9141 ECU4 SID\$09 INF\$04 9 M20	\$48 \$6B \$13 \$49 \$04 \$14 \$00 \$00 \$00 \$00 CS
9141 ECU4 SID\$09 INF\$04 9 M21	\$48 \$6B \$13 \$49 \$04 \$15 \$36 \$34 \$35 \$38 CS
9141 ECU4 SID\$09 INF\$04 9 M22	\$48 \$6B \$13 \$49 \$04 \$16 \$39 \$37 \$37 \$33 CS
9141 ECU4 SID\$09 INF\$04 9 M23	\$48 \$6B \$13 \$49 \$04 \$17 \$00 \$00 \$00 \$00 CS
9141 ECU4 SID\$09 INF\$04 9 M24	\$48 \$6B \$13 \$49 \$04 \$18 \$00 \$00 \$00 \$00 CS
9141 ECU4 SID\$09 INF\$04 9 M25	\$48 \$6B \$13 \$49 \$04 \$19 \$37 \$34 \$35 \$38 CS
9141 ECU4 SID\$09 INF\$04 9 M26	\$48 \$6B \$13 \$49 \$04 \$1A \$39 \$37 \$37 \$32 CS
9141 ECU4 SID\$09 INF\$04 9 M27	\$48 \$6B \$13 \$49 \$04 \$1B \$00 \$00 \$00 \$00 CS
9141 ECU4 SID\$09 INF\$04 9 M28	\$48 \$6B \$13 \$49 \$04 \$1C \$00 \$00 \$00 \$00 CS
9141 ECU4 SID\$09 INF\$04 9 M29	\$48 \$6B \$13 \$49 \$04 \$1D \$38 \$34 \$35 \$38 CS
9141 ECU4 SID\$09 INF\$04 9 M30	\$48 \$6B \$13 \$49 \$04 \$1E \$39 \$37 \$37 \$31 CS
9141 ECU4 SID\$09 INF\$04 9 M31	\$48 \$6B \$13 \$49 \$04 \$1F \$00 \$00 \$00 \$00 CS
9141 ECU4 SID\$09 INF\$04 9 M32	\$48 \$6B \$13 \$49 \$04 \$20 \$00 \$00 \$00 \$00 CS
9141 ECU4 SID\$09 INF\$04 9 M33	\$48 \$6B \$13 \$49 \$04 \$21 \$39 \$34 \$35 \$38 CS
9141 ECU4 SID\$09 INF\$04 9 M34	\$48 \$6B \$13 \$49 \$04 \$22 \$39 \$37 \$37 \$30 CS
9141 ECU4 SID\$09 INF\$04 9 M35	\$48 \$6B \$13 \$49 \$04 \$23 \$00 \$00 \$00 \$00 CS
9141 ECU4 SID\$09 INF\$04 9 M36	\$48 \$6B \$13 \$49 \$04 \$24 \$00 \$00 \$00 \$00 CS
9141 ECU4 SID\$09 INF\$05 9	\$48 \$6B \$13 \$49 \$05 \$09 CS
9141 ECU4 SID\$09 INF\$06 9 M1	\$48 \$6B \$13 \$49 \$06 \$01 \$47 \$91 \$BC \$88 CS
9141 ECU4 SID\$09 INF\$06 9 M2	\$48 \$6B \$13 \$49 \$06 \$02 \$48 \$91 \$BC \$87 CS
9141 ECU4 SID\$09 INF\$06 9 M3	\$48 \$6B \$13 \$49 \$06 \$03 \$49 \$91 \$BC \$86 CS
9141 ECU4 SID\$09 INF\$06 9 M4	\$48 \$6B \$13 \$49 \$06 \$04 \$4A \$91 \$BC \$85 CS
9141 ECU4 SID\$09 INF\$06 9 M5	\$48 \$6B \$13 \$49 \$06 \$05 \$4B \$91 \$BC \$84 CS
9141 ECU4 SID\$09 INF\$06 9 M6	\$48 \$6B \$13 \$49 \$06 \$06 \$4C \$91 \$BC \$83 CS
9141 ECU4 SID\$09 INF\$06 9 M7	\$48 \$6B \$13 \$49 \$06 \$07 \$4D \$91 \$BC \$82 CS
9141 ECU4 SID\$09 INF\$06 9 M8	\$48 \$6B \$13 \$49 \$06 \$08 \$4E \$55 \$AA \$81 CS
9141 ECU4 SID\$09 INF\$06 9 M9	\$48 \$6B \$13 \$49 \$06 \$09 \$4F \$FF \$00 \$80 CS
9141 ECU5 SID\$09 INF\$00	\$48 \$6B \$14 \$49 \$00 \$3C \$00 \$00 \$00 CS
9141 ECU5 SID\$09 INF\$03 9	\$48 \$6B \$14 \$49 \$03 \$24 CS
9141 ECU5 SID\$09 INF\$04 9 M1	\$48 \$6B \$14 \$49 \$04 \$01 \$31 \$35 \$35 \$38 CS
9141 ECU5 SID\$09 INF\$04 9 M2	\$48 \$6B \$14 \$49 \$04 \$02 \$39 \$37 \$37 \$38 CS
9141 ECU5 SID\$09 INF\$04 9 M3	\$48 \$6B \$14 \$49 \$04 \$03 \$00 \$00 \$00 \$00 CS
9141 ECU5 SID\$09 INF\$04 9 M4	\$48 \$6B \$14 \$49 \$04 \$04 \$00 \$00 \$00 \$00 CS
9141 ECU5 SID\$09 INF\$04 9 M5	\$48 \$6B \$14 \$49 \$04 \$05 \$32 \$35 \$35 \$38 CS
9141 ECU5 SID\$09 INF\$04 9 M6	\$48 \$6B \$14 \$49 \$04 \$06 \$39 \$37 \$37 \$37 CS
9141 ECU5 SID\$09 INF\$04 9 M7	\$48 \$6B \$14 \$49 \$04 \$07 \$00 \$00 \$00 \$00 CS
9141 ECU5 SID\$09 INF\$04 9 M8	\$48 \$6B \$14 \$49 \$04 \$08 \$00 \$00 \$00 \$00 CS
9141 ECU5 SID\$09 INF\$04 9 M9	\$48 \$6B \$14 \$49 \$04 \$09 \$33 \$35 \$35 \$38 CS
9141 ECU5 SID\$09 INF\$04 9 M10	\$48 \$6B \$14 \$49 \$04 \$0A \$39 \$37 \$37 \$36 CS
9141 ECU5 SID\$09 INF\$04 9 M11	\$48 \$6B \$14 \$49 \$04 \$0B \$00 \$00 \$00 \$00 CS
9141 ECU5 SID\$09 INF\$04 9 M12	\$48 \$6B \$14 \$49 \$04 \$0C \$00 \$00 \$00 \$00 CS
9141 ECU5 SID\$09 INF\$04 9 M13	\$48 \$6B \$14 \$49 \$04 \$0D \$34 \$35 \$35 \$38 CS
9141 ECU5 SID\$09 INF\$04 9 M14	\$48 \$6B \$14 \$49 \$04 \$0E \$39 \$37 \$37 \$35 CS
9141 ECU5 SID\$09 INF\$04 9 M15	\$48 \$6B \$14 \$49 \$04 \$0F \$00 \$00 \$00 \$00 CS
9141 ECU5 SID\$09 INF\$04 9 M16	\$48 \$6B \$14 \$49 \$04 \$10 \$00 \$00 \$00 \$00 CS
9141 ECU5 SID\$09 INF\$04 9 M17	\$48 \$6B \$14 \$49 \$04 \$11 \$35 \$35 \$35 \$38 CS
9141 ECU5 SID\$09 INF\$04 9 M18	\$48 \$6B \$14 \$49 \$04 \$12 \$39 \$37 \$37 \$34 CS
9141 ECU5 SID\$09 INF\$04 9 M19	\$48 \$6B \$14 \$49 \$04 \$13 \$00 \$00 \$00 \$00 CS
9141 ECU5 SID\$09 INF\$04 9 M20	\$48 \$6B \$14 \$49 \$04 \$14 \$00 \$00 \$00 \$00 CS
9141 ECU5 SID\$09 INF\$04 9 M21	\$48 \$6B \$14 \$49 \$04 \$15 \$36 \$35 \$35 \$38 CS
9141 ECU5 SID\$09 INF\$04 9 M22	\$48 \$6B \$14 \$49 \$04 \$16 \$39 \$37 \$37 \$33 CS
9141 ECU5 SID\$09 INF\$04 9 M23	\$48 \$6B \$14 \$49 \$04 \$17 \$00 \$00 \$00 \$00 CS
9141 ECU5 SID\$09 INF\$04 9 M24	\$48 \$6B \$14 \$49 \$04 \$18 \$00 \$00 \$00 \$00 CS
9141 ECU5 SID\$09 INF\$04 9 M25	\$48 \$6B \$14 \$49 \$04 \$19 \$37 \$35 \$35 \$38 CS

9141 ECU5 SID\$09 INF\$04 9 M26	\$48 \$6B \$14 \$49 \$04 \$1A \$39 \$37 \$37 \$32 CS
9141 ECU5 SID\$09 INF\$04 9 M27	\$48 \$6B \$14 \$49 \$04 \$1B \$00 \$00 \$00 \$00 CS
9141 ECU5 SID\$09 INF\$04 9 M28	\$48 \$6B \$14 \$49 \$04 \$1C \$00 \$00 \$00 \$00 CS
9141 ECU5 SID\$09 INF\$04 9 M29	\$48 \$6B \$14 \$49 \$04 \$1D \$38 \$35 \$35 \$38 CS
9141 ECU5 SID\$09 INF\$04 9 M30	\$48 \$6B \$14 \$49 \$04 \$1E \$39 \$37 \$37 \$31 CS
9141 ECU5 SID\$09 INF\$04 9 M31	\$48 \$6B \$14 \$49 \$04 \$1F \$00 \$00 \$00 \$00 CS
9141 ECU5 SID\$09 INF\$04 9 M32	\$48 \$6B \$14 \$49 \$04 \$20 \$00 \$00 \$00 \$00 CS
9141 ECU5 SID\$09 INF\$04 9 M33	\$48 \$6B \$14 \$49 \$04 \$21 \$39 \$35 \$35 \$38 CS
9141 ECU5 SID\$09 INF\$04 9 M34	\$48 \$6B \$14 \$49 \$04 \$22 \$39 \$37 \$37 \$30 CS
9141 ECU5 SID\$09 INF\$04 9 M35	\$48 \$6B \$14 \$49 \$04 \$23 \$00 \$00 \$00 \$00 CS
9141 ECU5 SID\$09 INF\$04 9 M36	\$48 \$6B \$14 \$49 \$04 \$24 \$00 \$00 \$00 \$00 CS
9141 ECU5 SID\$09 INF\$05 9	\$48 \$6B \$14 \$49 \$05 \$09 CS
9141 ECU5 SID\$09 INF\$06 9 M1	\$48 \$6B \$14 \$49 \$06 \$01 \$57 \$91 \$BC \$88 CS
9141 ECU5 SID\$09 INF\$06 9 M2	\$48 \$6B \$14 \$49 \$06 \$02 \$58 \$91 \$BC \$87 CS
9141 ECU5 SID\$09 INF\$06 9 M3	\$48 \$6B \$14 \$49 \$06 \$03 \$59 \$91 \$BC \$86 CS
9141 ECU5 SID\$09 INF\$06 9 M4	\$48 \$6B \$14 \$49 \$06 \$04 \$5A \$91 \$BC \$85 CS
9141 ECU5 SID\$09 INF\$06 9 M5	\$48 \$6B \$14 \$49 \$06 \$05 \$5B \$91 \$BC \$84 CS
9141 ECU5 SID\$09 INF\$06 9 M6	\$48 \$6B \$14 \$49 \$06 \$06 \$5C \$91 \$BC \$83 CS
9141 ECU5 SID\$09 INF\$06 9 M7	\$48 \$6B \$14 \$49 \$06 \$07 \$5D \$91 \$BC \$82 CS
9141 ECU5 SID\$09 INF\$06 9 M8	\$48 \$6B \$14 \$49 \$06 \$08 \$5E \$55 \$AA \$81 CS
9141 ECU5 SID\$09 INF\$06 9 M9	\$48 \$6B \$14 \$49 \$06 \$09 \$5F \$FF \$00 \$80 CS
9141 ECU6 SID\$09 INF\$00	\$48 \$6B \$15 \$49 \$00 \$3C \$00 \$00 \$00 CS
9141 ECU6 SID\$09 INF\$03 9	\$48 \$6B \$15 \$49 \$03 \$24 CS
9141 ECU6 SID\$09 INF\$04 9 M1	\$48 \$6B \$15 \$49 \$04 \$01 \$31 \$36 \$35 \$38 CS
9141 ECU6 SID\$09 INF\$04 9 M2	\$48 \$6B \$15 \$49 \$04 \$02 \$39 \$37 \$37 \$38 CS
9141 ECU6 SID\$09 INF\$04 9 M3	\$48 \$6B \$15 \$49 \$04 \$03 \$00 \$00 \$00 \$00 CS
9141 ECU6 SID\$09 INF\$04 9 M4	\$48 \$6B \$15 \$49 \$04 \$04 \$00 \$00 \$00 \$00 CS
9141 ECU6 SID\$09 INF\$04 9 M5	\$48 \$6B \$15 \$49 \$04 \$05 \$32 \$36 \$35 \$38 CS
9141 ECU6 SID\$09 INF\$04 9 M6	\$48 \$6B \$15 \$49 \$04 \$06 \$39 \$37 \$37 \$37 CS
9141 ECU6 SID\$09 INF\$04 9 M7	\$48 \$6B \$15 \$49 \$04 \$07 \$00 \$00 \$00 \$00 CS
9141 ECU6 SID\$09 INF\$04 9 M8	\$48 \$6B \$15 \$49 \$04 \$08 \$00 \$00 \$00 \$00 CS
9141 ECU6 SID\$09 INF\$04 9 M9	\$48 \$6B \$15 \$49 \$04 \$09 \$33 \$36 \$35 \$38 CS
9141 ECU6 SID\$09 INF\$04 9 M10	\$48 \$6B \$15 \$49 \$04 \$0A \$39 \$37 \$37 \$36 CS
9141 ECU6 SID\$09 INF\$04 9 M11	\$48 \$6B \$15 \$49 \$04 \$0B \$00 \$00 \$00 \$00 CS
9141 ECU6 SID\$09 INF\$04 9 M12	\$48 \$6B \$15 \$49 \$04 \$0C \$00 \$00 \$00 \$00 CS
9141 ECU6 SID\$09 INF\$04 9 M13	\$48 \$6B \$15 \$49 \$04 \$0D \$34 \$36 \$35 \$38 CS
9141 ECU6 SID\$09 INF\$04 9 M14	\$48 \$6B \$15 \$49 \$04 \$0E \$39 \$37 \$37 \$35 CS
9141 ECU6 SID\$09 INF\$04 9 M15	\$48 \$6B \$15 \$49 \$04 \$0F \$00 \$00 \$00 \$00 CS
9141 ECU6 SID\$09 INF\$04 9 M16	\$48 \$6B \$15 \$49 \$04 \$10 \$00 \$00 \$00 \$00 CS
9141 ECU6 SID\$09 INF\$04 9 M17	\$48 \$6B \$15 \$49 \$04 \$11 \$35 \$36 \$35 \$38 CS
9141 ECU6 SID\$09 INF\$04 9 M18	\$48 \$6B \$15 \$49 \$04 \$12 \$39 \$37 \$37 \$34 CS
9141 ECU6 SID\$09 INF\$04 9 M19	\$48 \$6B \$15 \$49 \$04 \$13 \$00 \$00 \$00 \$00 CS
9141 ECU6 SID\$09 INF\$04 9 M20	\$48 \$6B \$15 \$49 \$04 \$14 \$00 \$00 \$00 \$00 CS
9141 ECU6 SID\$09 INF\$04 9 M21	\$48 \$6B \$15 \$49 \$04 \$15 \$36 \$36 \$35 \$38 CS
9141 ECU6 SID\$09 INF\$04 9 M22	\$48 \$6B \$15 \$49 \$04 \$16 \$39 \$37 \$37 \$33 CS
9141 ECU6 SID\$09 INF\$04 9 M23	\$48 \$6B \$15 \$49 \$04 \$17 \$00 \$00 \$00 \$00 CS
9141 ECU6 SID\$09 INF\$04 9 M24	\$48 \$6B \$15 \$49 \$04 \$18 \$00 \$00 \$00 \$00 CS
9141 ECU6 SID\$09 INF\$04 9 M25	\$48 \$6B \$15 \$49 \$04 \$19 \$37 \$36 \$35 \$38 CS
9141 ECU6 SID\$09 INF\$04 9 M26	\$48 \$6B \$15 \$49 \$04 \$1A \$39 \$37 \$37 \$32 CS
9141 ECU6 SID\$09 INF\$04 9 M27	\$48 \$6B \$15 \$49 \$04 \$1B \$00 \$00 \$00 \$00 CS
9141 ECU6 SID\$09 INF\$04 9 M28	\$48 \$6B \$15 \$49 \$04 \$1C \$00 \$00 \$00 \$00 CS
9141 ECU6 SID\$09 INF\$04 9 M29	\$48 \$6B \$15 \$49 \$04 \$1D \$38 \$36 \$35 \$38 CS
9141 ECU6 SID\$09 INF\$04 9 M30	\$48 \$6B \$15 \$49 \$04 \$1E \$39 \$37 \$37 \$31 CS
9141 ECU6 SID\$09 INF\$04 9 M31	\$48 \$6B \$15 \$49 \$04 \$1F \$00 \$00 \$00 \$00 CS
9141 ECU6 SID\$09 INF\$04 9 M32	\$48 \$6B \$15 \$49 \$04 \$20 \$00 \$00 \$00 \$00 CS
9141 ECU6 SID\$09 INF\$04 9 M33	\$48 \$6B \$15 \$49 \$04 \$21 \$39 \$36 \$35 \$38 CS
9141 ECU6 SID\$09 INF\$04 9 M34	\$48 \$6B \$15 \$49 \$04 \$22 \$39 \$37 \$37 \$30 CS
9141 ECU6 SID\$09 INF\$04 9 M35	\$48 \$6B \$15 \$49 \$04 \$23 \$00 \$00 \$00 \$00 CS

9141 ECU6 SID\$09 INF\$04 9 M36	\$48 \$6B \$15 \$49 \$04 \$24 \$00 \$00 \$00 \$00 CS
9141 ECU6 SID\$09 INF\$05 9	\$48 \$6B \$15 \$49 \$05 \$09 CS
9141 ECU6 SID\$09 INF\$06 9 M1	\$48 \$6B \$15 \$49 \$06 \$01 \$67 \$91 \$BC \$88 CS
9141 ECU6 SID\$09 INF\$06 9 M2	\$48 \$6B \$15 \$49 \$06 \$02 \$68 \$91 \$BC \$87 CS
9141 ECU6 SID\$09 INF\$06 9 M3	\$48 \$6B \$15 \$49 \$06 \$03 \$69 \$91 \$BC \$86 CS
9141 ECU6 SID\$09 INF\$06 9 M4	\$48 \$6B \$15 \$49 \$06 \$04 \$6A \$91 \$BC \$85 CS
9141 ECU6 SID\$09 INF\$06 9 M5	\$48 \$6B \$15 \$49 \$06 \$05 \$6B \$91 \$BC \$84 CS
9141 ECU6 SID\$09 INF\$06 9 M6	\$48 \$6B \$15 \$49 \$06 \$06 \$6C \$91 \$BC \$83 CS
9141 ECU6 SID\$09 INF\$06 9 M7	\$48 \$6B \$15 \$49 \$06 \$07 \$6D \$91 \$BC \$82 CS
9141 ECU6 SID\$09 INF\$06 9 M8	\$48 \$6B \$15 \$49 \$06 \$08 \$6E \$55 \$AA \$81 CS
9141 ECU6 SID\$09 INF\$06 9 M9	\$48 \$6B \$15 \$49 \$06 \$09 \$6F \$FF \$00 \$80 CS
9141 ECU7 SID\$09 INF\$00	\$48 \$6B \$16 \$49 \$00 \$3C \$00 \$00 \$00 CS
9141 ECU7 SID\$09 INF\$03 9	\$48 \$6B \$16 \$49 \$03 \$24 CS
9141 ECU7 SID\$09 INF\$04 9 M1	\$48 \$6B \$16 \$49 \$04 \$01 \$31 \$37 \$35 \$38 CS
9141 ECU7 SID\$09 INF\$04 9 M2	\$48 \$6B \$16 \$49 \$04 \$02 \$39 \$37 \$37 \$38 CS
9141 ECU7 SID\$09 INF\$04 9 M3	\$48 \$6B \$16 \$49 \$04 \$03 \$00 \$00 \$00 \$00 CS
9141 ECU7 SID\$09 INF\$04 9 M4	\$48 \$6B \$16 \$49 \$04 \$04 \$00 \$00 \$00 \$00 CS
9141 ECU7 SID\$09 INF\$04 9 M5	\$48 \$6B \$16 \$49 \$04 \$05 \$32 \$37 \$35 \$38 CS
9141 ECU7 SID\$09 INF\$04 9 M6	\$48 \$6B \$16 \$49 \$04 \$06 \$39 \$37 \$37 \$37 CS
9141 ECU7 SID\$09 INF\$04 9 M7	\$48 \$6B \$16 \$49 \$04 \$07 \$00 \$00 \$00 \$00 CS
9141 ECU7 SID\$09 INF\$04 9 M8	\$48 \$6B \$16 \$49 \$04 \$08 \$00 \$00 \$00 \$00 CS
9141 ECU7 SID\$09 INF\$04 9 M9	\$48 \$6B \$16 \$49 \$04 \$09 \$33 \$37 \$35 \$38 CS
9141 ECU7 SID\$09 INF\$04 9 M10	\$48 \$6B \$16 \$49 \$04 \$0A \$39 \$37 \$37 \$36 CS
9141 ECU7 SID\$09 INF\$04 9 M11	\$48 \$6B \$16 \$49 \$04 \$0B \$00 \$00 \$00 \$00 CS
9141 ECU7 SID\$09 INF\$04 9 M12	\$48 \$6B \$16 \$49 \$04 \$0C \$00 \$00 \$00 \$00 CS
9141 ECU7 SID\$09 INF\$04 9 M13	\$48 \$6B \$16 \$49 \$04 \$0D \$34 \$37 \$35 \$38 CS
9141 ECU7 SID\$09 INF\$04 9 M14	\$48 \$6B \$16 \$49 \$04 \$0E \$39 \$37 \$37 \$35 CS
9141 ECU7 SID\$09 INF\$04 9 M15	\$48 \$6B \$16 \$49 \$04 \$0F \$00 \$00 \$00 \$00 CS
9141 ECU7 SID\$09 INF\$04 9 M16	\$48 \$6B \$16 \$49 \$04 \$10 \$00 \$00 \$00 \$00 CS
9141 ECU7 SID\$09 INF\$04 9 M17	\$48 \$6B \$16 \$49 \$04 \$11 \$35 \$37 \$35 \$38 CS
9141 ECU7 SID\$09 INF\$04 9 M18	\$48 \$6B \$16 \$49 \$04 \$12 \$39 \$37 \$37 \$34 CS
9141 ECU7 SID\$09 INF\$04 9 M19	\$48 \$6B \$16 \$49 \$04 \$13 \$00 \$00 \$00 \$00 CS
9141 ECU7 SID\$09 INF\$04 9 M20	\$48 \$6B \$16 \$49 \$04 \$14 \$00 \$00 \$00 \$00 CS
9141 ECU7 SID\$09 INF\$04 9 M21	\$48 \$6B \$16 \$49 \$04 \$15 \$36 \$37 \$35 \$38 CS
9141 ECU7 SID\$09 INF\$04 9 M22	\$48 \$6B \$16 \$49 \$04 \$16 \$39 \$37 \$37 \$33 CS
9141 ECU7 SID\$09 INF\$04 9 M23	\$48 \$6B \$16 \$49 \$04 \$17 \$00 \$00 \$00 \$00 CS
9141 ECU7 SID\$09 INF\$04 9 M24	\$48 \$6B \$16 \$49 \$04 \$18 \$00 \$00 \$00 \$00 CS
9141 ECU7 SID\$09 INF\$04 9 M25	\$48 \$6B \$16 \$49 \$04 \$19 \$37 \$37 \$35 \$38 CS
9141 ECU7 SID\$09 INF\$04 9 M26	\$48 \$6B \$16 \$49 \$04 \$1A \$39 \$37 \$37 \$32 CS
9141 ECU7 SID\$09 INF\$04 9 M26	\$48 \$6B \$16 \$49 \$04 \$1B \$00 \$00 \$00 \$00 CS
9141 ECU7 SID\$09 INF\$04 9 M28	\$48 \$6B \$16 \$49 \$04 \$1C \$00 \$00 \$00 \$00 CS
9141 ECU7 SID\$09 INF\$04 9 M29	\$48 \$6B \$16 \$49 \$04 \$1D \$38 \$37 \$35 \$38 CS
9141 ECU7 SID\$09 INF\$04 9 M30	\$48 \$6B \$16 \$49 \$04 \$1E \$39 \$37 \$37 \$31 CS
9141 ECU7 SID\$09 INF\$04 9 M31	\$48 \$6B \$16 \$49 \$04 \$1F \$00 \$00 \$00 \$00 CS
9141 ECU7 SID\$09 INF\$04 9 M32	\$48 \$6B \$16 \$49 \$04 \$20 \$00 \$00 \$00 \$00 CS
9141 ECU7 SID\$09 INF\$04 9 M33	\$48 \$6B \$16 \$49 \$04 \$21 \$39 \$37 \$35 \$38 CS
9141 ECU7 SID\$09 INF\$04 9 M34	\$48 \$6B \$16 \$49 \$04 \$22 \$39 \$37 \$37 \$30 CS
9141 ECU7 SID\$09 INF\$04 9 M35	\$48 \$6B \$16 \$49 \$04 \$23 \$00 \$00 \$00 \$00 CS
9141 ECU7 SID\$09 INF\$04 9 M36	\$48 \$6B \$16 \$49 \$04 \$24 \$00 \$00 \$00 \$00 CS
9141 ECU7 SID\$09 INF\$05 9	\$48 \$6B \$16 \$49 \$05 \$09 CS
9141 ECU7 SID\$09 INF\$06 9 M1	\$48 \$6B \$16 \$49 \$06 \$01 \$77 \$91 \$BC \$88 CS
9141 ECU7 SID\$09 INF\$06 9 M2	\$48 \$6B \$16 \$49 \$06 \$02 \$78 \$91 \$BC \$87 CS
9141 ECU7 SID\$09 INF\$06 9 M3	\$48 \$6B \$16 \$49 \$06 \$03 \$79 \$91 \$BC \$86 CS
9141 ECU7 SID\$09 INF\$06 9 M4	\$48 \$6B \$16 \$49 \$06 \$04 \$7A \$91 \$BC \$85 CS
9141 ECU7 SID\$09 INF\$06 9 M5	\$48 \$6B \$16 \$49 \$06 \$05 \$7B \$91 \$BC \$84 CS
9141 ECU7 SID\$09 INF\$06 9 M6	\$48 \$6B \$16 \$49 \$06 \$06 \$7C \$91 \$BC \$83 CS
9141 ECU7 SID\$09 INF\$06 9 M7	\$48 \$6B \$16 \$49 \$06 \$07 \$7D \$91 \$BC \$82 CS
9141 ECU7 SID\$09 INF\$06 9 M8	\$48 \$6B \$16 \$49 \$06 \$08 \$7E \$55 \$AA \$81 CS

9141 ECU7 SID\$09 INF\$06 9 M9	\$48 \$6B \$16 \$49 \$06 \$09 \$7F \$FF \$00 \$80 CS
9141 ECU8 SID\$09 INF\$00	\$48 \$6B \$17 \$49 \$00 \$3C \$00 \$00 \$00 CS
9141 ECU8 SID\$09 INF\$03 9	\$48 \$6B \$17 \$49 \$03 \$24 CS
9141 ECU8 SID\$09 INF\$04 9 M1	\$48 \$6B \$17 \$49 \$04 \$01 \$31 \$38 \$35 \$38 CS
9141 ECU8 SID\$09 INF\$04 9 M2	\$48 \$6B \$17 \$49 \$04 \$02 \$39 \$37 \$37 \$38 CS
9141 ECU8 SID\$09 INF\$04 9 M3	\$48 \$6B \$17 \$49 \$04 \$03 \$00 \$00 \$00 \$00 CS
9141 ECU8 SID\$09 INF\$04 9 M4	\$48 \$6B \$17 \$49 \$04 \$04 \$00 \$00 \$00 \$00 CS
9141 ECU8 SID\$09 INF\$04 9 M5	\$48 \$6B \$17 \$49 \$04 \$05 \$32 \$38 \$35 \$38 CS
9141 ECU8 SID\$09 INF\$04 9 M6	\$48 \$6B \$17 \$49 \$04 \$06 \$39 \$37 \$37 \$37 CS
9141 ECU8 SID\$09 INF\$04 9 M7	\$48 \$6B \$17 \$49 \$04 \$07 \$00 \$00 \$00 \$00 CS
9141 ECU8 SID\$09 INF\$04 9 M8	\$48 \$6B \$17 \$49 \$04 \$08 \$00 \$00 \$00 \$00 CS
9141 ECU8 SID\$09 INF\$04 9 M9	\$48 \$6B \$17 \$49 \$04 \$09 \$33 \$38 \$35 \$38 CS
9141 ECU8 SID\$09 INF\$04 9 M10	\$48 \$6B \$17 \$49 \$04 \$0A \$39 \$37 \$37 \$36 CS
9141 ECU8 SID\$09 INF\$04 9 M11	\$48 \$6B \$17 \$49 \$04 \$0B \$00 \$00 \$00 \$00 CS
9141 ECU8 SID\$09 INF\$04 9 M12	\$48 \$6B \$17 \$49 \$04 \$0C \$00 \$00 \$00 \$00 CS
9141 ECU8 SID\$09 INF\$04 9 M13	\$48 \$6B \$17 \$49 \$04 \$0D \$34 \$38 \$35 \$38 CS
9141 ECU8 SID\$09 INF\$04 9 M14	\$48 \$6B \$17 \$49 \$04 \$0E \$39 \$37 \$37 \$35 CS
9141 ECU8 SID\$09 INF\$04 9 M15	\$48 \$6B \$17 \$49 \$04 \$0F \$00 \$00 \$00 \$00 CS
9141 ECU8 SID\$09 INF\$04 9 M16	\$48 \$6B \$17 \$49 \$04 \$10 \$00 \$00 \$00 \$00 CS
9141 ECU8 SID\$09 INF\$04 9 M17	\$48 \$6B \$17 \$49 \$04 \$11 \$35 \$38 \$35 \$38 CS
9141 ECU8 SID\$09 INF\$04 9 M18	\$48 \$6B \$17 \$49 \$04 \$12 \$39 \$37 \$37 \$34 CS
9141 ECU8 SID\$09 INF\$04 9 M19	\$48 \$6B \$17 \$49 \$04 \$13 \$00 \$00 \$00 \$00 CS
9141 ECU8 SID\$09 INF\$04 9 M20	\$48 \$6B \$17 \$49 \$04 \$14 \$00 \$00 \$00 \$00 CS
9141 ECU8 SID\$09 INF\$04 9 M21	\$48 \$6B \$17 \$49 \$04 \$15 \$36 \$38 \$35 \$38 CS
9141 ECU8 SID\$09 INF\$04 9 M22	\$48 \$6B \$17 \$49 \$04 \$16 \$39 \$37 \$37 \$33 CS
9141 ECU8 SID\$09 INF\$04 9 M23	\$48 \$6B \$17 \$49 \$04 \$17 \$00 \$00 \$00 \$00 CS
9141 ECU8 SID\$09 INF\$04 9 M24	\$48 \$6B \$17 \$49 \$04 \$17 \$00 \$00 \$00 \$00 CS
9141 ECU8 SID\$09 INF\$04 9 M25	\$48 \$6B \$17 \$49 \$04 \$19 \$37 \$38 \$35 \$38 CS
9141 ECU8 SID\$09 INF\$04 9 M26	\$48 \$6B \$17 \$49 \$04 \$1A \$39 \$37 \$37 \$32 CS
9141 ECU8 SID\$09 INF\$04 9 M27	\$48 \$6B \$17 \$49 \$04 \$1B \$00 \$00 \$00 \$00 CS
9141 ECU8 SID\$09 INF\$04 9 M28	\$48 \$6B \$17 \$49 \$04 \$1C \$00 \$00 \$00 \$00 CS
9141 ECU8 SID\$09 INF\$04 9 M29	\$48 \$6B \$17 \$49 \$04 \$1D \$38 \$38 \$35 \$38 CS
9141 ECU8 SID\$09 INF\$04 9 M30	\$48 \$6B \$17 \$49 \$04 \$1E \$39 \$37 \$37 \$31 CS
9141 ECU8 SID\$09 INF\$04 9 M31	\$48 \$6B \$17 \$49 \$04 \$1F \$00 \$00 \$00 \$00 CS
9141 ECU8 SID\$09 INF\$04 9 M32	\$48 \$6B \$17 \$49 \$04 \$20 \$00 \$00 \$00 \$00 CS
9141 ECU8 SID\$09 INF\$04 9 M33	\$48 \$6B \$17 \$49 \$04 \$21 \$39 \$38 \$35 \$38 CS
9141 ECU8 SID\$09 INF\$04 9 M34	\$48 \$6B \$17 \$49 \$04 \$22 \$39 \$37 \$37 \$30 CS
9141 ECU8 SID\$09 INF\$04 9 M35	\$48 \$6B \$17 \$49 \$04 \$23 \$00 \$00 \$00 \$00 CS
9141 ECU8 SID\$09 INF\$04 9 M36	\$48 \$6B \$17 \$49 \$04 \$24 \$00 \$00 \$00 \$00 CS
9141 ECU8 SID\$09 INF\$05 9	\$48 \$6B \$17 \$49 \$05 \$09 CS
9141 ECU8 SID\$09 INF\$06 9 M1	\$48 \$6B \$17 \$49 \$06 \$01 \$87 \$91 \$BC \$88 CS
9141 ECU8 SID\$09 INF\$06 9 M2	\$48 \$6B \$17 \$49 \$06 \$02 \$88 \$91 \$BC \$87 CS
9141 ECU8 SID\$09 INF\$06 9 M3	\$48 \$6B \$17 \$49 \$06 \$03 \$89 \$91 \$BC \$86 CS
9141 ECU8 SID\$09 INF\$06 9 M4	\$48 \$6B \$17 \$49 \$06 \$04 \$8A \$91 \$BC \$85 CS
9141 ECU8 SID\$09 INF\$06 9 M5	\$48 \$6B \$17 \$49 \$06 \$05 \$8B \$91 \$BC \$84 CS
9141 ECU8 SID\$09 INF\$06 9 M6	\$48 \$6B \$17 \$49 \$06 \$06 \$8C \$91 \$BC \$83 CS
9141 ECU8 SID\$09 INF\$06 9 M7	\$48 \$6B \$17 \$49 \$06 \$07 \$8D \$91 \$BC \$82 CS
9141 ECU8 SID\$09 INF\$06 9 M8	\$48 \$6B \$17 \$49 \$06 \$08 \$8E \$55 \$AA \$81 CS
9141 ECU8 SID\$09 INF\$06 9 M9	\$48 \$6B \$17 \$49 \$06 \$09 \$8F \$FF \$00 \$80 CS

Where CS indicates a correctly computed Checksum as defined by ISO 9141-2.

**Figure 16 - Pre-defined messages for ISO 9141-2**

## 5.4.5.4 ISO 9141-2 Message Timing

For convenience, the test cases in this document will use the text in the 'Parameter Reference' column when referring to the specific value, detailed in the 'Value' column of the table below. Unless otherwise specified, the following timing values shall be used:

Parameter Reference	Value
W1_MIN	60 ms
W1	100 ms
W1_MAX	300 ms
W2_MIN	5 ms
W2	15 ms
W2_MAX	20 ms
W3_MIN	0 ms
W3	15 ms
W3_MAX	20 ms
W4_MIN	25 ms
W4	40 ms
W4_MAX	50 ms
W5 (also known as W0)	At least 300 ms
W5 (also known as W0)	5 ms
P1_MIN	0 ms
P1	5 ms
P1_MAX	20 ms
P2_MIN	0 ms
P2	25 ms
P2_MAX	50 ms
P3_MIN	0 ms
P3	25 ms
P3_MAX	5000 ms
P4_MIN	0 ms
P4	25 ms
P4_MAX	20 ms

**Figure 17 - Pre-defined message timing for ISO 9141-2**

## 5.4.5.5 ISO 9141-2 Connected State

The device shall initially be in the 'Power On' state with the K and L Lines connected and the appropriate pull-up applied (as depicted in 5.4.5.1).

## 5.4.5.6 ISO 9141-2 (2 ECUs) Initialized State

The OBD network shall have determined to be ISO 9141-2 at a baud rate of 10400 with two OBD II ECUs having been identified. The DUT may produce a periodic message transmission to keep the network alive. A successful completion of any section from 8.4.2 to 8.4.3 shall define this state.

## 5.4.5.7 ISO 9141-2 (8 ECUs) Initialized State

The OBD network shall have determined to be ISO 9141-2 at a baud rate of 10400 with eight OBD II ECUs having been identified. The DUT may produce a periodic message transmission to keep the network alive. A successful completion of 8.4.3 shall define this state.

## 5.4.5.8 ISO 9141-2 (2 ECUs) SID9 Ready State

The SID \$09 INFOTYPES shall be known for each of the two OBD II ECUs on the network. The DUT may produce a periodic message transmission to keep the network alive. A successful completion of 9.4.17 shall define this state.

#### 5.4.5.9 ISO 9141-2 (2 ECUs) SID9 Diesel Ready State

The SID \$09 INFOTYPES shall be known for each of the two OBD II ECUs on the network. The DUT may produce a periodic message transmission to keep the network alive. A successful completion of 0 shall define this state.

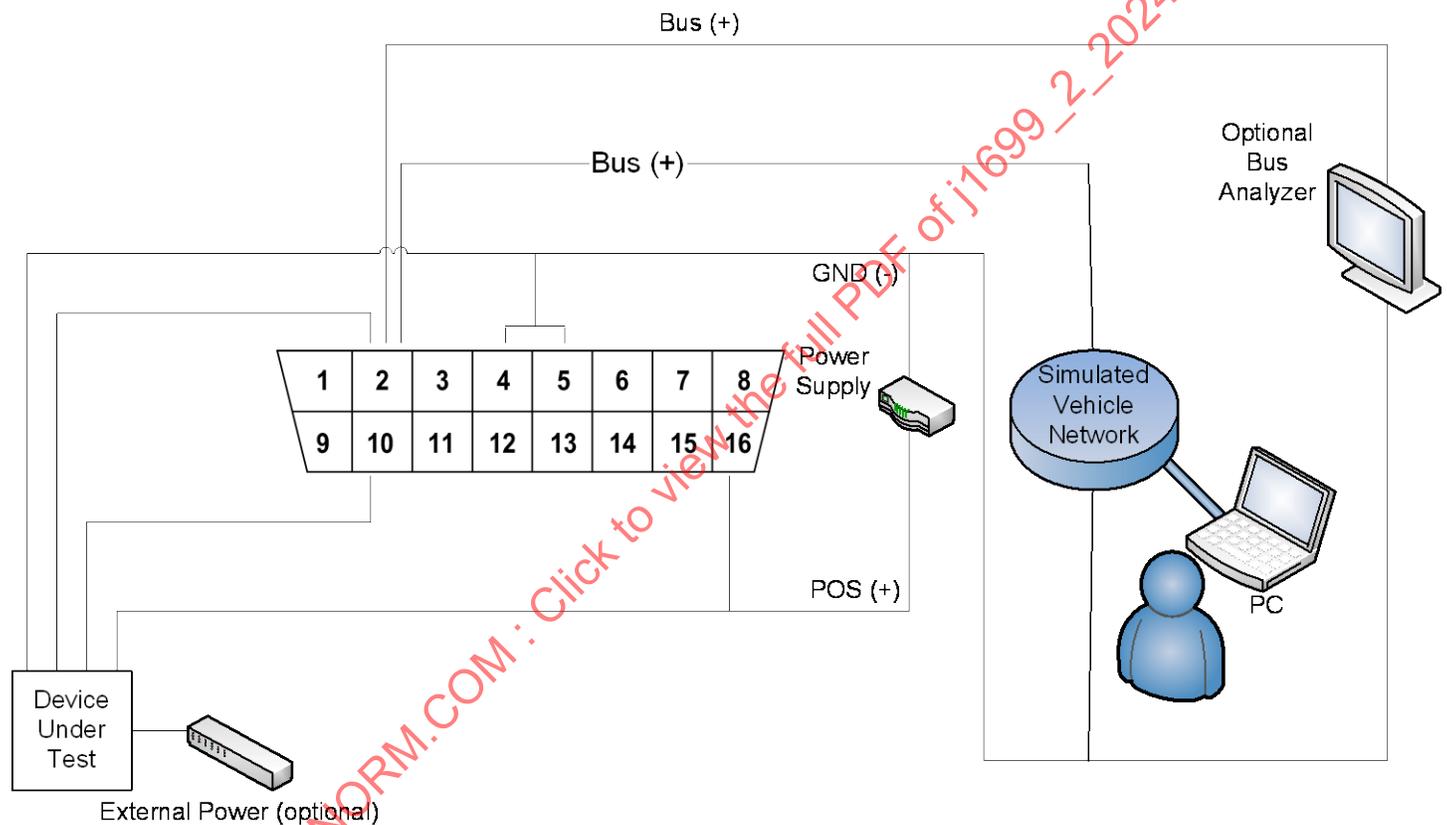
#### 5.4.5.10 ISO 9141-2 (8 ECUs) SID9 Ready State

The SID \$09 INFOTYPES shall be known for each of the eight OBD II ECUs on the network. The DUT may produce a periodic message transmission to keep the network alive. A successful completion of 9.4.19 shall define this state.

#### 5.4.6 SAE J1850 VPW

##### 5.4.6.1 SAE J1850 VPW Electrical Connections

Figure 18 outlines the typical set-up for SAE J1850 VPW testing.



**Figure 18 - Test set-up for SAE J1850 VPW testing**

## 5.4.6.2 SAE J1850 VPW ECU Addresses

The following address bytes have been pre-defined for SAE J1850 VPW. For convenience, the test cases in this document will use the text in the 'ECU Reference' column when referring to the specific set of address bytes, detailed in the 'Address Bytes' column of the table below.

ECU Reference	Address Bytes
ECU #1	\$10
ECU #2	\$11
ECU #3	\$12
ECU #4	\$13
ECU #5	\$14
ECU #6	\$15
ECU #7	\$16
ECU #8	\$17

**Figure 19 - ECU reference and associated addresses for SAE J1850 VPW**

## 5.4.6.3 SAE J1850 VPW Messages

The following messages have been pre-defined for SAE J1850 VPW. For convenience, the test cases in this document will use the text in the 'Message Reference' column when referring to the specific set of message bytes, detailed in the 'Message Bytes' column of the table below.

Message Reference	Message Bytes
1850VPW_SID\$01_PID\$00	\$68 \$6A \$F1 \$01 \$00
1850VPW_ECU1_SID\$01_PID\$00	\$48 \$6B \$10 \$41 \$00 \$80 \$10 \$00 \$00
1850VPW_ECU2_SID\$01_PID\$00	\$48 \$6B \$11 \$41 \$00 \$80 \$10 \$00 \$00
1850VPW_ECU3_SID\$01_PID\$00	\$48 \$6B \$12 \$41 \$00 \$00 \$00 \$00 \$00
1850VPW_ECU4_SID\$01_PID\$00	\$48 \$6B \$13 \$41 \$00 \$00 \$00 \$00 \$00
1850VPW_ECU5_SID\$01_PID\$00	\$48 \$6B \$14 \$41 \$00 \$00 \$00 \$00 \$00
1850VPW_ECU6_SID\$01_PID\$00	\$48 \$6B \$15 \$41 \$00 \$00 \$00 \$00 \$00
1850VPW_ECU7_SID\$01_PID\$00	\$48 \$6B \$16 \$41 \$00 \$00 \$00 \$00 \$00
1850VPW_ECU8_SID\$01_PID\$00	\$48 \$6B \$17 \$41 \$00 \$00 \$00 \$00 \$00
1850VPW_SID\$01_PID\$01	\$68 \$6A \$F1 \$01 \$01
1850VPW_SID\$01_PID\$0C	\$68 \$6A \$F1 \$01 \$0C
1850VPW_SID\$01_PID\$20	\$68 \$6A \$F1 \$01 \$20
1850VPW_SID\$01_PID\$40	\$68 \$6A \$F1 \$01 \$40
1850VPW_SID\$01_PID\$41	\$68 \$6A \$F1 \$01 \$41
1850VPW_ECU1_SID\$01_PID\$00_20	\$48 \$6B \$10 \$41 \$00 \$80 \$10 \$00 \$01
1850VPW_ECU1_SID\$01_PID\$01_SPARK	\$48 \$6B \$10 \$41 \$01 \$00 \$07 \$EF \$EF
1850VPW_ECU1_SID\$01_PID\$01_COMP	\$48 \$6B \$10 \$41 \$01 \$00 \$0F \$EB \$EB
1850VPW_ECU1_SID\$01_PID\$0C_KOEO	\$48 \$6B \$10 \$41 \$0C \$00 \$00
1850VPW_ECU1_SID\$01_PID\$20	\$48 \$6B \$10 \$41 \$20 \$00 \$00 \$00 \$01
1850VPW_ECU1_SID\$01_PID\$40	\$48 \$6B \$10 \$41 \$40 \$10 \$00 \$00 \$00
1850VPW_ECU1_SID\$01_PID\$41_SPARK	\$48 \$6B \$10 \$41 \$41 \$00 \$07 \$EF \$EF
1850VPW_ECU2_SID\$01_PID\$01_SPARK	\$48 \$6B \$11 \$41 \$01 \$81 \$44 \$00 \$00
1850VPW_ECU2_SID\$01_PID\$01_COMP	\$48 \$6B \$11 \$41 \$01 \$81 \$4C \$00 \$00
1850VPW_ECU2_SID\$01_PID\$0C_KOEO	\$48 \$6B \$11 \$41 \$0C \$00 \$00
1850VPW_SID\$02_PID\$02	\$68 \$6A \$F1 \$02 \$02 \$00
1850VPW_ECU1_SID\$02_PID\$02_NoDTC	\$48 \$6B \$10 \$42 \$02 \$00 \$00 \$00
1850VPW_ECU2_SID\$02_PID\$02_NoDTC	\$48 \$6B \$11 \$42 \$02 \$00 \$00 \$00
1850VPW_ECU1_SID\$02_PID\$02_DTC	\$48 \$6B \$10 \$42 \$02 \$00 \$01 \$07
1850VPW_SID\$03	\$68 \$6A \$F1 \$03
1850VPW_ECU1_SID\$03_NoDTC	\$48 \$6B \$10 \$43 \$00
1850VPW_ECU1_SID\$03_DTC_M1	\$48 \$6B \$10 \$43 \$01 \$07 \$01 \$10 \$01 \$15
1850VPW_ECU1_SID\$03_DTC_M2	\$48 \$6B \$10 \$43 \$01 \$24 \$00 \$00 \$00 \$00
1850VPW_ECU2_SID\$03_NoDTC	\$48 \$6B \$11 \$43 \$00

1850VPW SID\$04	\$68 \$6A \$F1 \$04
1850VPW ECU1 SID\$04 DONE	\$48 \$6B \$10 \$44
1850VPW ECU1 SID\$04 NRC22	\$48 \$6B \$10 \$7F \$44 \$22
1850VPW ECU1 SID\$04 NRC78	\$48 \$6B \$10 \$7F \$44 \$78
1850VPW ECU2 SID\$04 DONE	\$48 \$6B \$11 \$44
1850VPW ECU2 SID\$04 NRC22	\$48 \$6B \$11 \$7F \$44 \$22
1850VPW SID\$05 MID\$01	\$68 \$6A \$F1 \$05 \$01 \$02
1850VPW ECU1 SID\$05 MID\$01	\$48 \$6B \$10 \$45 \$01 \$02 \$5A
1850VPW SID\$06 MID\$00	\$68 \$6A \$F1 \$06 \$00
1850VPW ECU1 SID\$06 MID\$00	\$48 \$6B \$10 \$46 \$00 \$CC \$00 \$00 \$00
1850VPW SID\$07	\$68 \$6A \$F1 \$07
1850VPW ECU1 SID\$07 NoDTC	\$48 \$6B \$10 \$47 \$00
1850VPW ECU1 SID\$07 DTC M1	\$48 \$6B \$10 \$47 \$01 \$07 \$01 \$10 \$01 \$15
1850VPW ECU1 SID\$07 DTC M2	\$48 \$6B \$10 \$47 \$01 \$24 \$00 \$00 \$00 \$00
1850VPW ECU2 SID\$07 NoDTC	\$48 \$6B \$11 \$47 \$00
1850VPW SID\$08 TID\$00	\$68 \$6A \$F1 \$08
1850VPW ECU1 SID\$08 TID\$00	\$48 \$6B \$10 \$48 \$00 \$80 \$00 \$00 \$00
1850VPW SID\$09 INF\$00	\$68 \$6A \$F1 \$09 \$00
1850VPW SID\$09 INF\$01	\$68 \$6A \$F1 \$09 \$01
1850VPW SID\$09 INF\$02	\$68 \$6A \$F1 \$09 \$02
1850VPW SID\$09 INF\$03	\$68 \$6A \$F1 \$09 \$03
1850VPW SID\$09 INF\$04	\$68 \$6A \$F1 \$09 \$04
1850VPW SID\$09 INF\$05	\$68 \$6A \$F1 \$09 \$05
1850VPW SID\$09 INF\$06	\$68 \$6A \$F1 \$09 \$06
1850VPW SID\$09 INF\$07	\$68 \$6A \$F1 \$09 \$07
1850VPW SID\$09 INF\$08	\$68 \$6A \$F1 \$09 \$08
1850VPW SID\$09 INF\$0B	\$68 \$6A \$F1 \$09 \$0B
1850VPW ECU1 SID\$09 INF\$00	\$48 \$6B \$10 \$49 \$00 \$FF \$00 \$00 \$00
1850VPW ECU1 SID\$09 INF\$00 DIESEL	\$48 \$6B \$10 \$49 \$00 \$FE \$20 \$00 \$00
1850VPW ECU1 SID\$09 INF\$01	\$48 \$6B \$10 \$49 \$01 \$05
1850VPW ECU1 SID\$09 INF\$02 M1	\$48 \$6B \$10 \$49 \$02 \$01 \$00 \$00 \$00 \$31
1850VPW ECU1 SID\$09 INF\$02 M2	\$48 \$6B \$10 \$49 \$02 \$02 \$47 \$59 \$44 \$45
1850VPW ECU1 SID\$09 INF\$02 M3	\$48 \$6B \$10 \$49 \$02 \$03 \$36 \$33 \$41 \$37
1850VPW ECU1 SID\$09 INF\$02 M4	\$48 \$6B \$10 \$49 \$02 \$04 \$41 \$30 \$30 \$30
1850VPW ECU1 SID\$09 INF\$02 M5	\$48 \$6B \$10 \$49 \$02 \$05 \$30 \$30 \$30 \$30
1850VPW ECU1 SID\$09 INF\$03	\$48 \$6B \$10 \$49 \$03 \$04
1850VPW ECU1 SID\$09 INF\$03 9	\$48 \$6B \$10 \$49 \$03 \$24
1850VPW ECU1 SID\$09 INF\$04 M1	\$48 \$6B \$10 \$49 \$04 \$01 \$31 \$32 \$35 \$38
1850VPW ECU1 SID\$09 INF\$04 M2	\$48 \$6B \$10 \$49 \$04 \$02 \$39 \$37 \$37 \$32
1850VPW ECU1 SID\$09 INF\$04 M3	\$48 \$6B \$10 \$49 \$04 \$03 \$00 \$00 \$00 \$00
1850VPW ECU1 SID\$09 INF\$04 M4	\$48 \$6B \$10 \$49 \$04 \$04 \$00 \$00 \$00 \$00
1850VPW ECU1 SID\$09 INF\$04 9 M1	\$48 \$6B \$10 \$49 \$04 \$01 \$31 \$31 \$35 \$38
1850VPW ECU1 SID\$09 INF\$04 9 M2	\$48 \$6B \$10 \$49 \$04 \$02 \$39 \$37 \$37 \$38
1850VPW ECU1 SID\$09 INF\$04 9 M3	\$48 \$6B \$10 \$49 \$04 \$03 \$00 \$00 \$00 \$00
1850VPW ECU1 SID\$09 INF\$04 9 M4	\$48 \$6B \$10 \$49 \$04 \$04 \$00 \$00 \$00 \$00
1850VPW ECU1 SID\$09 INF\$04 9 M5	\$48 \$6B \$10 \$49 \$04 \$05 \$32 \$31 \$35 \$38
1850VPW ECU1 SID\$09 INF\$04 9 M6	\$48 \$6B \$10 \$49 \$04 \$06 \$39 \$37 \$37 \$37
1850VPW ECU1 SID\$09 INF\$04 9 M7	\$48 \$6B \$10 \$49 \$04 \$07 \$00 \$00 \$00 \$00
1850VPW ECU1 SID\$09 INF\$04 9 M8	\$48 \$6B \$10 \$49 \$04 \$08 \$00 \$00 \$00 \$00
1850VPW ECU1 SID\$09 INF\$04 9 M9	\$48 \$6B \$10 \$49 \$04 \$09 \$33 \$31 \$35 \$38
1850VPW ECU1 SID\$09 INF\$04 9 M10	\$48 \$6B \$10 \$49 \$04 \$0A \$39 \$37 \$37 \$36
1850VPW ECU1 SID\$09 INF\$04 9 M11	\$48 \$6B \$10 \$49 \$04 \$0B \$00 \$00 \$00 \$00
1850VPW ECU1 SID\$09 INF\$04 9 M12	\$48 \$6B \$10 \$49 \$04 \$0C \$00 \$00 \$00 \$00
1850VPW ECU1 SID\$09 INF\$04 9 M13	\$48 \$6B \$10 \$49 \$04 \$0D \$34 \$31 \$35 \$38
1850VPW ECU1 SID\$09 INF\$04 9 M14	\$48 \$6B \$10 \$49 \$04 \$0E \$39 \$37 \$37 \$35
1850VPW ECU1 SID\$09 INF\$04 9 M15	\$48 \$6B \$10 \$49 \$04 \$0F \$00 \$00 \$00 \$00
1850VPW ECU1 SID\$09 INF\$04 9 M16	\$48 \$6B \$10 \$49 \$04 \$10 \$00 \$00 \$00 \$00
1850VPW ECU1 SID\$09 INF\$04 9 M17	\$48 \$6B \$10 \$49 \$04 \$11 \$35 \$31 \$35 \$38

1850VPW ECU1 SID\$09 INF\$04 9 M18	\$48 \$6B \$10 \$49 \$04 \$12 \$39 \$37 \$37 \$34
1850VPW ECU1 SID\$09 INF\$04 9 M19	\$48 \$6B \$10 \$49 \$04 \$13 \$00 \$00 \$00 \$00
1850VPW ECU1 SID\$09 INF\$04 9 M20	\$48 \$6B \$10 \$49 \$04 \$14 \$00 \$00 \$00 \$00
1850VPW ECU1 SID\$09 INF\$04 9 M21	\$48 \$6B \$10 \$49 \$04 \$15 \$36 \$31 \$35 \$38
1850VPW ECU1 SID\$09 INF\$04 9 M22	\$48 \$6B \$10 \$49 \$04 \$16 \$39 \$37 \$37 \$33
1850VPW ECU1 SID\$09 INF\$04 9 M23	\$48 \$6B \$10 \$49 \$04 \$17 \$00 \$00 \$00 \$00
1850VPW ECU1 SID\$09 INF\$04 9 M24	\$48 \$6B \$10 \$49 \$04 \$18 \$00 \$00 \$00 \$00
1850VPW ECU1 SID\$09 INF\$04 9 M25	\$48 \$6B \$10 \$49 \$04 \$19 \$37 \$31 \$35 \$38
1850VPW ECU1 SID\$09 INF\$04 9 M26	\$48 \$6B \$10 \$49 \$04 \$1A \$39 \$37 \$37 \$32
1850VPW ECU1 SID\$09 INF\$04 9 M27	\$48 \$6B \$10 \$49 \$04 \$1B \$00 \$00 \$00 \$00
1850VPW ECU1 SID\$09 INF\$04 9 M28	\$48 \$6B \$10 \$49 \$04 \$1C \$00 \$00 \$00 \$00
1850VPW ECU1 SID\$09 INF\$04 9 M29	\$48 \$6B \$10 \$49 \$04 \$1D \$38 \$31 \$35 \$38
1850VPW ECU1 SID\$09 INF\$04 9 M30	\$48 \$6B \$10 \$49 \$04 \$1E \$39 \$37 \$37 \$31
1850VPW ECU1 SID\$09 INF\$04 9 M31	\$48 \$6B \$10 \$49 \$04 \$1F \$00 \$00 \$00 \$00
1850VPW ECU1 SID\$09 INF\$04 9 M32	\$48 \$6B \$10 \$49 \$04 \$20 \$00 \$00 \$00 \$00
1850VPW ECU1 SID\$09 INF\$04 9 M33	\$48 \$6B \$10 \$49 \$04 \$21 \$39 \$31 \$35 \$38
1850VPW ECU1 SID\$09 INF\$04 9 M34	\$48 \$6B \$10 \$49 \$04 \$22 \$39 \$37 \$37 \$30
1850VPW ECU1 SID\$09 INF\$04 9 M35	\$48 \$6B \$10 \$49 \$04 \$23 \$00 \$00 \$00 \$00
1850VPW ECU1 SID\$09 INF\$04 9 M36	\$48 \$6B \$10 \$49 \$04 \$24 \$00 \$00 \$00 \$00
1850VPW ECU1 SID\$09 INF\$05	\$48 \$6B \$10 \$49 \$05 \$01
1850VPW ECU1 SID\$09 INF\$05 9	\$48 \$6B \$10 \$49 \$05 \$09
1850VPW ECU1 SID\$09 INF\$06	\$48 \$6B \$10 \$49 \$06 \$01 \$17 \$91 \$BC \$88
1850VPW ECU1 SID\$09 INF\$06 9 M1	\$48 \$6B \$10 \$49 \$06 \$01 \$17 \$91 \$BC \$88
1850VPW ECU1 SID\$09 INF\$06 9 M2	\$48 \$6B \$10 \$49 \$06 \$02 \$18 \$91 \$BC \$87
1850VPW ECU1 SID\$09 INF\$06 9 M3	\$48 \$6B \$10 \$49 \$06 \$03 \$19 \$91 \$BC \$86
1850VPW ECU1 SID\$09 INF\$06 9 M4	\$48 \$6B \$10 \$49 \$06 \$04 \$1A \$91 \$BC \$85
1850VPW ECU1 SID\$09 INF\$06 9 M5	\$48 \$6B \$10 \$49 \$06 \$05 \$1B \$91 \$BC \$84
1850VPW ECU1 SID\$09 INF\$06 9 M6	\$48 \$6B \$10 \$49 \$06 \$06 \$1C \$91 \$BC \$83
1850VPW ECU1 SID\$09 INF\$06 9 M7	\$48 \$6B \$10 \$49 \$06 \$07 \$1D \$91 \$BC \$82
1850VPW ECU1 SID\$09 INF\$06 9 M8	\$48 \$6B \$10 \$49 \$06 \$08 \$1E \$55 \$AA \$81
1850VPW ECU1 SID\$09 INF\$06 9 M9	\$48 \$6B \$10 \$49 \$06 \$09 \$1F \$FF \$00 \$80
1850VPW ECU1 SID\$09 INF\$07 832	\$48 \$6B \$10 \$49 \$07 \$08
1850VPW ECU1 SID\$09 INF\$07 840	\$48 \$6B \$10 \$49 \$07 \$0A
1850VPW ECU1 SID\$09 INF\$07 B32	\$48 \$6B \$10 \$49 \$07 \$08
1850VPW ECU1 SID\$09 INF\$07 B36	\$48 \$6B \$10 \$49 \$07 \$09
1850VPW ECU1 SID\$09 INF\$08 32 M1	\$48 \$6B \$10 \$49 \$08 \$01 \$00 \$64 \$00 \$69
1850VPW ECU1 SID\$09 INF\$08 32 M2	\$48 \$6B \$10 \$49 \$08 \$02 \$00 \$0A \$00 \$0F
1850VPW ECU1 SID\$09 INF\$08 32 M3	\$48 \$6B \$10 \$49 \$08 \$03 \$00 \$00 \$00 \$00
1850VPW ECU1 SID\$09 INF\$08 32 M4	\$48 \$6B \$10 \$49 \$08 \$04 \$00 \$1E \$00 \$23
1850VPW ECU1 SID\$09 INF\$08 32 M5	\$48 \$6B \$10 \$49 \$08 \$05 \$00 \$00 \$00 \$00
1850VPW ECU1 SID\$09 INF\$08 32 M6	\$48 \$6B \$10 \$49 \$08 \$06 \$00 \$32 \$00 \$37
1850VPW ECU1 SID\$09 INF\$08 32 M7	\$48 \$6B \$10 \$49 \$08 \$07 \$00 \$3C \$00 \$41
1850VPW ECU1 SID\$09 INF\$08 32 M8	\$48 \$6B \$10 \$49 \$08 \$08 \$00 \$46 \$00 \$4B
1850VPW ECU1 SID\$09 INF\$08 40 M1	\$48 \$6B \$10 \$49 \$08 \$01 \$00 \$64 \$00 \$69
1850VPW ECU1 SID\$09 INF\$08 40 M2	\$48 \$6B \$10 \$49 \$08 \$02 \$00 \$0A \$00 \$0F
1850VPW ECU1 SID\$09 INF\$08 40 M3	\$48 \$6B \$10 \$49 \$08 \$03 \$00 \$00 \$00 \$00
1850VPW ECU1 SID\$09 INF\$08 40 M4	\$48 \$6B \$10 \$49 \$08 \$04 \$00 \$1E \$00 \$23
1850VPW ECU1 SID\$09 INF\$08 40 M5	\$48 \$6B \$10 \$49 \$08 \$05 \$00 \$00 \$00 \$00
1850VPW ECU1 SID\$09 INF\$08 40 M6	\$48 \$6B \$10 \$49 \$08 \$06 \$00 \$32 \$00 \$37
1850VPW ECU1 SID\$09 INF\$08 40 M7	\$48 \$6B \$10 \$49 \$08 \$07 \$00 \$3C \$00 \$41
1850VPW ECU1 SID\$09 INF\$08 40 M8	\$48 \$6B \$10 \$49 \$08 \$08 \$00 \$46 \$00 \$4B
1850VPW ECU1 SID\$09 INF\$08 40 M9	\$48 \$6B \$10 \$49 \$08 \$09 \$00 \$5A \$00 \$5F
1850VPW ECU1 SID\$09 INF\$08 40 M10	\$48 \$6B \$10 \$49 \$08 \$0A \$00 \$00 \$00 \$00
1850VPW ECU1 SID\$09 INF\$0B 32 M1	\$48 \$6B \$10 \$49 \$0B \$01 \$00 \$64 \$00 \$69
1850VPW ECU1 SID\$09 INF\$0B 32 M2	\$48 \$6B \$10 \$49 \$0B \$02 \$00 \$0A \$00 \$0F
1850VPW ECU1 SID\$09 INF\$0B 32 M3	\$48 \$6B \$10 \$49 \$0B \$03 \$00 \$00 \$00 \$00
1850VPW ECU1 SID\$09 INF\$0B 32 M4	\$48 \$6B \$10 \$49 \$0B \$04 \$00 \$00 \$00 \$00
1850VPW ECU1 SID\$09 INF\$0B 32 M5	\$48 \$6B \$10 \$49 \$0B \$05 \$00 \$00 \$00 \$00

1850VPW ECU1 SID\$09 INF\$0B 32 M6	\$48 \$6B \$10 \$49 \$0B \$06 \$00 \$32 \$00 \$37
1850VPW ECU1 SID\$09 INF\$0B 32 M7	\$48 \$6B \$10 \$49 \$0B \$07 \$00 \$3C \$00 \$41
1850VPW ECU1 SID\$09 INF\$0B 32 M8	\$48 \$6B \$10 \$49 \$0B \$08 \$00 \$46 \$00 \$4B
1850VPW ECU1 SID\$09 INF\$0B 36 M1	\$48 \$6B \$10 \$49 \$0B \$01 \$00 \$64 \$00 \$69
1850VPW ECU1 SID\$09 INF\$0B 36 M2	\$48 \$6B \$10 \$49 \$0B \$02 \$00 \$0A \$00 \$0F
1850VPW ECU1 SID\$09 INF\$0B 36 M3	\$48 \$6B \$10 \$49 \$0B \$03 \$00 \$00 \$00 \$00
1850VPW ECU1 SID\$09 INF\$0B 36 M4	\$48 \$6B \$10 \$49 \$0B \$04 \$00 \$00 \$00 \$00
1850VPW ECU1 SID\$09 INF\$0B 36 M5	\$48 \$6B \$10 \$49 \$0B \$05 \$00 \$00 \$00 \$00
1850VPW ECU1 SID\$09 INF\$0B 36 M6	\$48 \$6B \$10 \$49 \$0B \$06 \$00 \$32 \$00 \$37
1850VPW ECU1 SID\$09 INF\$0B 36 M7	\$48 \$6B \$10 \$49 \$0B \$07 \$00 \$3C \$00 \$41
1850VPW ECU1 SID\$09 INF\$0B 36 M8	\$48 \$6B \$10 \$49 \$0B \$08 \$00 \$46 \$00 \$4B
1850VPW ECU1 SID\$09 INF\$0B 36 M9	\$48 \$6B \$10 \$49 \$0B \$09 \$00 \$50 \$00 \$55
1850VPW ECU1 SID\$09 NRC78	\$48 \$6B \$10 \$7F \$49 \$78
1850VPW ECU2 SID\$09 INF\$00	\$48 \$6B \$11 \$49 \$00 \$3F \$00 \$00 \$00
1850VPW ECU2 SID\$09 INF\$00 DIESEL	\$48 \$6B \$11 \$49 \$00 \$3E \$20 \$00 \$00
1850VPW ECU2 SID\$09 INF\$03	\$48 \$6B \$11 \$49 \$03 \$04
1850VPW ECU2 SID\$09 INF\$03 9	\$48 \$6B \$11 \$49 \$03 \$24
1850VPW ECU2 SID\$09 INF\$04 M1	\$48 \$6B \$11 \$49 \$04 \$01 \$32 \$34 \$32 \$32
1850VPW ECU2 SID\$09 INF\$04 M2	\$48 \$6B \$11 \$49 \$04 \$02 \$31 \$34 \$31 \$39
1850VPW ECU2 SID\$09 INF\$04 M3	\$48 \$6B \$11 \$49 \$04 \$03 \$00 \$00 \$00 \$00
1850VPW ECU2 SID\$09 INF\$04 M4	\$48 \$6B \$11 \$49 \$04 \$04 \$00 \$00 \$00 \$00
1850VPW ECU2 SID\$09 INF\$04 9 M1	\$48 \$6B \$11 \$49 \$04 \$01 \$31 \$32 \$35 \$38
1850VPW ECU2 SID\$09 INF\$04 9 M2	\$48 \$6B \$11 \$49 \$04 \$02 \$39 \$37 \$37 \$38
1850VPW ECU2 SID\$09 INF\$04 9 M3	\$48 \$6B \$11 \$49 \$04 \$03 \$00 \$00 \$00 \$00
1850VPW ECU2 SID\$09 INF\$04 9 M4	\$48 \$6B \$11 \$49 \$04 \$04 \$00 \$00 \$00 \$00
1850VPW ECU2 SID\$09 INF\$04 9 M5	\$48 \$6B \$11 \$49 \$04 \$05 \$32 \$32 \$35 \$38
1850VPW ECU2 SID\$09 INF\$04 9 M6	\$48 \$6B \$11 \$49 \$04 \$06 \$39 \$37 \$37 \$37
1850VPW ECU2 SID\$09 INF\$04 9 M7	\$48 \$6B \$11 \$49 \$04 \$07 \$00 \$00 \$00 \$00
1850VPW ECU2 SID\$09 INF\$04 9 M8	\$48 \$6B \$11 \$49 \$04 \$08 \$00 \$00 \$00 \$00
1850VPW ECU2 SID\$09 INF\$04 9 M9	\$48 \$6B \$11 \$49 \$04 \$09 \$33 \$32 \$35 \$38
1850VPW ECU2 SID\$09 INF\$04 9 M10	\$48 \$6B \$11 \$49 \$04 \$0A \$39 \$37 \$37 \$36
1850VPW ECU2 SID\$09 INF\$04 9 M11	\$48 \$6B \$11 \$49 \$04 \$0B \$00 \$00 \$00 \$00
1850VPW ECU2 SID\$09 INF\$04 9 M12	\$48 \$6B \$11 \$49 \$04 \$0C \$00 \$00 \$00 \$00
1850VPW ECU2 SID\$09 INF\$04 9 M13	\$48 \$6B \$11 \$49 \$04 \$0D \$34 \$32 \$35 \$38
1850VPW ECU2 SID\$09 INF\$04 9 M14	\$48 \$6B \$11 \$49 \$04 \$0E \$39 \$37 \$37 \$35
1850VPW ECU2 SID\$09 INF\$04 9 M15	\$48 \$6B \$11 \$49 \$04 \$0F \$00 \$00 \$00 \$00
1850VPW ECU2 SID\$09 INF\$04 9 M16	\$48 \$6B \$11 \$49 \$04 \$10 \$00 \$00 \$00 \$00
1850VPW ECU2 SID\$09 INF\$04 9 M17	\$48 \$6B \$11 \$49 \$04 \$11 \$35 \$32 \$35 \$38
1850VPW ECU2 SID\$09 INF\$04 9 M18	\$48 \$6B \$11 \$49 \$04 \$12 \$39 \$37 \$37 \$34
1850VPW ECU2 SID\$09 INF\$04 9 M19	\$48 \$6B \$11 \$49 \$04 \$13 \$00 \$00 \$00 \$00
1850VPW ECU2 SID\$09 INF\$04 9 M20	\$48 \$6B \$11 \$49 \$04 \$14 \$00 \$00 \$00 \$00
1850VPW ECU2 SID\$09 INF\$04 9 M21	\$48 \$6B \$11 \$49 \$04 \$15 \$36 \$32 \$35 \$38
1850VPW ECU2 SID\$09 INF\$04 9 M22	\$48 \$6B \$11 \$49 \$04 \$16 \$39 \$37 \$37 \$33
1850VPW ECU2 SID\$09 INF\$04 9 M23	\$48 \$6B \$11 \$49 \$04 \$17 \$00 \$00 \$00 \$00
1850VPW ECU2 SID\$09 INF\$04 9 M24	\$48 \$6B \$11 \$49 \$04 \$18 \$00 \$00 \$00 \$00
1850VPW ECU2 SID\$09 INF\$04 9 M25	\$48 \$6B \$11 \$49 \$04 \$19 \$37 \$32 \$35 \$38
1850VPW ECU2 SID\$09 INF\$04 9 M26	\$48 \$6B \$11 \$49 \$04 \$1A \$39 \$37 \$37 \$32
1850VPW ECU2 SID\$09 INF\$04 9 M27	\$48 \$6B \$11 \$49 \$04 \$1B \$00 \$00 \$00 \$00
1850VPW ECU2 SID\$09 INF\$04 9 M28	\$48 \$6B \$11 \$49 \$04 \$1C \$00 \$00 \$00 \$00
1850VPW ECU2 SID\$09 INF\$04 9 M29	\$48 \$6B \$11 \$49 \$04 \$1D \$38 \$32 \$35 \$38
1850VPW ECU2 SID\$09 INF\$04 9 M30	\$48 \$6B \$11 \$49 \$04 \$1E \$39 \$37 \$37 \$31
1850VPW ECU2 SID\$09 INF\$04 9 M31	\$48 \$6B \$11 \$49 \$04 \$1F \$00 \$00 \$00 \$00
1850VPW ECU2 SID\$09 INF\$04 9 M32	\$48 \$6B \$11 \$49 \$04 \$20 \$00 \$00 \$00 \$00
1850VPW ECU2 SID\$09 INF\$04 9 M33	\$48 \$6B \$11 \$49 \$04 \$21 \$39 \$32 \$35 \$38
1850VPW ECU2 SID\$09 INF\$04 9 M34	\$48 \$6B \$11 \$49 \$04 \$22 \$39 \$37 \$37 \$30
1850VPW ECU2 SID\$09 INF\$04 9 M35	\$48 \$6B \$11 \$49 \$04 \$23 \$00 \$00 \$00 \$00
1850VPW ECU2 SID\$09 INF\$04 9 M36	\$48 \$6B \$11 \$49 \$04 \$24 \$00 \$00 \$00 \$00
1850VPW ECU2 SID\$09 INF\$05	\$48 \$6B \$11 \$49 \$05 \$01

1850VPW ECU2 SID\$09 INF\$05 9	\$48 \$6B \$11 \$49 \$05 \$09
1850VPW ECU2 SID\$09 INF\$06	\$48 \$6B \$11 \$49 \$06 \$01 \$27 \$91 \$BC \$88
1850VPW ECU2 SID\$09 INF\$06 9 M1	\$48 \$6B \$11 \$49 \$06 \$01 \$27 \$91 \$BC \$88
1850VPW ECU2 SID\$09 INF\$06 9 M2	\$48 \$6B \$11 \$49 \$06 \$02 \$28 \$91 \$BC \$87
1850VPW ECU2 SID\$09 INF\$06 9 M3	\$48 \$6B \$11 \$49 \$06 \$03 \$29 \$91 \$BC \$86
1850VPW ECU2 SID\$09 INF\$06 9 M4	\$48 \$6B \$11 \$49 \$06 \$04 \$2A \$91 \$BC \$85
1850VPW ECU2 SID\$09 INF\$06 9 M5	\$48 \$6B \$11 \$49 \$06 \$05 \$2B \$91 \$BC \$84
1850VPW ECU2 SID\$09 INF\$06 9 M6	\$48 \$6B \$11 \$49 \$06 \$06 \$2C \$91 \$BC \$83
1850VPW ECU2 SID\$09 INF\$06 9 M7	\$48 \$6B \$11 \$49 \$06 \$07 \$2D \$91 \$BC \$82
1850VPW ECU2 SID\$09 INF\$06 9 M8	\$48 \$6B \$11 \$49 \$06 \$08 \$2E \$55 \$AA \$81
1850VPW ECU2 SID\$09 INF\$06 9 M9	\$48 \$6B \$11 \$49 \$06 \$09 \$2F \$FF \$00 \$80
1850VPW ECU2 SID\$09 INF\$07 832	\$48 \$6B \$11 \$49 \$07 \$08
1850VPW ECU2 SID\$09 INF\$07 840	\$48 \$6B \$11 \$49 \$07 \$0A
1850VPW ECU2 SID\$09 INF\$07 B32	\$48 \$6B \$11 \$49 \$07 \$08
1850VPW ECU2 SID\$09 INF\$07 B36	\$48 \$6B \$11 \$49 \$07 \$09
1850VPW ECU2 SID\$09 INF\$08 32 M1	\$48 \$6B \$11 \$49 \$08 \$01 \$00 \$64 \$00 \$69
1850VPW ECU2 SID\$09 INF\$08 32 M2	\$48 \$6B \$11 \$49 \$08 \$02 \$00 \$00 \$00 \$00
1850VPW ECU2 SID\$09 INF\$08 32 M3	\$48 \$6B \$11 \$49 \$08 \$03 \$00 \$14 \$00 \$19
1850VPW ECU2 SID\$09 INF\$08 32 M4	\$48 \$6B \$11 \$49 \$08 \$04 \$00 \$00 \$00 \$00
1850VPW ECU2 SID\$09 INF\$08 32 M5	\$48 \$6B \$11 \$49 \$08 \$05 \$00 \$28 \$00 \$2D
1850VPW ECU2 SID\$09 INF\$08 32 M6	\$48 \$6B \$11 \$49 \$08 \$06 \$00 \$00 \$00 \$00
1850VPW ECU2 SID\$09 INF\$08 32 M7	\$48 \$6B \$11 \$49 \$08 \$07 \$00 \$00 \$00 \$00
1850VPW ECU2 SID\$09 INF\$08 32 M8	\$48 \$6B \$11 \$49 \$08 \$08 \$00 \$00 \$00 \$00
1850VPW ECU2 SID\$09 INF\$08 40 M1	\$48 \$6B \$11 \$49 \$08 \$01 \$00 \$64 \$00 \$69
1850VPW ECU2 SID\$09 INF\$08 40 M2	\$48 \$6B \$11 \$49 \$08 \$02 \$00 \$00 \$00 \$00
1850VPW ECU2 SID\$09 INF\$08 40 M3	\$48 \$6B \$11 \$49 \$08 \$03 \$00 \$14 \$00 \$19
1850VPW ECU2 SID\$09 INF\$08 40 M4	\$48 \$6B \$11 \$49 \$08 \$04 \$00 \$00 \$00 \$00
1850VPW ECU2 SID\$09 INF\$08 40 M5	\$48 \$6B \$11 \$49 \$08 \$05 \$00 \$28 \$00 \$2D
1850VPW ECU2 SID\$09 INF\$08 40 M6	\$48 \$6B \$11 \$49 \$08 \$06 \$00 \$00 \$00 \$00
1850VPW ECU2 SID\$09 INF\$08 40 M7	\$48 \$6B \$11 \$49 \$08 \$07 \$00 \$00 \$00 \$00
1850VPW ECU2 SID\$09 INF\$08 40 M8	\$48 \$6B \$11 \$49 \$08 \$08 \$00 \$00 \$00 \$00
1850VPW ECU2 SID\$09 INF\$08 40 M9	\$48 \$6B \$11 \$49 \$08 \$09 \$00 \$00 \$00 \$00
1850VPW ECU2 SID\$09 INF\$08 40 M10	\$48 \$6B \$11 \$49 \$08 \$0A \$00 \$5A \$00 \$5F
1850VPW ECU2 SID\$09 INF\$0B 32 M1	\$48 \$6B \$11 \$49 \$0B \$01 \$00 \$64 \$00 \$69
1850VPW ECU2 SID\$09 INF\$0B 32 M2	\$48 \$6B \$11 \$49 \$0B \$02 \$00 \$00 \$00 \$00
1850VPW ECU2 SID\$09 INF\$0B 32 M3	\$48 \$6B \$11 \$49 \$0B \$03 \$00 \$14 \$00 \$19
1850VPW ECU2 SID\$09 INF\$0B 32 M4	\$48 \$6B \$11 \$49 \$0B \$04 \$00 \$00 \$00 \$00
1850VPW ECU2 SID\$09 INF\$0B 32 M5	\$48 \$6B \$11 \$49 \$0B \$05 \$00 \$28 \$00 \$2D
1850VPW ECU2 SID\$09 INF\$0B 32 M6	\$48 \$6B \$11 \$49 \$0B \$06 \$00 \$00 \$00 \$00
1850VPW ECU2 SID\$09 INF\$0B 32 M7	\$48 \$6B \$11 \$49 \$0B \$07 \$00 \$00 \$00 \$00
1850VPW ECU2 SID\$09 INF\$0B 32 M8	\$48 \$6B \$11 \$49 \$0B \$08 \$00 \$00 \$00 \$00
1850VPW ECU2 SID\$09 INF\$0B 36 M1	\$48 \$6B \$11 \$49 \$0B \$01 \$00 \$64 \$00 \$69
1850VPW ECU2 SID\$09 INF\$0B 36 M2	\$48 \$6B \$11 \$49 \$0B \$02 \$00 \$00 \$00 \$00
1850VPW ECU2 SID\$09 INF\$0B 36 M3	\$48 \$6B \$11 \$49 \$0B \$03 \$00 \$14 \$00 \$19
1850VPW ECU2 SID\$09 INF\$0B 36 M4	\$48 \$6B \$11 \$49 \$0B \$04 \$00 \$00 \$00 \$00
1850VPW ECU2 SID\$09 INF\$0B 36 M5	\$48 \$6B \$11 \$49 \$0B \$05 \$00 \$28 \$00 \$2D
1850VPW ECU2 SID\$09 INF\$0B 36 M6	\$48 \$6B \$11 \$49 \$0B \$06 \$00 \$00 \$00 \$00
1850VPW ECU2 SID\$09 INF\$0B 36 M7	\$48 \$6B \$11 \$49 \$0B \$07 \$00 \$00 \$00 \$00
1850VPW ECU2 SID\$09 INF\$0B 36 M8	\$48 \$6B \$11 \$49 \$0B \$08 \$00 \$00 \$00 \$00
1850VPW ECU2 SID\$09 INF\$0B 36 M9	\$48 \$6B \$11 \$49 \$0B \$09 \$00 \$00 \$00 \$00
1850VPW ECU3 SID\$09 INF\$00	\$48 \$6B \$12 \$49 \$00 \$3C \$00 \$00 \$00
1850VPW ECU3 SID\$09 INF\$03 9	\$48 \$6B \$12 \$49 \$03 \$24
1850VPW ECU3 SID\$09 INF\$04 9 M1	\$48 \$6B \$12 \$49 \$04 \$01 \$31 \$33 \$35 \$38
1850VPW ECU3 SID\$09 INF\$04 9 M2	\$48 \$6B \$12 \$49 \$04 \$02 \$39 \$37 \$37 \$38
1850VPW ECU3 SID\$09 INF\$04 9 M3	\$48 \$6B \$12 \$49 \$04 \$03 \$00 \$00 \$00 \$00
1850VPW ECU3 SID\$09 INF\$04 9 M4	\$48 \$6B \$12 \$49 \$04 \$04 \$00 \$00 \$00 \$00
1850VPW ECU3 SID\$09 INF\$04 9 M5	\$48 \$6B \$12 \$49 \$04 \$05 \$32 \$33 \$35 \$38
1850VPW ECU3 SID\$09 INF\$04 9 M6	\$48 \$6B \$12 \$49 \$04 \$06 \$39 \$37 \$37 \$37

1850VPW ECU3 SID\$09 INF\$04 9 M7	\$48 \$6B \$12 \$49 \$04 \$07 \$00 \$00 \$00 \$00
1850VPW ECU3 SID\$09 INF\$04 9 M8	\$48 \$6B \$12 \$49 \$04 \$08 \$00 \$00 \$00 \$00
1850VPW ECU3 SID\$09 INF\$04 9 M9	\$48 \$6B \$12 \$49 \$04 \$09 \$33 \$33 \$35 \$38
1850VPW ECU3 SID\$09 INF\$04 9 M10	\$48 \$6B \$12 \$49 \$04 \$0A \$39 \$37 \$37 \$36
1850VPW ECU3 SID\$09 INF\$04 9 M11	\$48 \$6B \$12 \$49 \$04 \$0B \$00 \$00 \$00 \$00
1850VPW ECU3 SID\$09 INF\$04 9 M12	\$48 \$6B \$12 \$49 \$04 \$0C \$00 \$00 \$00 \$00
1850VPW ECU3 SID\$09 INF\$04 9 M13	\$48 \$6B \$12 \$49 \$04 \$0D \$34 \$33 \$35 \$38
1850VPW ECU3 SID\$09 INF\$04 9 M14	\$48 \$6B \$12 \$49 \$04 \$0E \$39 \$37 \$37 \$35
1850VPW ECU3 SID\$09 INF\$04 9 M15	\$48 \$6B \$12 \$49 \$04 \$0F \$00 \$00 \$00 \$00
1850VPW ECU3 SID\$09 INF\$04 9 M16	\$48 \$6B \$12 \$49 \$04 \$10 \$00 \$00 \$00 \$00
1850VPW ECU3 SID\$09 INF\$04 9 M17	\$48 \$6B \$12 \$49 \$04 \$11 \$35 \$33 \$35 \$38
1850VPW ECU3 SID\$09 INF\$04 9 M18	\$48 \$6B \$12 \$49 \$04 \$12 \$39 \$37 \$37 \$34
1850VPW ECU3 SID\$09 INF\$04 9 M19	\$48 \$6B \$12 \$49 \$04 \$13 \$00 \$00 \$00 \$00
1850VPW ECU3 SID\$09 INF\$04 9 M20	\$48 \$6B \$12 \$49 \$04 \$14 \$00 \$00 \$00 \$00
1850VPW ECU3 SID\$09 INF\$04 9 M21	\$48 \$6B \$12 \$49 \$04 \$15 \$36 \$33 \$35 \$38
1850VPW ECU3 SID\$09 INF\$04 9 M22	\$48 \$6B \$12 \$49 \$04 \$16 \$39 \$37 \$37 \$33
1850VPW ECU3 SID\$09 INF\$04 9 M23	\$48 \$6B \$12 \$49 \$04 \$17 \$00 \$00 \$00 \$00
1850VPW ECU3 SID\$09 INF\$04 9 M24	\$48 \$6B \$12 \$49 \$04 \$18 \$00 \$00 \$00 \$00
1850VPW ECU3 SID\$09 INF\$04 9 M25	\$48 \$6B \$12 \$49 \$04 \$19 \$37 \$33 \$35 \$38
1850VPW ECU3 SID\$09 INF\$04 9 M26	\$48 \$6B \$12 \$49 \$04 \$1A \$39 \$37 \$37 \$32
1850VPW ECU3 SID\$09 INF\$04 9 M27	\$48 \$6B \$12 \$49 \$04 \$1B \$00 \$00 \$00 \$00
1850VPW ECU3 SID\$09 INF\$04 9 M28	\$48 \$6B \$12 \$49 \$04 \$1C \$00 \$00 \$00 \$00
1850VPW ECU3 SID\$09 INF\$04 9 M29	\$48 \$6B \$12 \$49 \$04 \$1D \$38 \$33 \$35 \$38
1850VPW ECU3 SID\$09 INF\$04 9 M30	\$48 \$6B \$12 \$49 \$04 \$1E \$39 \$37 \$37 \$31
1850VPW ECU3 SID\$09 INF\$04 9 M31	\$48 \$6B \$12 \$49 \$04 \$1F \$00 \$00 \$00 \$00
1850VPW ECU3 SID\$09 INF\$04 9 M32	\$48 \$6B \$12 \$49 \$04 \$20 \$00 \$00 \$00 \$00
1850VPW ECU3 SID\$09 INF\$04 9 M33	\$48 \$6B \$12 \$49 \$04 \$21 \$39 \$33 \$35 \$38
1850VPW ECU3 SID\$09 INF\$04 9 M34	\$48 \$6B \$12 \$49 \$04 \$22 \$39 \$37 \$37 \$30
1850VPW ECU3 SID\$09 INF\$04 9 M35	\$48 \$6B \$12 \$49 \$04 \$23 \$00 \$00 \$00 \$00
1850VPW ECU3 SID\$09 INF\$04 9 M36	\$48 \$6B \$12 \$49 \$04 \$24 \$00 \$00 \$00 \$00
1850VPW ECU3 SID\$09 INF\$05 9	\$48 \$6B \$12 \$49 \$05 \$09
1850VPW ECU3 SID\$09 INF\$06 9 M1	\$48 \$6B \$12 \$49 \$06 \$01 \$37 \$91 \$BC \$88
1850VPW ECU3 SID\$09 INF\$06 9 M2	\$48 \$6B \$12 \$49 \$06 \$02 \$38 \$91 \$BC \$87
1850VPW ECU3 SID\$09 INF\$06 9 M3	\$48 \$6B \$12 \$49 \$06 \$03 \$39 \$91 \$BC \$86
1850VPW ECU3 SID\$09 INF\$06 9 M4	\$48 \$6B \$12 \$49 \$06 \$04 \$3A \$91 \$BC \$85
1850VPW ECU3 SID\$09 INF\$06 9 M5	\$48 \$6B \$12 \$49 \$06 \$05 \$3B \$91 \$BC \$84
1850VPW ECU3 SID\$09 INF\$06 9 M6	\$48 \$6B \$12 \$49 \$06 \$06 \$3C \$91 \$BC \$83
1850VPW ECU3 SID\$09 INF\$06 9 M7	\$48 \$6B \$12 \$49 \$06 \$07 \$3D \$91 \$BC \$82
1850VPW ECU3 SID\$09 INF\$06 9 M8	\$48 \$6B \$12 \$49 \$06 \$08 \$3E \$55 \$AA \$81
1850VPW ECU3 SID\$09 INF\$06 9 M9	\$48 \$6B \$12 \$49 \$06 \$09 \$3F \$FF \$00 \$80
1850VPW ECU4 SID\$09 INF\$00	\$48 \$6B \$13 \$49 \$00 \$3C \$00 \$00 \$00
1850VPW ECU4 SID\$09 INF\$03 9	\$48 \$6B \$13 \$49 \$03 \$24
1850VPW ECU4 SID\$09 INF\$04 9 M1	\$48 \$6B \$13 \$49 \$04 \$01 \$31 \$34 \$35 \$38
1850VPW ECU4 SID\$09 INF\$04 9 M2	\$48 \$6B \$13 \$49 \$04 \$02 \$39 \$37 \$37 \$38
1850VPW ECU4 SID\$09 INF\$04 9 M3	\$48 \$6B \$13 \$49 \$04 \$03 \$00 \$00 \$00 \$00
1850VPW ECU4 SID\$09 INF\$04 9 M4	\$48 \$6B \$13 \$49 \$04 \$04 \$00 \$00 \$00 \$00
1850VPW ECU4 SID\$09 INF\$04 9 M5	\$48 \$6B \$13 \$49 \$04 \$05 \$32 \$34 \$35 \$38
1850VPW ECU4 SID\$09 INF\$04 9 M6	\$48 \$6B \$13 \$49 \$04 \$06 \$39 \$37 \$37 \$37
1850VPW ECU4 SID\$09 INF\$04 9 M7	\$48 \$6B \$13 \$49 \$04 \$07 \$00 \$00 \$00 \$00
1850VPW ECU4 SID\$09 INF\$04 9 M8	\$48 \$6B \$13 \$49 \$04 \$08 \$00 \$00 \$00 \$00
1850VPW ECU4 SID\$09 INF\$04 9 M9	\$48 \$6B \$13 \$49 \$04 \$09 \$33 \$34 \$35 \$38
1850VPW ECU4 SID\$09 INF\$04 9 M10	\$48 \$6B \$13 \$49 \$04 \$0A \$39 \$37 \$37 \$36
1850VPW ECU4 SID\$09 INF\$04 9 M11	\$48 \$6B \$13 \$49 \$04 \$0B \$00 \$00 \$00 \$00
1850VPW ECU4 SID\$09 INF\$04 9 M12	\$48 \$6B \$13 \$49 \$04 \$0C \$00 \$00 \$00 \$00
1850VPW ECU4 SID\$09 INF\$04 9 M13	\$48 \$6B \$13 \$49 \$04 \$0D \$34 \$34 \$35 \$38
1850VPW ECU4 SID\$09 INF\$04 9 M14	\$48 \$6B \$13 \$49 \$04 \$0E \$39 \$37 \$37 \$35
1850VPW ECU4 SID\$09 INF\$04 9 M15	\$48 \$6B \$13 \$49 \$04 \$0F \$00 \$00 \$00 \$00
1850VPW ECU4 SID\$09 INF\$04 9 M16	\$48 \$6B \$13 \$49 \$04 \$10 \$00 \$00 \$00 \$00

1850VPW ECU4 SID\$09 INF\$04 9 M17	\$48 \$6B \$13 \$49 \$04 \$11 \$35 \$34 \$35 \$38
1850VPW ECU4 SID\$09 INF\$04 9 M18	\$48 \$6B \$13 \$49 \$04 \$12 \$39 \$37 \$37 \$34
1850VPW ECU4 SID\$09 INF\$04 9 M19	\$48 \$6B \$13 \$49 \$04 \$13 \$00 \$00 \$00 \$00
1850VPW ECU4 SID\$09 INF\$04 9 M20	\$48 \$6B \$13 \$49 \$04 \$14 \$00 \$00 \$00 \$00
1850VPW ECU4 SID\$09 INF\$04 9 M21	\$48 \$6B \$13 \$49 \$04 \$15 \$36 \$34 \$35 \$38
1850VPW ECU4 SID\$09 INF\$04 9 M22	\$48 \$6B \$13 \$49 \$04 \$16 \$39 \$37 \$37 \$33
1850VPW ECU4 SID\$09 INF\$04 9 M23	\$48 \$6B \$13 \$49 \$04 \$17 \$00 \$00 \$00 \$00
1850VPW ECU4 SID\$09 INF\$04 9 M24	\$48 \$6B \$13 \$49 \$04 \$18 \$00 \$00 \$00 \$00
1850VPW ECU4 SID\$09 INF\$04 9 M25	\$48 \$6B \$13 \$49 \$04 \$19 \$37 \$34 \$35 \$38
1850VPW ECU4 SID\$09 INF\$04 9 M26	\$48 \$6B \$13 \$49 \$04 \$1A \$39 \$37 \$37 \$32
1850VPW ECU4 SID\$09 INF\$04 9 M27	\$48 \$6B \$13 \$49 \$04 \$1B \$00 \$00 \$00 \$00
1850VPW ECU4 SID\$09 INF\$04 9 M28	\$48 \$6B \$13 \$49 \$04 \$1C \$00 \$00 \$00 \$00
1850VPW ECU4 SID\$09 INF\$04 9 M29	\$48 \$6B \$13 \$49 \$04 \$1D \$38 \$34 \$35 \$38
1850VPW ECU4 SID\$09 INF\$04 9 M30	\$48 \$6B \$13 \$49 \$04 \$1E \$39 \$37 \$37 \$31
1850VPW ECU4 SID\$09 INF\$04 9 M31	\$48 \$6B \$13 \$49 \$04 \$1F \$00 \$00 \$00 \$00
1850VPW ECU4 SID\$09 INF\$04 9 M32	\$48 \$6B \$13 \$49 \$04 \$20 \$00 \$00 \$00 \$00
1850VPW ECU4 SID\$09 INF\$04 9 M33	\$48 \$6B \$13 \$49 \$04 \$21 \$39 \$34 \$35 \$38
1850VPW ECU4 SID\$09 INF\$04 9 M34	\$48 \$6B \$13 \$49 \$04 \$22 \$39 \$37 \$37 \$30
1850VPW ECU4 SID\$09 INF\$04 9 M35	\$48 \$6B \$13 \$49 \$04 \$23 \$00 \$00 \$00 \$00
1850VPW ECU4 SID\$09 INF\$04 9 M36	\$48 \$6B \$13 \$49 \$04 \$24 \$00 \$00 \$00 \$00
1850VPW ECU4 SID\$09 INF\$05 9	\$48 \$6B \$13 \$49 \$05 \$09
1850VPW ECU4 SID\$09 INF\$06 9 M1	\$48 \$6B \$13 \$49 \$06 \$01 \$47 \$91 \$BC \$88
1850VPW ECU4 SID\$09 INF\$06 9 M2	\$48 \$6B \$13 \$49 \$06 \$02 \$48 \$91 \$BC \$87
1850VPW ECU4 SID\$09 INF\$06 9 M3	\$48 \$6B \$13 \$49 \$06 \$03 \$49 \$91 \$BC \$86
1850VPW ECU4 SID\$09 INF\$06 9 M4	\$48 \$6B \$13 \$49 \$06 \$04 \$4A \$91 \$BC \$85
1850VPW ECU4 SID\$09 INF\$06 9 M5	\$48 \$6B \$13 \$49 \$06 \$05 \$4B \$91 \$BC \$84
1850VPW ECU4 SID\$09 INF\$06 9 M6	\$48 \$6B \$13 \$49 \$06 \$06 \$4C \$91 \$BC \$83
1850VPW ECU4 SID\$09 INF\$06 9 M7	\$48 \$6B \$13 \$49 \$06 \$07 \$4D \$91 \$BC \$82
1850VPW ECU4 SID\$09 INF\$06 9 M8	\$48 \$6B \$13 \$49 \$06 \$08 \$4E \$55 \$AA \$81
1850VPW ECU4 SID\$09 INF\$06 9 M9	\$48 \$6B \$13 \$49 \$06 \$09 \$4F \$FF \$00 \$80
1850VPW ECU5 SID\$09 INF\$00	\$48 \$6B \$14 \$49 \$00 \$3C \$00 \$00 \$00
1850VPW ECU5 SID\$09 INF\$03 9	\$48 \$6B \$14 \$49 \$03 \$24
1850VPW ECU5 SID\$09 INF\$04 9 M1	\$48 \$6B \$14 \$49 \$04 \$01 \$31 \$35 \$35 \$38
1850VPW ECU5 SID\$09 INF\$04 9 M2	\$48 \$6B \$14 \$49 \$04 \$02 \$39 \$37 \$37 \$38
1850VPW ECU5 SID\$09 INF\$04 9 M3	\$48 \$6B \$14 \$49 \$04 \$03 \$00 \$00 \$00 \$00
1850VPW ECU5 SID\$09 INF\$04 9 M4	\$48 \$6B \$14 \$49 \$04 \$04 \$00 \$00 \$00 \$00
1850VPW ECU5 SID\$09 INF\$04 9 M5	\$48 \$6B \$14 \$49 \$04 \$05 \$32 \$35 \$35 \$38
1850VPW ECU5 SID\$09 INF\$04 9 M6	\$48 \$6B \$14 \$49 \$04 \$06 \$39 \$37 \$37 \$37
1850VPW ECU5 SID\$09 INF\$04 9 M7	\$48 \$6B \$14 \$49 \$04 \$07 \$00 \$00 \$00 \$00
1850VPW ECU5 SID\$09 INF\$04 9 M8	\$48 \$6B \$14 \$49 \$04 \$08 \$00 \$00 \$00 \$00
1850VPW ECU5 SID\$09 INF\$04 9 M9	\$48 \$6B \$14 \$49 \$04 \$09 \$33 \$35 \$35 \$38
1850VPW ECU5 SID\$09 INF\$04 9 M10	\$48 \$6B \$14 \$49 \$04 \$0A \$39 \$37 \$37 \$36
1850VPW ECU5 SID\$09 INF\$04 9 M11	\$48 \$6B \$14 \$49 \$04 \$0B \$00 \$00 \$00 \$00
1850VPW ECU5 SID\$09 INF\$04 9 M12	\$48 \$6B \$14 \$49 \$04 \$0C \$00 \$00 \$00 \$00
1850VPW ECU5 SID\$09 INF\$04 9 M13	\$48 \$6B \$14 \$49 \$04 \$0D \$34 \$35 \$35 \$38
1850VPW ECU5 SID\$09 INF\$04 9 M14	\$48 \$6B \$14 \$49 \$04 \$0E \$39 \$37 \$37 \$35
1850VPW ECU5 SID\$09 INF\$04 9 M15	\$48 \$6B \$14 \$49 \$04 \$0F \$00 \$00 \$00 \$00
1850VPW ECU5 SID\$09 INF\$04 9 M16	\$48 \$6B \$14 \$49 \$04 \$10 \$00 \$00 \$00 \$00
1850VPW ECU5 SID\$09 INF\$04 9 M17	\$48 \$6B \$14 \$49 \$04 \$11 \$35 \$35 \$35 \$38
1850VPW ECU5 SID\$09 INF\$04 9 M18	\$48 \$6B \$14 \$49 \$04 \$12 \$39 \$37 \$37 \$34
1850VPW ECU5 SID\$09 INF\$04 9 M19	\$48 \$6B \$14 \$49 \$04 \$13 \$00 \$00 \$00 \$00
1850VPW ECU5 SID\$09 INF\$04 9 M20	\$48 \$6B \$14 \$49 \$04 \$14 \$00 \$00 \$00 \$00
1850VPW ECU5 SID\$09 INF\$04 9 M21	\$48 \$6B \$14 \$49 \$04 \$15 \$36 \$35 \$35 \$38
1850VPW ECU5 SID\$09 INF\$04 9 M22	\$48 \$6B \$14 \$49 \$04 \$16 \$39 \$37 \$37 \$33
1850VPW ECU5 SID\$09 INF\$04 9 M23	\$48 \$6B \$14 \$49 \$04 \$17 \$00 \$00 \$00 \$00
1850VPW ECU5 SID\$09 INF\$04 9 M24	\$48 \$6B \$14 \$49 \$04 \$18 \$00 \$00 \$00 \$00
1850VPW ECU5 SID\$09 INF\$04 9 M25	\$48 \$6B \$14 \$49 \$04 \$19 \$37 \$35 \$35 \$38
1850VPW ECU5 SID\$09 INF\$04 9 M26	\$48 \$6B \$14 \$49 \$04 \$1A \$39 \$37 \$37 \$32

1850VPW ECU5 SID\$09 INF\$04 9 M27	\$48 \$6B \$14 \$49 \$04 \$1B \$00 \$00 \$00 \$00
1850VPW ECU5 SID\$09 INF\$04 9 M28	\$48 \$6B \$14 \$49 \$04 \$1C \$00 \$00 \$00 \$00
1850VPW ECU5 SID\$09 INF\$04 9 M29	\$48 \$6B \$14 \$49 \$04 \$1D \$38 \$35 \$35 \$38
1850VPW ECU5 SID\$09 INF\$04 9 M30	\$48 \$6B \$14 \$49 \$04 \$1E \$39 \$37 \$37 \$31
1850VPW ECU5 SID\$09 INF\$04 9 M31	\$48 \$6B \$14 \$49 \$04 \$1F \$00 \$00 \$00 \$00
1850VPW ECU5 SID\$09 INF\$04 9 M32	\$48 \$6B \$14 \$49 \$04 \$20 \$00 \$00 \$00 \$00
1850VPW ECU5 SID\$09 INF\$04 9 M33	\$48 \$6B \$14 \$49 \$04 \$21 \$39 \$35 \$35 \$38
1850VPW ECU5 SID\$09 INF\$04 9 M34	\$48 \$6B \$14 \$49 \$04 \$22 \$39 \$37 \$37 \$30
1850VPW ECU5 SID\$09 INF\$04 9 M35	\$48 \$6B \$14 \$49 \$04 \$23 \$00 \$00 \$00 \$00
1850VPW ECU5 SID\$09 INF\$04 9 M36	\$48 \$6B \$14 \$49 \$04 \$24 \$00 \$00 \$00 \$00
1850VPW ECU5 SID\$09 INF\$05 9	\$48 \$6B \$14 \$49 \$05 \$09
1850VPW ECU5 SID\$09 INF\$06 9 M1	\$48 \$6B \$14 \$49 \$06 \$01 \$57 \$91 \$BC \$88
1850VPW ECU5 SID\$09 INF\$06 9 M2	\$48 \$6B \$14 \$49 \$06 \$02 \$58 \$91 \$BC \$87
1850VPW ECU5 SID\$09 INF\$06 9 M3	\$48 \$6B \$14 \$49 \$06 \$03 \$59 \$91 \$BC \$86
1850VPW ECU5 SID\$09 INF\$06 9 M4	\$48 \$6B \$14 \$49 \$06 \$04 \$5A \$91 \$BC \$85
1850VPW ECU5 SID\$09 INF\$06 9 M5	\$48 \$6B \$14 \$49 \$06 \$05 \$5B \$91 \$BC \$84
1850VPW ECU5 SID\$09 INF\$06 9 M6	\$48 \$6B \$14 \$49 \$06 \$06 \$5C \$91 \$BC \$83
1850VPW ECU5 SID\$09 INF\$06 9 M7	\$48 \$6B \$14 \$49 \$06 \$07 \$5D \$91 \$BC \$82
1850VPW ECU5 SID\$09 INF\$06 9 M8	\$48 \$6B \$14 \$49 \$06 \$08 \$5E \$55 \$AA \$81
1850VPW ECU5 SID\$09 INF\$06 9 M9	\$48 \$6B \$14 \$49 \$06 \$09 \$5F \$FF \$00 \$80
1850VPW ECU6 SID\$09 INF\$00	\$48 \$6B \$15 \$49 \$00 \$3C \$00 \$00 \$00
1850VPW ECU6 SID\$09 INF\$03 9	\$48 \$6B \$15 \$49 \$03 \$24
1850VPW ECU6 SID\$09 INF\$04 9 M1	\$48 \$6B \$15 \$49 \$04 \$01 \$31 \$36 \$35 \$38
1850VPW ECU6 SID\$09 INF\$04 9 M2	\$48 \$6B \$15 \$49 \$04 \$02 \$39 \$37 \$37 \$38
1850VPW ECU6 SID\$09 INF\$04 9 M3	\$48 \$6B \$15 \$49 \$04 \$03 \$00 \$00 \$00 \$00
1850VPW ECU6 SID\$09 INF\$04 9 M4	\$48 \$6B \$15 \$49 \$04 \$04 \$00 \$00 \$00 \$00
1850VPW ECU6 SID\$09 INF\$04 9 M5	\$48 \$6B \$15 \$49 \$04 \$05 \$32 \$36 \$35 \$38
1850VPW ECU6 SID\$09 INF\$04 9 M6	\$48 \$6B \$15 \$49 \$04 \$06 \$39 \$37 \$37 \$37
1850VPW ECU6 SID\$09 INF\$04 9 M7	\$48 \$6B \$15 \$49 \$04 \$07 \$00 \$00 \$00 \$00
1850VPW ECU6 SID\$09 INF\$04 9 M8	\$48 \$6B \$15 \$49 \$04 \$08 \$00 \$00 \$00 \$00
1850VPW ECU6 SID\$09 INF\$04 9 M9	\$48 \$6B \$15 \$49 \$04 \$09 \$33 \$36 \$35 \$38
1850VPW ECU6 SID\$09 INF\$04 9 M10	\$48 \$6B \$15 \$49 \$04 \$0A \$39 \$37 \$37 \$36
1850VPW ECU6 SID\$09 INF\$04 9 M11	\$48 \$6B \$15 \$49 \$04 \$0B \$00 \$00 \$00 \$00
1850VPW ECU6 SID\$09 INF\$04 9 M12	\$48 \$6B \$15 \$49 \$04 \$0C \$00 \$00 \$00 \$00
1850VPW ECU6 SID\$09 INF\$04 9 M13	\$48 \$6B \$15 \$49 \$04 \$0D \$34 \$36 \$35 \$38
1850VPW ECU6 SID\$09 INF\$04 9 M14	\$48 \$6B \$15 \$49 \$04 \$0E \$39 \$37 \$37 \$35
1850VPW ECU6 SID\$09 INF\$04 9 M15	\$48 \$6B \$15 \$49 \$04 \$0F \$00 \$00 \$00 \$00
1850VPW ECU6 SID\$09 INF\$04 9 M16	\$48 \$6B \$15 \$49 \$04 \$10 \$00 \$00 \$00 \$00
1850VPW ECU6 SID\$09 INF\$04 9 M17	\$48 \$6B \$15 \$49 \$04 \$11 \$35 \$36 \$35 \$38
1850VPW ECU6 SID\$09 INF\$04 9 M18	\$48 \$6B \$15 \$49 \$04 \$12 \$39 \$37 \$37 \$34
1850VPW ECU6 SID\$09 INF\$04 9 M19	\$48 \$6B \$15 \$49 \$04 \$13 \$00 \$00 \$00 \$00
1850VPW ECU6 SID\$09 INF\$04 9 M20	\$48 \$6B \$15 \$49 \$04 \$14 \$00 \$00 \$00 \$00
1850VPW ECU6 SID\$09 INF\$04 9 M21	\$48 \$6B \$15 \$49 \$04 \$15 \$36 \$36 \$35 \$38
1850VPW ECU6 SID\$09 INF\$04 9 M22	\$48 \$6B \$15 \$49 \$04 \$16 \$39 \$37 \$37 \$33
1850VPW ECU6 SID\$09 INF\$04 9 M23	\$48 \$6B \$15 \$49 \$04 \$17 \$00 \$00 \$00 \$00
1850VPW ECU6 SID\$09 INF\$04 9 M24	\$48 \$6B \$15 \$49 \$04 \$18 \$00 \$00 \$00 \$00
1850VPW ECU6 SID\$09 INF\$04 9 M25	\$48 \$6B \$15 \$49 \$04 \$19 \$37 \$36 \$35 \$38
1850VPW ECU6 SID\$09 INF\$04 9 M26	\$48 \$6B \$15 \$49 \$04 \$1A \$39 \$37 \$37 \$32
1850VPW ECU6 SID\$09 INF\$04 9 M27	\$48 \$6B \$15 \$49 \$04 \$1B \$00 \$00 \$00 \$00
1850VPW ECU6 SID\$09 INF\$04 9 M28	\$48 \$6B \$15 \$49 \$04 \$1C \$00 \$00 \$00 \$00
1850VPW ECU6 SID\$09 INF\$04 9 M29	\$48 \$6B \$15 \$49 \$04 \$1D \$38 \$36 \$35 \$38
1850VPW ECU6 SID\$09 INF\$04 9 M30	\$48 \$6B \$15 \$49 \$04 \$1E \$39 \$37 \$37 \$31
1850VPW ECU6 SID\$09 INF\$04 9 M31	\$48 \$6B \$15 \$49 \$04 \$1F \$00 \$00 \$00 \$00
1850VPW ECU6 SID\$09 INF\$04 9 M32	\$48 \$6B \$15 \$49 \$04 \$20 \$00 \$00 \$00 \$00
1850VPW ECU6 SID\$09 INF\$04 9 M33	\$48 \$6B \$15 \$49 \$04 \$21 \$39 \$36 \$35 \$38
1850VPW ECU6 SID\$09 INF\$04 9 M34	\$48 \$6B \$15 \$49 \$04 \$22 \$39 \$37 \$37 \$30
1850VPW ECU6 SID\$09 INF\$04 9 M35	\$48 \$6B \$15 \$49 \$04 \$23 \$00 \$00 \$00 \$00
1850VPW ECU6 SID\$09 INF\$04 9 M36	\$48 \$6B \$15 \$49 \$04 \$24 \$00 \$00 \$00 \$00

1850VPW ECU6 SID\$09 INF\$05 9	\$48 \$6B \$15 \$49 \$05 \$09
1850VPW ECU6 SID\$09 INF\$06 9 M1	\$48 \$6B \$15 \$49 \$06 \$01 \$67 \$91 \$BC \$88
1850VPW ECU6 SID\$09 INF\$06 9 M2	\$48 \$6B \$15 \$49 \$06 \$02 \$68 \$91 \$BC \$87
1850VPW ECU6 SID\$09 INF\$06 9 M3	\$48 \$6B \$15 \$49 \$06 \$03 \$69 \$91 \$BC \$86
1850VPW ECU6 SID\$09 INF\$06 9 M4	\$48 \$6B \$15 \$49 \$06 \$04 \$6A \$91 \$BC \$85
1850VPW ECU6 SID\$09 INF\$06 9 M5	\$48 \$6B \$15 \$49 \$06 \$05 \$6B \$91 \$BC \$84
1850VPW ECU6 SID\$09 INF\$06 9 M6	\$48 \$6B \$15 \$49 \$06 \$06 \$6C \$91 \$BC \$83
1850VPW ECU6 SID\$09 INF\$06 9 M7	\$48 \$6B \$15 \$49 \$06 \$07 \$6D \$91 \$BC \$82
1850VPW ECU6 SID\$09 INF\$06 9 M8	\$48 \$6B \$15 \$49 \$06 \$08 \$6E \$55 \$AA \$81
1850VPW ECU6 SID\$09 INF\$06 9 M9	\$48 \$6B \$15 \$49 \$06 \$09 \$6F \$FF \$00 \$80
1850VPW ECU7 SID\$09 INF\$00	\$48 \$6B \$16 \$49 \$00 \$3C \$00 \$00 \$00
1850VPW ECU7 SID\$09 INF\$03 9	\$48 \$6B \$16 \$49 \$03 \$24
1850VPW ECU7 SID\$09 INF\$04 9 M1	\$48 \$6B \$16 \$49 \$04 \$01 \$31 \$37 \$35 \$38
1850VPW ECU7 SID\$09 INF\$04 9 M2	\$48 \$6B \$16 \$49 \$04 \$02 \$39 \$37 \$37 \$38
1850VPW ECU7 SID\$09 INF\$04 9 M3	\$48 \$6B \$16 \$49 \$04 \$03 \$00 \$00 \$00 \$00
1850VPW ECU7 SID\$09 INF\$04 9 M4	\$48 \$6B \$16 \$49 \$04 \$04 \$00 \$00 \$00 \$00
1850VPW ECU7 SID\$09 INF\$04 9 M5	\$48 \$6B \$16 \$49 \$04 \$05 \$32 \$37 \$35 \$38
1850VPW ECU7 SID\$09 INF\$04 9 M6	\$48 \$6B \$16 \$49 \$04 \$06 \$39 \$37 \$37 \$37
1850VPW ECU7 SID\$09 INF\$04 9 M7	\$48 \$6B \$16 \$49 \$04 \$07 \$00 \$00 \$00 \$00
1850VPW ECU7 SID\$09 INF\$04 9 M8	\$48 \$6B \$16 \$49 \$04 \$08 \$00 \$00 \$00 \$00
1850VPW ECU7 SID\$09 INF\$04 9 M9	\$48 \$6B \$16 \$49 \$04 \$09 \$33 \$37 \$35 \$38
1850VPW ECU7 SID\$09 INF\$04 9 M10	\$48 \$6B \$16 \$49 \$04 \$0A \$39 \$37 \$37 \$36
1850VPW ECU7 SID\$09 INF\$04 9 M11	\$48 \$6B \$16 \$49 \$04 \$0B \$00 \$00 \$00 \$00
1850VPW ECU7 SID\$09 INF\$04 9 M12	\$48 \$6B \$16 \$49 \$04 \$0C \$00 \$00 \$00 \$00
1850VPW ECU7 SID\$09 INF\$04 9 M13	\$48 \$6B \$16 \$49 \$04 \$0D \$34 \$37 \$35 \$38
1850VPW ECU7 SID\$09 INF\$04 9 M14	\$48 \$6B \$16 \$49 \$04 \$0E \$39 \$37 \$37 \$35
1850VPW ECU7 SID\$09 INF\$04 9 M15	\$48 \$6B \$16 \$49 \$04 \$0F \$00 \$00 \$00 \$00
1850VPW ECU7 SID\$09 INF\$04 9 M16	\$48 \$6B \$16 \$49 \$04 \$10 \$00 \$00 \$00 \$00
1850VPW ECU7 SID\$09 INF\$04 9 M17	\$48 \$6B \$16 \$49 \$04 \$11 \$35 \$37 \$35 \$38
1850VPW ECU7 SID\$09 INF\$04 9 M18	\$48 \$6B \$16 \$49 \$04 \$12 \$39 \$37 \$37 \$34
1850VPW ECU7 SID\$09 INF\$04 9 M19	\$48 \$6B \$16 \$49 \$04 \$13 \$00 \$00 \$00 \$00
1850VPW ECU7 SID\$09 INF\$04 9 M20	\$48 \$6B \$16 \$49 \$04 \$14 \$00 \$00 \$00 \$00
1850VPW ECU7 SID\$09 INF\$04 9 M21	\$48 \$6B \$16 \$49 \$04 \$15 \$36 \$37 \$35 \$38
1850VPW ECU7 SID\$09 INF\$04 9 M22	\$48 \$6B \$16 \$49 \$04 \$16 \$39 \$37 \$37 \$33
1850VPW ECU7 SID\$09 INF\$04 9 M23	\$48 \$6B \$16 \$49 \$04 \$17 \$00 \$00 \$00 \$00
1850VPW ECU7 SID\$09 INF\$04 9 M24	\$48 \$6B \$16 \$49 \$04 \$18 \$00 \$00 \$00 \$00
1850VPW ECU7 SID\$09 INF\$04 9 M25	\$48 \$6B \$16 \$49 \$04 \$19 \$37 \$37 \$35 \$38
1850VPW ECU7 SID\$09 INF\$04 9 M26	\$48 \$6B \$16 \$49 \$04 \$1A \$39 \$37 \$37 \$32
1850VPW ECU7 SID\$09 INF\$04 9 M26	\$48 \$6B \$16 \$49 \$04 \$1B \$00 \$00 \$00 \$00
1850VPW ECU7 SID\$09 INF\$04 9 M28	\$48 \$6B \$16 \$49 \$04 \$1C \$00 \$00 \$00 \$00
1850VPW ECU7 SID\$09 INF\$04 9 M29	\$48 \$6B \$16 \$49 \$04 \$1D \$38 \$37 \$35 \$38
1850VPW ECU7 SID\$09 INF\$04 9 M30	\$48 \$6B \$16 \$49 \$04 \$1E \$39 \$37 \$37 \$31
1850VPW ECU7 SID\$09 INF\$04 9 M31	\$48 \$6B \$16 \$49 \$04 \$1F \$00 \$00 \$00 \$00
1850VPW ECU7 SID\$09 INF\$04 9 M32	\$48 \$6B \$16 \$49 \$04 \$20 \$00 \$00 \$00 \$00
1850VPW ECU7 SID\$09 INF\$04 9 M33	\$48 \$6B \$16 \$49 \$04 \$21 \$39 \$37 \$35 \$38
1850VPW ECU7 SID\$09 INF\$04 9 M34	\$48 \$6B \$16 \$49 \$04 \$22 \$39 \$37 \$37 \$30
1850VPW ECU7 SID\$09 INF\$04 9 M35	\$48 \$6B \$16 \$49 \$04 \$23 \$00 \$00 \$00 \$00
1850VPW ECU7 SID\$09 INF\$04 9 M36	\$48 \$6B \$16 \$49 \$04 \$24 \$00 \$00 \$00 \$00
1850VPW ECU7 SID\$09 INF\$05 9	\$48 \$6B \$16 \$49 \$05 \$09
1850VPW ECU7 SID\$09 INF\$06 9 M1	\$48 \$6B \$16 \$49 \$06 \$01 \$77 \$91 \$BC \$88
1850VPW ECU7 SID\$09 INF\$06 9 M2	\$48 \$6B \$16 \$49 \$06 \$02 \$78 \$91 \$BC \$87
1850VPW ECU7 SID\$09 INF\$06 9 M3	\$48 \$6B \$16 \$49 \$06 \$03 \$79 \$91 \$BC \$86
1850VPW ECU7 SID\$09 INF\$06 9 M4	\$48 \$6B \$16 \$49 \$06 \$04 \$7A \$91 \$BC \$85
1850VPW ECU7 SID\$09 INF\$06 9 M5	\$48 \$6B \$16 \$49 \$06 \$05 \$7B \$91 \$BC \$84
1850VPW ECU7 SID\$09 INF\$06 9 M6	\$48 \$6B \$16 \$49 \$06 \$06 \$7C \$91 \$BC \$83
1850VPW ECU7 SID\$09 INF\$06 9 M7	\$48 \$6B \$16 \$49 \$06 \$07 \$7D \$91 \$BC \$82
1850VPW ECU7 SID\$09 INF\$06 9 M8	\$48 \$6B \$16 \$49 \$06 \$08 \$7E \$55 \$AA \$81
1850VPW ECU7 SID\$09 INF\$06 9 M9	\$48 \$6B \$16 \$49 \$06 \$09 \$7F \$FF \$00 \$80

1850VPW ECU8 SID\$09 INF\$00	\$48 \$6B \$17 \$49 \$00 \$3C \$00 \$00 \$00
1850VPW ECU8 SID\$09 INF\$03 9	\$48 \$6B \$17 \$49 \$03 \$24
1850VPW ECU8 SID\$09 INF\$04 9 M1	\$48 \$6B \$17 \$49 \$04 \$01 \$31 \$38 \$35 \$38
1850VPW ECU8 SID\$09 INF\$04 9 M2	\$48 \$6B \$17 \$49 \$04 \$02 \$39 \$37 \$37 \$38
1850VPW ECU8 SID\$09 INF\$04 9 M3	\$48 \$6B \$17 \$49 \$04 \$03 \$00 \$00 \$00 \$00
1850VPW ECU8 SID\$09 INF\$04 9 M4	\$48 \$6B \$17 \$49 \$04 \$04 \$00 \$00 \$00 \$00
1850VPW ECU8 SID\$09 INF\$04 9 M5	\$48 \$6B \$17 \$49 \$04 \$05 \$32 \$38 \$35 \$38
1850VPW ECU8 SID\$09 INF\$04 9 M6	\$48 \$6B \$17 \$49 \$04 \$06 \$39 \$37 \$37 \$37
1850VPW ECU8 SID\$09 INF\$04 9 M7	\$48 \$6B \$17 \$49 \$04 \$07 \$00 \$00 \$00 \$00
1850VPW ECU8 SID\$09 INF\$04 9 M8	\$48 \$6B \$17 \$49 \$04 \$08 \$00 \$00 \$00 \$00
1850VPW ECU8 SID\$09 INF\$04 9 M9	\$48 \$6B \$17 \$49 \$04 \$09 \$33 \$38 \$35 \$38
1850VPW ECU8 SID\$09 INF\$04 9 M10	\$48 \$6B \$17 \$49 \$04 \$0A \$39 \$37 \$37 \$36
1850VPW ECU8 SID\$09 INF\$04 9 M11	\$48 \$6B \$17 \$49 \$04 \$0B \$00 \$00 \$00 \$00
1850VPW ECU8 SID\$09 INF\$04 9 M12	\$48 \$6B \$17 \$49 \$04 \$0C \$00 \$00 \$00 \$00
1850VPW ECU8 SID\$09 INF\$04 9 M13	\$48 \$6B \$17 \$49 \$04 \$0D \$34 \$38 \$35 \$38
1850VPW ECU8 SID\$09 INF\$04 9 M14	\$48 \$6B \$17 \$49 \$04 \$0E \$39 \$37 \$37 \$35
1850VPW ECU8 SID\$09 INF\$04 9 M15	\$48 \$6B \$17 \$49 \$04 \$0F \$00 \$00 \$00 \$00
1850VPW ECU8 SID\$09 INF\$04 9 M16	\$48 \$6B \$17 \$49 \$04 \$10 \$00 \$00 \$00 \$00
1850VPW ECU8 SID\$09 INF\$04 9 M17	\$48 \$6B \$17 \$49 \$04 \$11 \$35 \$38 \$35 \$38
1850VPW ECU8 SID\$09 INF\$04 9 M18	\$48 \$6B \$17 \$49 \$04 \$12 \$39 \$37 \$37 \$34
1850VPW ECU8 SID\$09 INF\$04 9 M19	\$48 \$6B \$17 \$49 \$04 \$13 \$00 \$00 \$00 \$00
1850VPW ECU8 SID\$09 INF\$04 9 M20	\$48 \$6B \$17 \$49 \$04 \$14 \$00 \$00 \$00 \$00
1850VPW ECU8 SID\$09 INF\$04 9 M21	\$48 \$6B \$17 \$49 \$04 \$15 \$36 \$38 \$35 \$38
1850VPW ECU8 SID\$09 INF\$04 9 M22	\$48 \$6B \$17 \$49 \$04 \$16 \$39 \$37 \$37 \$33
1850VPW ECU8 SID\$09 INF\$04 9 M23	\$48 \$6B \$17 \$49 \$04 \$17 \$00 \$00 \$00 \$00
1850VPW ECU8 SID\$09 INF\$04 9 M24	\$48 \$6B \$17 \$49 \$04 \$17 \$00 \$00 \$00 \$00
1850VPW ECU8 SID\$09 INF\$04 9 M25	\$48 \$6B \$17 \$49 \$04 \$19 \$37 \$38 \$35 \$38
1850VPW ECU8 SID\$09 INF\$04 9 M26	\$48 \$6B \$17 \$49 \$04 \$1A \$39 \$37 \$37 \$32
1850VPW ECU8 SID\$09 INF\$04 9 M27	\$48 \$6B \$17 \$49 \$04 \$1B \$00 \$00 \$00 \$00
1850VPW ECU8 SID\$09 INF\$04 9 M28	\$48 \$6B \$17 \$49 \$04 \$1C \$00 \$00 \$00 \$00
1850VPW ECU8 SID\$09 INF\$04 9 M29	\$48 \$6B \$17 \$49 \$04 \$1D \$38 \$38 \$35 \$38
1850VPW ECU8 SID\$09 INF\$04 9 M30	\$48 \$6B \$17 \$49 \$04 \$1E \$39 \$37 \$37 \$31
1850VPW ECU8 SID\$09 INF\$04 9 M31	\$48 \$6B \$17 \$49 \$04 \$1F \$00 \$00 \$00 \$00
1850VPW ECU8 SID\$09 INF\$04 9 M32	\$48 \$6B \$17 \$49 \$04 \$20 \$00 \$00 \$00 \$00
1850VPW ECU8 SID\$09 INF\$04 9 M33	\$48 \$6B \$17 \$49 \$04 \$21 \$39 \$38 \$35 \$38
1850VPW ECU8 SID\$09 INF\$04 9 M34	\$48 \$6B \$17 \$49 \$04 \$22 \$39 \$37 \$37 \$30
1850VPW ECU8 SID\$09 INF\$04 9 M35	\$48 \$6B \$17 \$49 \$04 \$23 \$00 \$00 \$00 \$00
1850VPW ECU8 SID\$09 INF\$04 9 M36	\$48 \$6B \$17 \$49 \$04 \$24 \$00 \$00 \$00 \$00
1850VPW ECU8 SID\$09 INF\$05 9	\$48 \$6B \$17 \$49 \$05 \$09
1850VPW ECU8 SID\$09 INF\$06 9 M1	\$48 \$6B \$17 \$49 \$06 \$01 \$87 \$91 \$BC \$88
1850VPW ECU8 SID\$09 INF\$06 9 M2	\$48 \$6B \$17 \$49 \$06 \$02 \$88 \$91 \$BC \$87
1850VPW ECU8 SID\$09 INF\$06 9 M3	\$48 \$6B \$17 \$49 \$06 \$03 \$89 \$91 \$BC \$86
1850VPW ECU8 SID\$09 INF\$06 9 M4	\$48 \$6B \$17 \$49 \$06 \$04 \$8A \$91 \$BC \$85
1850VPW ECU8 SID\$09 INF\$06 9 M5	\$48 \$6B \$17 \$49 \$06 \$05 \$8B \$91 \$BC \$84
1850VPW ECU8 SID\$09 INF\$06 9 M6	\$48 \$6B \$17 \$49 \$06 \$06 \$8C \$91 \$BC \$83
1850VPW ECU8 SID\$09 INF\$06 9 M7	\$48 \$6B \$17 \$49 \$06 \$07 \$8D \$91 \$BC \$82
1850VPW ECU8 SID\$09 INF\$06 9 M8	\$48 \$6B \$17 \$49 \$06 \$08 \$8E \$55 \$AA \$81
1850VPW ECU8 SID\$09 INF\$06 9 M9	\$48 \$6B \$17 \$49 \$06 \$09 \$8F \$FF \$00 \$80

**Figure 20 - Pre-defined messages for SAE J1850 VPW**

#### 5.4.6.4 SAE J1850 VPW Message Timing

For convenience, the test cases in this document will use the text in the 'Parameter Reference' column when referring to the specific value, detailed in the 'Value' column of the table below. Unless otherwise specified, the following timing values shall be used:

Parameter Reference	Value
P2_MIN	0 ms
P2	25 ms
P2_MAX	100 ms

**Figure 21 - Pre-defined message timing for SAE J1850 VPW**

#### 5.4.6.5 SAE J1850 VPW Connected State

The device shall initially be in the 'Power On' state, connected to the SAE J1850 network with the appropriate termination applied (as depicted in 5.4.6.1).

#### 5.4.6.6 SAE J1850 VPW (2 ECUs) Initialized State

The OBD network shall have determined to be SAE J1850 VPW at a baud rate of 10400 with two OBD II ECUs having been identified. The DUT may produce a periodic message transmission to keep the network alive. A successful completion of 8.5.1 shall define this state.

#### 5.4.6.7 SAE J1850 VPW (8 ECUs) Initialized State

The OBD network shall have determined to be SAE J1850 VPW at a baud rate of 10400 with eight OBD II ECUs having been identified. The DUT may produce a periodic message transmission to keep the network alive. A successful completion of 8.5.2 shall define this state.

#### 5.4.6.8 SAE J1850 VPW (2 ECUs) SID9 Ready State

The SID \$09 INFOTYPES shall be known for each of the two simulated OBD II ECUs on the network. The DUT may produce a periodic message transmission to keep the network alive. A successful completion of 9.5.17 shall define this state.

#### 5.4.6.9 SAE J1850 VPW (2 ECUs) SID9 Diesel Ready State

The SID \$09 INFOTYPES shall be known for each of the two simulated OBD II ECUs on the network. The DUT may produce a periodic message transmission to keep the network alive. A successful completion of 0 shall define this state.

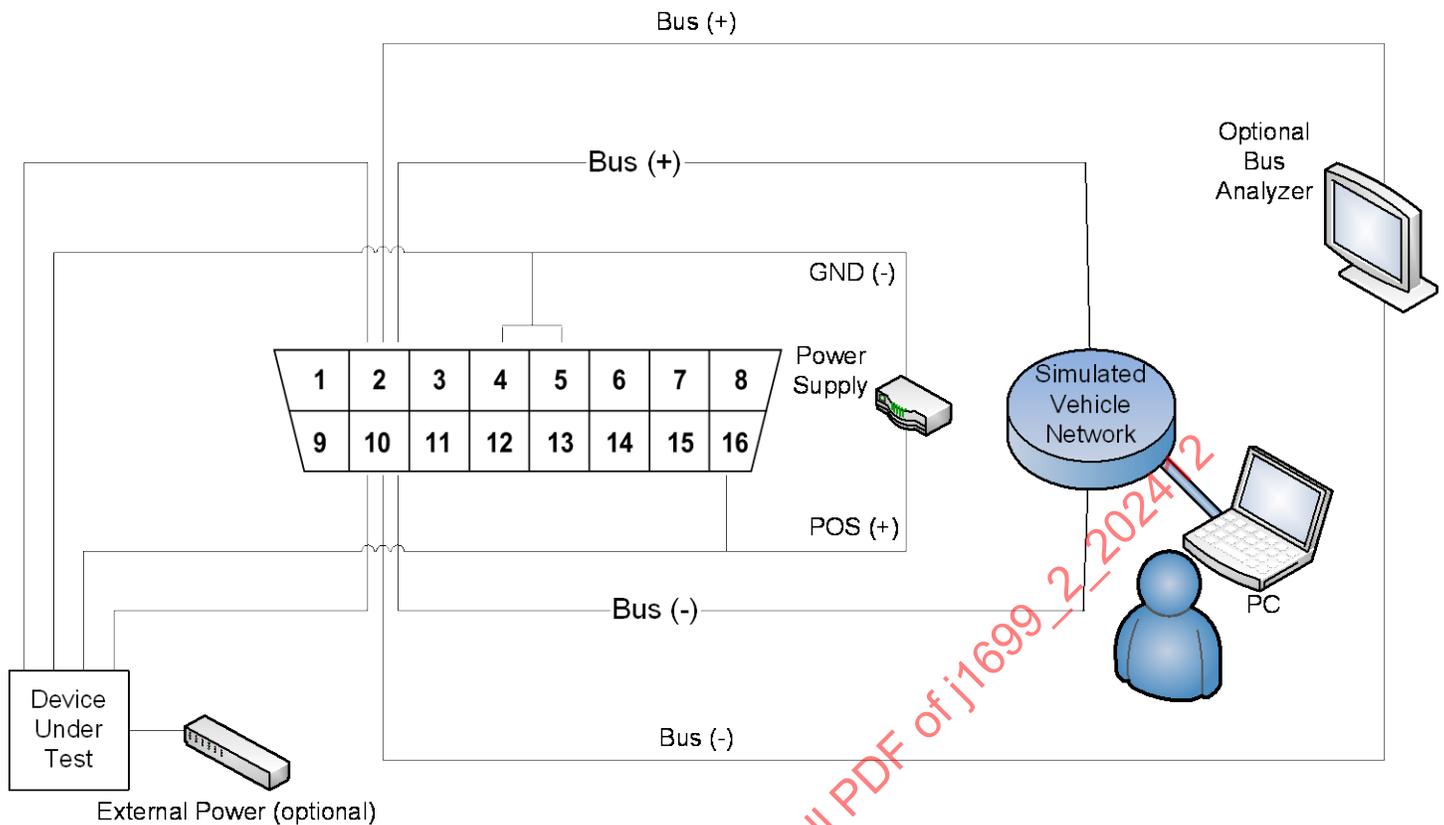
#### 5.4.6.10 SAE J1850 VPW (8 ECUs) SID9 Ready State

The SID \$09 INFOTYPES shall be known for each of the eight simulated OBD II ECUs on the network. The DUT may produce a periodic message transmission to keep the network alive. A successful completion of 9.5.19 shall define this state.

#### 5.4.7 SAE J1850 PWM

##### 5.4.7.1 SAE J1850 PWM Electrical Connections

Figure 22 outlines the typical set-up for SAE J1850 PWM testing.



**Figure 22 - Test set-up for SAE J1850 PWM testing**

#### 5.4.7.2 SAE J1850 PWM ECU Addresses

The following address bytes have been pre-defined for SAE J1850 PWM. For convenience, the test cases in this document will use the text in the 'ECU Reference' column when referring to the specific set of address bytes, detailed in the 'Address Bytes' column of the table below.

ECU Reference	Address Bytes
ECU #1	\$10
ECU #2	\$11
ECU #3	\$12
ECU #4	\$13
ECU #5	\$14
ECU #6	\$15
ECU #7	\$16
ECU #8	\$17

**Figure 23 - ECU reference and associated addresses for SAE J1850 PWM**

## 5.4.7.3 SAE J1850 PWM Messages

The following messages have been pre-defined for SAE J1850 PWM. For convenience, the test cases in this document will use the text in the 'Message Reference' column when referring to the specific set of message bytes, detailed in the 'Message Bytes' column of the table below.

Message Reference	Message Bytes
1850PWM_SID\$01_PID\$00	\$61 \$6A \$F1 \$01 \$00
1850PWM_ECU1_SID\$01_PID\$00	\$41 \$6B \$10 \$41 \$00 \$80 \$10 \$00 \$00
1850PWM_ECU2_SID\$01_PID\$00	\$41 \$6B \$11 \$41 \$00 \$80 \$10 \$00 \$00
1850PWM_ECU2_SID\$01_PID\$00	\$41 \$6B \$11 \$41 \$00 \$80 \$10 \$00 \$00
1850PWM_ECU3_SID\$01_PID\$00	\$41 \$6B \$12 \$41 \$00 \$00 \$00 \$00 \$00
1850PWM_ECU4_SID\$01_PID\$00	\$41 \$6B \$13 \$41 \$00 \$00 \$00 \$00 \$00
1850PWM_ECU5_SID\$01_PID\$00	\$41 \$6B \$14 \$41 \$00 \$00 \$00 \$00 \$00
1850PWM_ECU6_SID\$01_PID\$00	\$41 \$6B \$15 \$41 \$00 \$00 \$00 \$00 \$00
1850PWM_ECU7_SID\$01_PID\$00	\$41 \$6B \$16 \$41 \$00 \$00 \$00 \$00 \$00
1850PWM_ECU8_SID\$01_PID\$00	\$41 \$6B \$17 \$41 \$00 \$00 \$00 \$00 \$00
1850PWM_SID\$01_PID\$01	\$61 \$6A \$F1 \$01 \$01
1850PWM_SID\$01_PID\$0C	\$61 \$6A \$F1 \$01 \$0C
1850PWM_SID\$01_PID\$20	\$61 \$6A \$F1 \$01 \$20
1850PWM_SID\$01_PID\$40	\$61 \$6A \$F1 \$01 \$40
1850PWM_SID\$01_PID\$41	\$61 \$6A \$F1 \$01 \$41
1850PWM_ECU1_SID\$01_PID\$00_20	\$41 \$6B \$10 \$41 \$00 \$80 \$10 \$00 \$01
1850PWM_ECU1_SID\$01_PID\$01_SPARK	\$41 \$6B \$10 \$41 \$01 \$00 \$07 \$EF \$EF
1850PWM_ECU1_SID\$01_PID\$01_COMP	\$41 \$6B \$10 \$41 \$01 \$00 \$0F \$EB \$EB
1850PWM_ECU1_SID\$01_PID\$0C_KOEO	\$41 \$6B \$10 \$41 \$0C \$00 \$00
1850PWM_ECU1_SID\$01_PID\$20	\$41 \$6B \$10 \$41 \$20 \$00 \$00 \$00 \$01
1850PWM_ECU1_SID\$01_PID\$40	\$41 \$6B \$10 \$41 \$40 \$10 \$00 \$00 \$00
1850PWM_ECU1_SID\$01_PID\$41_SPARK	\$41 \$6B \$10 \$41 \$41 \$00 \$07 \$EF \$EF
1850PWM_ECU2_SID\$01_PID\$01_SPARK	\$41 \$6B \$11 \$41 \$01 \$81 \$44 \$00 \$00
1850PWM_ECU2_SID\$01_PID\$01_COMP	\$41 \$6B \$11 \$41 \$01 \$81 \$4C \$00 \$00
1850PWM_ECU2_SID\$01_PID\$0C_KOEO	\$41 \$6B \$11 \$41 \$0C \$00 \$00
1850PWM_SID\$02_PID\$02	\$61 \$6A \$F1 \$02 \$02 \$00
1850PWM_ECU1_SID\$02_PID\$02_NoDTC	\$41 \$6B \$10 \$42 \$02 \$00 \$00 \$00
1850PWM_ECU2_SID\$02_PID\$02_NoDTC	\$41 \$6B \$11 \$42 \$02 \$00 \$00 \$00
1850PWM_ECU1_SID\$02_PID\$02_DTC	\$41 \$6B \$10 \$42 \$02 \$00 \$01 \$07
1850PWM_SID\$03	\$61 \$6A \$F1 \$03
1850PWM_ECU1_SID\$03_NoDTC	\$41 \$6B \$10 \$43 \$00
1850PWM_ECU1_SID\$03_DTC_M1	\$41 \$6B \$10 \$43 \$01 \$07 \$01 \$10 \$01 \$15
1850PWM_ECU1_SID\$03_DTC_M2	\$41 \$6B \$10 \$43 \$01 \$24 \$00 \$00 \$00 \$00
1850PWM_ECU2_SID\$03_NoDTC	\$41 \$6B \$11 \$43 \$00
1850PWM_SID\$04	\$61 \$6A \$F1 \$04
1850PWM_ECU1_SID\$04_DONE	\$41 \$6B \$10 \$44
1850PWM_ECU1_SID\$04_NRC22	\$41 \$6B \$10 \$7F \$44 \$22
1850PWM_ECU1_SID\$04_NRC78	\$41 \$6B \$10 \$7F \$44 \$78
1850PWM_ECU2_SID\$04_DONE	\$41 \$6B \$11 \$44
1850PWM_ECU2_SID\$04_NRC22	\$41 \$6B \$11 \$7F \$44 \$22
1850PWM_SID\$05_MID\$01	\$61 \$6A \$F1 \$05 \$01 \$02
1850PWM_ECU1_SID\$05_MID\$01	\$41 \$6B \$10 \$45 \$01 \$02 \$5A
1850PWM_SID\$06_MID\$00	\$61 \$6A \$F1 \$06 \$00
1850PWM_ECU1_SID\$06_MID\$00	\$41 \$6B \$10 \$46 \$00 \$CC \$00 \$00 \$00
1850PWM_SID\$07	\$61 \$6A \$F1 \$07
1850PWM_ECU1_SID\$07_NoDTC	\$41 \$6B \$10 \$47 \$00
1850PWM_ECU1_SID\$07_DTC_M1	\$41 \$6B \$10 \$47 \$01 \$07 \$01 \$10 \$01 \$15
1850PWM_ECU1_SID\$07_DTC_M2	\$41 \$6B \$10 \$47 \$01 \$24 \$00 \$00 \$00 \$00
1850PWM_ECU2_SID\$07_NoDTC	\$41 \$6B \$11 \$47 \$00
1850PWM_SID\$08_TID\$00	\$61 \$6A \$F1 \$08
1850PWM_ECU1_SID\$08_TID\$00	\$41 \$6B \$10 \$48 \$00 \$80 \$00 \$00 \$00

1850PWM SID\$09 INF\$00	\$61 \$6A \$F1 \$09 \$00
1850PWM SID\$09 INF\$01	\$61 \$6A \$F1 \$09 \$01
1850PWM SID\$09 INF\$02	\$61 \$6A \$F1 \$09 \$02
1850PWM SID\$09 INF\$03	\$61 \$6A \$F1 \$09 \$03
1850PWM SID\$09 INF\$04	\$61 \$6A \$F1 \$09 \$04
1850PWM SID\$09 INF\$05	\$61 \$6A \$F1 \$09 \$05
1850PWM SID\$09 INF\$06	\$61 \$6A \$F1 \$09 \$06
1850PWM SID\$09 INF\$07	\$61 \$6A \$F1 \$09 \$07
1850PWM SID\$09 INF\$08	\$61 \$6A \$F1 \$09 \$08
1850PWM SID\$09 INF\$0B	\$61 \$6A \$F1 \$09 \$0B
1850PWM ECU1 SID\$09 INF\$00	\$41 \$6B \$10 \$49 \$00 \$FF \$00 \$00 \$00
1850PWM ECU1 SID\$09 INF\$01	\$41 \$6B \$10 \$49 \$01 \$05
1850PWM ECU1 SID\$09 INF\$02 M1	\$41 \$6B \$10 \$49 \$02 \$01 \$00 \$00 \$00 \$31
1850PWM ECU1 SID\$09 INF\$02 M2	\$41 \$6B \$10 \$49 \$02 \$02 \$47 \$59 \$44 \$45
1850PWM ECU1 SID\$09 INF\$02 M3	\$41 \$6B \$10 \$49 \$02 \$03 \$36 \$33 \$41 \$37
1850PWM ECU1 SID\$09 INF\$02 M4	\$41 \$6B \$10 \$49 \$02 \$04 \$41 \$30 \$30 \$30
1850PWM ECU1 SID\$09 INF\$02 M5	\$41 \$6B \$10 \$49 \$02 \$05 \$30 \$30 \$30 \$30
1850PWM ECU1 SID\$09 INF\$03	\$41 \$6B \$10 \$49 \$03 \$04
1850PWM ECU1 SID\$09 INF\$03 9	\$41 \$6B \$10 \$49 \$03 \$24
1850PWM ECU1 SID\$09 INF\$04 M1	\$41 \$6B \$10 \$49 \$04 \$01 \$31 \$32 \$35 \$38
1850PWM ECU1 SID\$09 INF\$04 M2	\$41 \$6B \$10 \$49 \$04 \$02 \$39 \$37 \$37 \$32
1850PWM ECU1 SID\$09 INF\$04 M3	\$41 \$6B \$10 \$49 \$04 \$03 \$00 \$00 \$00 \$00
1850PWM ECU1 SID\$09 INF\$04 M4	\$41 \$6B \$10 \$49 \$04 \$04 \$00 \$00 \$00 \$00
1850PWM ECU1 SID\$09 INF\$04 9 M1	\$41 \$6B \$10 \$49 \$04 \$01 \$31 \$31 \$35 \$38
1850PWM ECU1 SID\$09 INF\$04 9 M2	\$41 \$6B \$10 \$49 \$04 \$02 \$39 \$37 \$37 \$38
1850PWM ECU1 SID\$09 INF\$04 9 M3	\$41 \$6B \$10 \$49 \$04 \$03 \$00 \$00 \$00 \$00
1850PWM ECU1 SID\$09 INF\$04 9 M4	\$41 \$6B \$10 \$49 \$04 \$04 \$00 \$00 \$00 \$00
1850PWM ECU1 SID\$09 INF\$04 9 M5	\$41 \$6B \$10 \$49 \$04 \$05 \$32 \$31 \$35 \$38
1850PWM ECU1 SID\$09 INF\$04 9 M6	\$41 \$6B \$10 \$49 \$04 \$06 \$39 \$37 \$37 \$37
1850PWM ECU1 SID\$09 INF\$04 9 M7	\$41 \$6B \$10 \$49 \$04 \$07 \$00 \$00 \$00 \$00
1850PWM ECU1 SID\$09 INF\$04 9 M8	\$41 \$6B \$10 \$49 \$04 \$08 \$00 \$00 \$00 \$00
1850PWM ECU1 SID\$09 INF\$04 9 M9	\$41 \$6B \$10 \$49 \$04 \$09 \$33 \$31 \$35 \$38
1850PWM ECU1 SID\$09 INF\$04 9 M10	\$41 \$6B \$10 \$49 \$04 \$0A \$39 \$37 \$37 \$36
1850PWM ECU1 SID\$09 INF\$04 9 M11	\$41 \$6B \$10 \$49 \$04 \$0B \$00 \$00 \$00 \$00
1850PWM ECU1 SID\$09 INF\$04 9 M12	\$41 \$6B \$10 \$49 \$04 \$0C \$00 \$00 \$00 \$00
1850PWM ECU1 SID\$09 INF\$04 9 M13	\$41 \$6B \$10 \$49 \$04 \$0D \$34 \$31 \$35 \$38
1850PWM ECU1 SID\$09 INF\$04 9 M14	\$41 \$6B \$10 \$49 \$04 \$0E \$39 \$37 \$37 \$35
1850PWM ECU1 SID\$09 INF\$04 9 M15	\$41 \$6B \$10 \$49 \$04 \$0F \$00 \$00 \$00 \$00
1850PWM ECU1 SID\$09 INF\$04 9 M16	\$41 \$6B \$10 \$49 \$04 \$10 \$00 \$00 \$00 \$00
1850PWM ECU1 SID\$09 INF\$04 9 M17	\$41 \$6B \$10 \$49 \$04 \$11 \$35 \$31 \$35 \$38
1850PWM ECU1 SID\$09 INF\$04 9 M18	\$41 \$6B \$10 \$49 \$04 \$12 \$39 \$37 \$37 \$34
1850PWM ECU1 SID\$09 INF\$04 9 M19	\$41 \$6B \$10 \$49 \$04 \$13 \$00 \$00 \$00 \$00
1850PWM ECU1 SID\$09 INF\$04 9 M20	\$41 \$6B \$10 \$49 \$04 \$14 \$00 \$00 \$00 \$00
1850PWM ECU1 SID\$09 INF\$04 9 M21	\$41 \$6B \$10 \$49 \$04 \$15 \$36 \$31 \$35 \$38
1850PWM ECU1 SID\$09 INF\$04 9 M22	\$41 \$6B \$10 \$49 \$04 \$16 \$39 \$37 \$37 \$33
1850PWM ECU1 SID\$09 INF\$04 9 M23	\$41 \$6B \$10 \$49 \$04 \$17 \$00 \$00 \$00 \$00
1850PWM ECU1 SID\$09 INF\$04 9 M24	\$41 \$6B \$10 \$49 \$04 \$18 \$00 \$00 \$00 \$00
1850PWM ECU1 SID\$09 INF\$04 9 M25	\$41 \$6B \$10 \$49 \$04 \$19 \$37 \$31 \$35 \$38
1850PWM ECU1 SID\$09 INF\$04 9 M26	\$41 \$6B \$10 \$49 \$04 \$1A \$39 \$37 \$37 \$32
1850PWM ECU1 SID\$09 INF\$04 9 M27	\$41 \$6B \$10 \$49 \$04 \$1B \$00 \$00 \$00 \$00
1850PWM ECU1 SID\$09 INF\$04 9 M28	\$41 \$6B \$10 \$49 \$04 \$1C \$00 \$00 \$00 \$00
1850PWM ECU1 SID\$09 INF\$04 9 M29	\$41 \$6B \$10 \$49 \$04 \$1D \$38 \$31 \$35 \$38
1850PWM ECU1 SID\$09 INF\$04 9 M30	\$41 \$6B \$10 \$49 \$04 \$1E \$39 \$37 \$37 \$31
1850PWM ECU1 SID\$09 INF\$04 9 M31	\$41 \$6B \$10 \$49 \$04 \$1F \$00 \$00 \$00 \$00
1850PWM ECU1 SID\$09 INF\$04 9 M32	\$41 \$6B \$10 \$49 \$04 \$20 \$00 \$00 \$00 \$00
1850PWM ECU1 SID\$09 INF\$04 9 M33	\$41 \$6B \$10 \$49 \$04 \$21 \$39 \$31 \$35 \$38
1850PWM ECU1 SID\$09 INF\$04 9 M34	\$41 \$6B \$10 \$49 \$04 \$22 \$39 \$37 \$37 \$30
1850PWM ECU1 SID\$09 INF\$04 9 M35	\$41 \$6B \$10 \$49 \$04 \$23 \$00 \$00 \$00 \$00

1850PWM ECU1 SID\$09 INF\$04 9 M36	\$41 \$6B \$10 \$49 \$04 \$24 \$00 \$00 \$00 \$00
1850PWM ECU1 SID\$09 INF\$05	\$41 \$6B \$10 \$49 \$05 \$01
1850PWM ECU1 SID\$09 INF\$05 9	\$41 \$6B \$10 \$49 \$05 \$09
1850PWM ECU1 SID\$09 INF\$06	\$41 \$6B \$10 \$49 \$06 \$01 \$17 \$91 \$BC \$88
1850PWM ECU1 SID\$09 INF\$06 9 M1	\$41 \$6B \$10 \$49 \$06 \$01 \$17 \$91 \$BC \$88
1850PWM ECU1 SID\$09 INF\$06 9 M2	\$41 \$6B \$10 \$49 \$06 \$02 \$18 \$91 \$BC \$87
1850PWM ECU1 SID\$09 INF\$06 9 M3	\$41 \$6B \$10 \$49 \$06 \$03 \$19 \$91 \$BC \$86
1850PWM ECU1 SID\$09 INF\$06 9 M4	\$41 \$6B \$10 \$49 \$06 \$04 \$1A \$91 \$BC \$85
1850PWM ECU1 SID\$09 INF\$06 9 M5	\$41 \$6B \$10 \$49 \$06 \$05 \$1B \$91 \$BC \$84
1850PWM ECU1 SID\$09 INF\$06 9 M6	\$41 \$6B \$10 \$49 \$06 \$06 \$1C \$91 \$BC \$83
1850PWM ECU1 SID\$09 INF\$06 9 M7	\$41 \$6B \$10 \$49 \$06 \$07 \$1D \$91 \$BC \$82
1850PWM ECU1 SID\$09 INF\$06 9 M8	\$41 \$6B \$10 \$49 \$06 \$08 \$1E \$55 \$AA \$81
1850PWM ECU1 SID\$09 INF\$06 9 M9	\$41 \$6B \$10 \$49 \$06 \$09 \$1F \$FF \$00 \$80
1850PWM ECU1 SID\$09 INF\$07 832	\$41 \$6B \$10 \$49 \$07 \$08
1850PWM ECU1 SID\$09 INF\$07 840	\$41 \$6B \$10 \$49 \$07 \$0A
1850PWM ECU1 SID\$09 INF\$08 32 M1	\$41 \$6B \$10 \$49 \$08 \$01 \$00 \$64 \$00 \$69
1850PWM ECU1 SID\$09 INF\$08 32 M2	\$41 \$6B \$10 \$49 \$08 \$02 \$00 \$0A \$00 \$0F
1850PWM ECU1 SID\$09 INF\$08 32 M3	\$41 \$6B \$10 \$49 \$08 \$03 \$00 \$00 \$00 \$00
1850PWM ECU1 SID\$09 INF\$08 32 M4	\$41 \$6B \$10 \$49 \$08 \$04 \$00 \$1E \$00 \$23
1850PWM ECU1 SID\$09 INF\$08 32 M5	\$41 \$6B \$10 \$49 \$08 \$05 \$00 \$00 \$00 \$00
1850PWM ECU1 SID\$09 INF\$08 32 M6	\$41 \$6B \$10 \$49 \$08 \$06 \$00 \$32 \$00 \$37
1850PWM ECU1 SID\$09 INF\$08 32 M7	\$41 \$6B \$10 \$49 \$08 \$07 \$00 \$3C \$00 \$41
1850PWM ECU1 SID\$09 INF\$08 32 M8	\$41 \$6B \$10 \$49 \$08 \$08 \$00 \$46 \$00 \$4B
1850PWM ECU1 SID\$09 INF\$08 40 M1	\$41 \$6B \$10 \$49 \$08 \$01 \$00 \$64 \$00 \$69
1850PWM ECU1 SID\$09 INF\$08 40 M2	\$41 \$6B \$10 \$49 \$08 \$02 \$00 \$0A \$00 \$0F
1850PWM ECU1 SID\$09 INF\$08 40 M3	\$41 \$6B \$10 \$49 \$08 \$03 \$00 \$00 \$00 \$00
1850PWM ECU1 SID\$09 INF\$08 40 M4	\$41 \$6B \$10 \$49 \$08 \$04 \$00 \$1E \$00 \$23
1850PWM ECU1 SID\$09 INF\$08 40 M5	\$41 \$6B \$10 \$49 \$08 \$05 \$00 \$00 \$00 \$00
1850PWM ECU1 SID\$09 INF\$08 40 M6	\$41 \$6B \$10 \$49 \$08 \$06 \$00 \$32 \$00 \$37
1850PWM ECU1 SID\$09 INF\$08 40 M7	\$41 \$6B \$10 \$49 \$08 \$07 \$00 \$3C \$00 \$41
1850PWM ECU1 SID\$09 INF\$08 40 M8	\$41 \$6B \$10 \$49 \$08 \$08 \$00 \$46 \$00 \$4B
1850PWM ECU1 SID\$09 INF\$08 40 M9	\$41 \$6B \$10 \$49 \$08 \$09 \$00 \$5A \$00 \$5F
1850PWM ECU1 SID\$09 INF\$08 40 M10	\$41 \$6B \$10 \$49 \$08 \$0A \$00 \$00 \$00 \$00
1850PWM ECU1 SID\$09 NRC78	\$41 \$6B \$10 \$7F \$49 \$78
1850PWM ECU2 SID\$09 INF\$00	\$41 \$6B \$11 \$49 \$00 \$3F \$00 \$00 \$00
1850PWM ECU2 SID\$09 INF\$03	\$41 \$6B \$11 \$49 \$03 \$04
1850PWM ECU2 SID\$09 INF\$03 9	\$41 \$6B \$11 \$49 \$03 \$24
1850PWM ECU2 SID\$09 INF\$04 M1	\$41 \$6B \$11 \$49 \$04 \$01 \$32 \$34 \$32 \$32
1850PWM ECU2 SID\$09 INF\$04 M2	\$41 \$6B \$11 \$49 \$04 \$02 \$31 \$34 \$31 \$39
1850PWM ECU2 SID\$09 INF\$04 M3	\$41 \$6B \$11 \$49 \$04 \$03 \$00 \$00 \$00 \$00
1850PWM ECU2 SID\$09 INF\$04 M4	\$41 \$6B \$11 \$49 \$04 \$04 \$00 \$00 \$00 \$00
1850PWM ECU2 SID\$09 INF\$04 9 M1	\$41 \$6B \$11 \$49 \$04 \$01 \$31 \$32 \$35 \$38
1850PWM ECU2 SID\$09 INF\$04 9 M2	\$41 \$6B \$11 \$49 \$04 \$02 \$39 \$37 \$37 \$38
1850PWM ECU2 SID\$09 INF\$04 9 M3	\$41 \$6B \$11 \$49 \$04 \$03 \$00 \$00 \$00 \$00
1850PWM ECU2 SID\$09 INF\$04 9 M4	\$41 \$6B \$11 \$49 \$04 \$04 \$00 \$00 \$00 \$00
1850PWM ECU2 SID\$09 INF\$04 9 M5	\$41 \$6B \$11 \$49 \$04 \$05 \$32 \$32 \$35 \$38
1850PWM ECU2 SID\$09 INF\$04 9 M6	\$41 \$6B \$11 \$49 \$04 \$06 \$39 \$37 \$37 \$37
1850PWM ECU2 SID\$09 INF\$04 9 M7	\$41 \$6B \$11 \$49 \$04 \$07 \$00 \$00 \$00 \$00
1850PWM ECU2 SID\$09 INF\$04 9 M8	\$41 \$6B \$11 \$49 \$04 \$08 \$00 \$00 \$00 \$00
1850PWM ECU2 SID\$09 INF\$04 9 M9	\$41 \$6B \$11 \$49 \$04 \$09 \$33 \$32 \$35 \$38
1850PWM ECU2 SID\$09 INF\$04 9 M10	\$41 \$6B \$11 \$49 \$04 \$0A \$39 \$37 \$37 \$36
1850PWM ECU2 SID\$09 INF\$04 9 M11	\$41 \$6B \$11 \$49 \$04 \$0B \$00 \$00 \$00 \$00
1850PWM ECU2 SID\$09 INF\$04 9 M12	\$41 \$6B \$11 \$49 \$04 \$0C \$00 \$00 \$00 \$00
1850PWM ECU2 SID\$09 INF\$04 9 M13	\$41 \$6B \$11 \$49 \$04 \$0D \$34 \$32 \$35 \$38
1850PWM ECU2 SID\$09 INF\$04 9 M14	\$41 \$6B \$11 \$49 \$04 \$0E \$39 \$37 \$37 \$35
1850PWM ECU2 SID\$09 INF\$04 9 M15	\$41 \$6B \$11 \$49 \$04 \$0F \$00 \$00 \$00 \$00
1850PWM ECU2 SID\$09 INF\$04 9 M16	\$41 \$6B \$11 \$49 \$04 \$10 \$00 \$00 \$00 \$00
1850PWM ECU2 SID\$09 INF\$04 9 M17	\$41 \$6B \$11 \$49 \$04 \$11 \$35 \$32 \$35 \$38

1850PWM ECU2 SID\$09 INF\$04 9 M18	\$41 \$6B \$11 \$49 \$04 \$12 \$39 \$37 \$37 \$34
1850PWM ECU2 SID\$09 INF\$04 9 M19	\$41 \$6B \$11 \$49 \$04 \$13 \$00 \$00 \$00 \$00
1850PWM ECU2 SID\$09 INF\$04 9 M20	\$41 \$6B \$11 \$49 \$04 \$14 \$00 \$00 \$00 \$00
1850PWM ECU2 SID\$09 INF\$04 9 M21	\$41 \$6B \$11 \$49 \$04 \$15 \$36 \$32 \$35 \$38
1850PWM ECU2 SID\$09 INF\$04 9 M22	\$41 \$6B \$11 \$49 \$04 \$16 \$39 \$37 \$37 \$33
1850PWM ECU2 SID\$09 INF\$04 9 M23	\$41 \$6B \$11 \$49 \$04 \$17 \$00 \$00 \$00 \$00
1850PWM ECU2 SID\$09 INF\$04 9 M24	\$41 \$6B \$11 \$49 \$04 \$18 \$00 \$00 \$00 \$00
1850PWM ECU2 SID\$09 INF\$04 9 M25	\$41 \$6B \$11 \$49 \$04 \$19 \$37 \$32 \$35 \$38
1850PWM ECU2 SID\$09 INF\$04 9 M26	\$41 \$6B \$11 \$49 \$04 \$1A \$39 \$37 \$37 \$32
1850PWM ECU2 SID\$09 INF\$04 9 M27	\$41 \$6B \$11 \$49 \$04 \$1B \$00 \$00 \$00 \$00
1850PWM ECU2 SID\$09 INF\$04 9 M28	\$41 \$6B \$11 \$49 \$04 \$1C \$00 \$00 \$00 \$00
1850PWM ECU2 SID\$09 INF\$04 9 M29	\$41 \$6B \$11 \$49 \$04 \$1D \$38 \$32 \$35 \$38
1850PWM ECU2 SID\$09 INF\$04 9 M30	\$41 \$6B \$11 \$49 \$04 \$1E \$39 \$37 \$37 \$31
1850PWM ECU2 SID\$09 INF\$04 9 M31	\$41 \$6B \$11 \$49 \$04 \$1F \$00 \$00 \$00 \$00
1850PWM ECU2 SID\$09 INF\$04 9 M32	\$41 \$6B \$11 \$49 \$04 \$20 \$00 \$00 \$00 \$00
1850PWM ECU2 SID\$09 INF\$04 9 M33	\$41 \$6B \$11 \$49 \$04 \$21 \$39 \$32 \$35 \$38
1850PWM ECU2 SID\$09 INF\$04 9 M34	\$41 \$6B \$11 \$49 \$04 \$22 \$39 \$37 \$37 \$30
1850PWM ECU2 SID\$09 INF\$04 9 M35	\$41 \$6B \$11 \$49 \$04 \$23 \$00 \$00 \$00 \$00
1850PWM ECU2 SID\$09 INF\$04 9 M36	\$41 \$6B \$11 \$49 \$04 \$24 \$00 \$00 \$00 \$00
1850PWM ECU2 SID\$09 INF\$05	\$41 \$6B \$11 \$49 \$05 \$01
1850PWM ECU2 SID\$09 INF\$05 9	\$41 \$6B \$11 \$49 \$05 \$09
1850PWM ECU2 SID\$09 INF\$06	\$41 \$6B \$11 \$49 \$06 \$01 \$27 \$91 \$BC \$88
1850PWM ECU2 SID\$09 INF\$06 9 M1	\$41 \$6B \$11 \$49 \$06 \$01 \$27 \$91 \$BC \$88
1850PWM ECU2 SID\$09 INF\$06 9 M2	\$41 \$6B \$11 \$49 \$06 \$02 \$28 \$91 \$BC \$87
1850PWM ECU2 SID\$09 INF\$06 9 M3	\$41 \$6B \$11 \$49 \$06 \$03 \$29 \$91 \$BC \$86
1850PWM ECU2 SID\$09 INF\$06 9 M4	\$41 \$6B \$11 \$49 \$06 \$04 \$2A \$91 \$BC \$85
1850PWM ECU2 SID\$09 INF\$06 9 M5	\$41 \$6B \$11 \$49 \$06 \$05 \$2B \$91 \$BC \$84
1850PWM ECU2 SID\$09 INF\$06 9 M6	\$41 \$6B \$11 \$49 \$06 \$06 \$2C \$91 \$BC \$83
1850PWM ECU2 SID\$09 INF\$06 9 M7	\$41 \$6B \$11 \$49 \$06 \$07 \$2D \$91 \$BC \$82
1850PWM ECU2 SID\$09 INF\$06 9 M8	\$41 \$6B \$11 \$49 \$06 \$08 \$2E \$55 \$AA \$81
1850PWM ECU2 SID\$09 INF\$06 9 M9	\$41 \$6B \$11 \$49 \$06 \$09 \$2F \$FF \$00 \$80
1850PWM ECU2 SID\$09 INF\$07 832	\$41 \$6B \$11 \$49 \$07 \$08
1850PWM ECU2 SID\$09 INF\$07 840	\$41 \$6B \$11 \$49 \$07 \$0A
1850PWM ECU2 SID\$09 INF\$07 B32	\$41 \$6B \$11 \$49 \$07 \$08
1850PWM ECU2 SID\$09 INF\$07 B36	\$41 \$6B \$11 \$49 \$07 \$09
1850PWM ECU2 SID\$09 INF\$08 32 M1	\$41 \$6B \$11 \$49 \$08 \$01 \$00 \$64 \$00 \$69
1850PWM ECU2 SID\$09 INF\$08 32 M2	\$41 \$6B \$11 \$49 \$08 \$02 \$00 \$00 \$00 \$00
1850PWM ECU2 SID\$09 INF\$08 32 M3	\$41 \$6B \$11 \$49 \$08 \$03 \$00 \$00 \$14 \$00 \$19
1850PWM ECU2 SID\$09 INF\$08 32 M4	\$41 \$6B \$11 \$49 \$08 \$04 \$00 \$00 \$00 \$00
1850PWM ECU2 SID\$09 INF\$08 32 M5	\$41 \$6B \$11 \$49 \$08 \$05 \$00 \$00 \$28 \$00 \$2D
1850PWM ECU2 SID\$09 INF\$08 32 M6	\$41 \$6B \$11 \$49 \$08 \$06 \$00 \$00 \$00 \$00
1850PWM ECU2 SID\$09 INF\$08 32 M7	\$41 \$6B \$11 \$49 \$08 \$07 \$00 \$00 \$00 \$00
1850PWM ECU2 SID\$09 INF\$08 32 M8	\$41 \$6B \$11 \$49 \$08 \$08 \$00 \$00 \$00 \$00
1850PWM ECU2 SID\$09 INF\$08 40 M1	\$41 \$6B \$11 \$49 \$08 \$01 \$00 \$64 \$00 \$69
1850PWM ECU2 SID\$09 INF\$08 40 M2	\$41 \$6B \$11 \$49 \$08 \$02 \$00 \$00 \$00 \$00
1850PWM ECU2 SID\$09 INF\$08 40 M3	\$41 \$6B \$11 \$49 \$08 \$03 \$00 \$00 \$14 \$00 \$19
1850PWM ECU2 SID\$09 INF\$08 40 M4	\$41 \$6B \$11 \$49 \$08 \$04 \$00 \$00 \$00 \$00
1850PWM ECU2 SID\$09 INF\$08 40 M5	\$41 \$6B \$11 \$49 \$08 \$05 \$00 \$00 \$28 \$00 \$2D
1850PWM ECU2 SID\$09 INF\$08 40 M6	\$41 \$6B \$11 \$49 \$08 \$06 \$00 \$00 \$00 \$00
1850PWM ECU2 SID\$09 INF\$08 40 M7	\$41 \$6B \$11 \$49 \$08 \$07 \$00 \$00 \$00 \$00
1850PWM ECU2 SID\$09 INF\$08 40 M8	\$41 \$6B \$11 \$49 \$08 \$08 \$00 \$00 \$00 \$00
1850PWM ECU2 SID\$09 INF\$08 40 M9	\$41 \$6B \$11 \$49 \$08 \$09 \$00 \$00 \$00 \$00
1850PWM ECU2 SID\$09 INF\$08 40 M10	\$41 \$6B \$11 \$49 \$08 \$0A \$00 \$5A \$00 \$5F
1850PWM ECU3 SID\$09 INF\$00	\$41 \$6B \$12 \$49 \$00 \$3C \$00 \$00 \$00
1850PWM ECU3 SID\$09 INF\$03 9	\$41 \$6B \$12 \$49 \$03 \$24
1850PWM ECU3 SID\$09 INF\$04 9 M1	\$41 \$6B \$12 \$49 \$04 \$01 \$31 \$33 \$35 \$38
1850PWM ECU3 SID\$09 INF\$04 9 M2	\$41 \$6B \$12 \$49 \$04 \$02 \$39 \$37 \$37 \$38
1850PWM ECU3 SID\$09 INF\$04 9 M3	\$41 \$6B \$12 \$49 \$04 \$03 \$00 \$00 \$00 \$00

1850PWM ECU3 SID\$09 INF\$04 9 M4	\$41 \$6B \$12 \$49 \$04 \$04 \$00 \$00 \$00 \$00
1850PWM ECU3 SID\$09 INF\$04 9 M5	\$41 \$6B \$12 \$49 \$04 \$05 \$32 \$33 \$35 \$38
1850PWM ECU3 SID\$09 INF\$04 9 M6	\$41 \$6B \$12 \$49 \$04 \$06 \$39 \$37 \$37 \$37
1850PWM ECU3 SID\$09 INF\$04 9 M7	\$41 \$6B \$12 \$49 \$04 \$07 \$00 \$00 \$00 \$00
1850PWM ECU3 SID\$09 INF\$04 9 M8	\$41 \$6B \$12 \$49 \$04 \$08 \$00 \$00 \$00 \$00
1850PWM ECU3 SID\$09 INF\$04 9 M9	\$41 \$6B \$12 \$49 \$04 \$09 \$33 \$33 \$35 \$38
1850PWM ECU3 SID\$09 INF\$04 9 M10	\$41 \$6B \$12 \$49 \$04 \$0A \$39 \$37 \$37 \$36
1850PWM ECU3 SID\$09 INF\$04 9 M11	\$41 \$6B \$12 \$49 \$04 \$0B \$00 \$00 \$00 \$00
1850PWM ECU3 SID\$09 INF\$04 9 M12	\$41 \$6B \$12 \$49 \$04 \$0C \$00 \$00 \$00 \$00
1850PWM ECU3 SID\$09 INF\$04 9 M13	\$41 \$6B \$12 \$49 \$04 \$0D \$34 \$33 \$35 \$38
1850PWM ECU3 SID\$09 INF\$04 9 M14	\$41 \$6B \$12 \$49 \$04 \$0E \$39 \$37 \$37 \$35
1850PWM ECU3 SID\$09 INF\$04 9 M15	\$41 \$6B \$12 \$49 \$04 \$0F \$00 \$00 \$00 \$00
1850PWM ECU3 SID\$09 INF\$04 9 M16	\$41 \$6B \$12 \$49 \$04 \$10 \$00 \$00 \$00 \$00
1850PWM ECU3 SID\$09 INF\$04 9 M17	\$41 \$6B \$12 \$49 \$04 \$11 \$35 \$33 \$35 \$38
1850PWM ECU3 SID\$09 INF\$04 9 M18	\$41 \$6B \$12 \$49 \$04 \$12 \$39 \$37 \$37 \$34
1850PWM ECU3 SID\$09 INF\$04 9 M19	\$41 \$6B \$12 \$49 \$04 \$13 \$00 \$00 \$00 \$00
1850PWM ECU3 SID\$09 INF\$04 9 M20	\$41 \$6B \$12 \$49 \$04 \$14 \$00 \$00 \$00 \$00
1850PWM ECU3 SID\$09 INF\$04 9 M21	\$41 \$6B \$12 \$49 \$04 \$15 \$36 \$33 \$35 \$38
1850PWM ECU3 SID\$09 INF\$04 9 M22	\$41 \$6B \$12 \$49 \$04 \$16 \$39 \$37 \$37 \$33
1850PWM ECU3 SID\$09 INF\$04 9 M23	\$41 \$6B \$12 \$49 \$04 \$17 \$00 \$00 \$00 \$00
1850PWM ECU3 SID\$09 INF\$04 9 M24	\$41 \$6B \$12 \$49 \$04 \$18 \$00 \$00 \$00 \$00
1850PWM ECU3 SID\$09 INF\$04 9 M25	\$41 \$6B \$12 \$49 \$04 \$19 \$37 \$33 \$35 \$38
1850PWM ECU3 SID\$09 INF\$04 9 M26	\$41 \$6B \$12 \$49 \$04 \$1A \$39 \$37 \$37 \$32
1850PWM ECU3 SID\$09 INF\$04 9 M27	\$41 \$6B \$12 \$49 \$04 \$1B \$00 \$00 \$00 \$00
1850PWM ECU3 SID\$09 INF\$04 9 M28	\$41 \$6B \$12 \$49 \$04 \$1C \$00 \$00 \$00 \$00
1850PWM ECU3 SID\$09 INF\$04 9 M29	\$41 \$6B \$12 \$49 \$04 \$1D \$38 \$33 \$35 \$38
1850PWM ECU3 SID\$09 INF\$04 9 M30	\$41 \$6B \$12 \$49 \$04 \$1E \$39 \$37 \$37 \$31
1850PWM ECU3 SID\$09 INF\$04 9 M31	\$41 \$6B \$12 \$49 \$04 \$1F \$00 \$00 \$00 \$00
1850PWM ECU3 SID\$09 INF\$04 9 M32	\$41 \$6B \$12 \$49 \$04 \$20 \$00 \$00 \$00 \$00
1850PWM ECU3 SID\$09 INF\$04 9 M33	\$41 \$6B \$12 \$49 \$04 \$21 \$39 \$33 \$35 \$38
1850PWM ECU3 SID\$09 INF\$04 9 M34	\$41 \$6B \$12 \$49 \$04 \$22 \$39 \$37 \$37 \$30
1850PWM ECU3 SID\$09 INF\$04 9 M35	\$41 \$6B \$12 \$49 \$04 \$23 \$00 \$00 \$00 \$00
1850PWM ECU3 SID\$09 INF\$04 9 M36	\$41 \$6B \$12 \$49 \$04 \$24 \$00 \$00 \$00 \$00
1850PWM ECU3 SID\$09 INF\$05 9	\$41 \$6B \$12 \$49 \$05 \$09
1850PWM ECU3 SID\$09 INF\$06 9 M1	\$41 \$6B \$12 \$49 \$06 \$01 \$37 \$91 \$BC \$88
1850PWM ECU3 SID\$09 INF\$06 9 M2	\$41 \$6B \$12 \$49 \$06 \$02 \$38 \$91 \$BC \$87
1850PWM ECU3 SID\$09 INF\$06 9 M3	\$41 \$6B \$12 \$49 \$06 \$03 \$39 \$91 \$BC \$86
1850PWM ECU3 SID\$09 INF\$06 9 M4	\$41 \$6B \$12 \$49 \$06 \$04 \$3A \$91 \$BC \$85
1850PWM ECU3 SID\$09 INF\$06 9 M5	\$41 \$6B \$12 \$49 \$06 \$05 \$3B \$91 \$BC \$84
1850PWM ECU3 SID\$09 INF\$06 9 M6	\$41 \$6B \$12 \$49 \$06 \$06 \$3C \$91 \$BC \$83
1850PWM ECU3 SID\$09 INF\$06 9 M7	\$41 \$6B \$12 \$49 \$06 \$07 \$3D \$91 \$BC \$82
1850PWM ECU3 SID\$09 INF\$06 9 M8	\$41 \$6B \$12 \$49 \$06 \$08 \$3E \$55 \$AA \$81
1850PWM ECU3 SID\$09 INF\$06 9 M9	\$41 \$6B \$12 \$49 \$06 \$09 \$3F \$FF \$00 \$80
1850PWM ECU4 SID\$09 INF\$00	\$41 \$6B \$13 \$49 \$00 \$3C \$00 \$00 \$00
1850PWM ECU4 SID\$09 INF\$03 9	\$41 \$6B \$13 \$49 \$03 \$24
1850PWM ECU4 SID\$09 INF\$04 9 M1	\$41 \$6B \$13 \$49 \$04 \$01 \$31 \$34 \$35 \$38
1850PWM ECU4 SID\$09 INF\$04 9 M2	\$41 \$6B \$13 \$49 \$04 \$02 \$39 \$37 \$37 \$38
1850PWM ECU4 SID\$09 INF\$04 9 M3	\$41 \$6B \$13 \$49 \$04 \$03 \$00 \$00 \$00 \$00
1850PWM ECU4 SID\$09 INF\$04 9 M4	\$41 \$6B \$13 \$49 \$04 \$04 \$00 \$00 \$00 \$00
1850PWM ECU4 SID\$09 INF\$04 9 M5	\$41 \$6B \$13 \$49 \$04 \$05 \$32 \$34 \$35 \$38
1850PWM ECU4 SID\$09 INF\$04 9 M6	\$41 \$6B \$13 \$49 \$04 \$06 \$39 \$37 \$37 \$37
1850PWM ECU4 SID\$09 INF\$04 9 M7	\$41 \$6B \$13 \$49 \$04 \$07 \$00 \$00 \$00 \$00
1850PWM ECU4 SID\$09 INF\$04 9 M8	\$41 \$6B \$13 \$49 \$04 \$08 \$00 \$00 \$00 \$00
1850PWM ECU4 SID\$09 INF\$04 9 M9	\$41 \$6B \$13 \$49 \$04 \$09 \$33 \$34 \$35 \$38
1850PWM ECU4 SID\$09 INF\$04 9 M10	\$41 \$6B \$13 \$49 \$04 \$0A \$39 \$37 \$37 \$36
1850PWM ECU4 SID\$09 INF\$04 9 M11	\$41 \$6B \$13 \$49 \$04 \$0B \$00 \$00 \$00 \$00
1850PWM ECU4 SID\$09 INF\$04 9 M12	\$41 \$6B \$13 \$49 \$04 \$0C \$00 \$00 \$00 \$00
1850PWM ECU4 SID\$09 INF\$04 9 M13	\$41 \$6B \$13 \$49 \$04 \$0D \$34 \$34 \$35 \$38

1850PWM ECU4 SID\$09 INF\$04 9 M14	\$41 \$6B \$13 \$49 \$04 \$0E \$39 \$37 \$37 \$35
1850PWM ECU4 SID\$09 INF\$04 9 M15	\$41 \$6B \$13 \$49 \$04 \$0F \$00 \$00 \$00 \$00
1850PWM ECU4 SID\$09 INF\$04 9 M16	\$41 \$6B \$13 \$49 \$04 \$10 \$00 \$00 \$00 \$00
1850PWM ECU4 SID\$09 INF\$04 9 M17	\$41 \$6B \$13 \$49 \$04 \$11 \$35 \$34 \$35 \$38
1850PWM ECU4 SID\$09 INF\$04 9 M18	\$41 \$6B \$13 \$49 \$04 \$12 \$39 \$37 \$37 \$34
1850PWM ECU4 SID\$09 INF\$04 9 M19	\$41 \$6B \$13 \$49 \$04 \$13 \$00 \$00 \$00 \$00
1850PWM ECU4 SID\$09 INF\$04 9 M20	\$41 \$6B \$13 \$49 \$04 \$14 \$00 \$00 \$00 \$00
1850PWM ECU4 SID\$09 INF\$04 9 M21	\$41 \$6B \$13 \$49 \$04 \$15 \$36 \$34 \$35 \$38
1850PWM ECU4 SID\$09 INF\$04 9 M22	\$41 \$6B \$13 \$49 \$04 \$16 \$39 \$37 \$37 \$33
1850PWM ECU4 SID\$09 INF\$04 9 M23	\$41 \$6B \$13 \$49 \$04 \$17 \$00 \$00 \$00 \$00
1850PWM ECU4 SID\$09 INF\$04 9 M24	\$41 \$6B \$13 \$49 \$04 \$18 \$00 \$00 \$00 \$00
1850PWM ECU4 SID\$09 INF\$04 9 M25	\$41 \$6B \$13 \$49 \$04 \$19 \$37 \$34 \$35 \$38
1850PWM ECU4 SID\$09 INF\$04 9 M26	\$41 \$6B \$13 \$49 \$04 \$1A \$39 \$37 \$37 \$32
1850PWM ECU4 SID\$09 INF\$04 9 M27	\$41 \$6B \$13 \$49 \$04 \$1B \$00 \$00 \$00 \$00
1850PWM ECU4 SID\$09 INF\$04 9 M28	\$41 \$6B \$13 \$49 \$04 \$1C \$00 \$00 \$00 \$00
1850PWM ECU4 SID\$09 INF\$04 9 M29	\$41 \$6B \$13 \$49 \$04 \$1D \$38 \$34 \$35 \$38
1850PWM ECU4 SID\$09 INF\$04 9 M30	\$41 \$6B \$13 \$49 \$04 \$1E \$39 \$37 \$37 \$31
1850PWM ECU4 SID\$09 INF\$04 9 M31	\$41 \$6B \$13 \$49 \$04 \$1F \$00 \$00 \$00 \$00
1850PWM ECU4 SID\$09 INF\$04 9 M32	\$41 \$6B \$13 \$49 \$04 \$20 \$00 \$00 \$00 \$00
1850PWM ECU4 SID\$09 INF\$04 9 M33	\$41 \$6B \$13 \$49 \$04 \$21 \$39 \$34 \$35 \$38
1850PWM ECU4 SID\$09 INF\$04 9 M34	\$41 \$6B \$13 \$49 \$04 \$22 \$39 \$37 \$37 \$30
1850PWM ECU4 SID\$09 INF\$04 9 M35	\$41 \$6B \$13 \$49 \$04 \$23 \$00 \$00 \$00 \$00
1850PWM ECU4 SID\$09 INF\$04 9 M36	\$41 \$6B \$13 \$49 \$04 \$24 \$00 \$00 \$00 \$00
1850PWM ECU4 SID\$09 INF\$05 9	\$41 \$6B \$13 \$49 \$05 \$09
1850PWM ECU4 SID\$09 INF\$06 9 M1	\$41 \$6B \$13 \$49 \$06 \$01 \$47 \$91 \$BC \$88
1850PWM ECU4 SID\$09 INF\$06 9 M2	\$41 \$6B \$13 \$49 \$06 \$02 \$48 \$91 \$BC \$87
1850PWM ECU4 SID\$09 INF\$06 9 M3	\$41 \$6B \$13 \$49 \$06 \$03 \$49 \$91 \$BC \$86
1850PWM ECU4 SID\$09 INF\$06 9 M4	\$41 \$6B \$13 \$49 \$06 \$04 \$4A \$91 \$BC \$85
1850PWM ECU4 SID\$09 INF\$06 9 M5	\$41 \$6B \$13 \$49 \$06 \$05 \$4B \$91 \$BC \$84
1850PWM ECU4 SID\$09 INF\$06 9 M6	\$41 \$6B \$13 \$49 \$06 \$06 \$4C \$91 \$BC \$83
1850PWM ECU4 SID\$09 INF\$06 9 M7	\$41 \$6B \$13 \$49 \$06 \$07 \$4D \$91 \$BC \$82
1850PWM ECU4 SID\$09 INF\$06 9 M8	\$41 \$6B \$13 \$49 \$06 \$08 \$4E \$55 \$AA \$81
1850PWM ECU4 SID\$09 INF\$06 9 M9	\$41 \$6B \$13 \$49 \$06 \$09 \$4F \$FF \$00 \$80
1850PWM ECU5 SID\$09 INF\$00	\$41 \$6B \$14 \$49 \$00 \$3C \$00 \$00 \$00
1850PWM ECU5 SID\$09 INF\$03 9	\$41 \$6B \$14 \$49 \$03 \$24
1850PWM ECU5 SID\$09 INF\$04 9 M1	\$41 \$6B \$14 \$49 \$04 \$01 \$31 \$35 \$35 \$38
1850PWM ECU5 SID\$09 INF\$04 9 M2	\$41 \$6B \$14 \$49 \$04 \$02 \$39 \$37 \$37 \$38
1850PWM ECU5 SID\$09 INF\$04 9 M3	\$41 \$6B \$14 \$49 \$04 \$03 \$00 \$00 \$00 \$00
1850PWM ECU5 SID\$09 INF\$04 9 M4	\$41 \$6B \$14 \$49 \$04 \$04 \$00 \$00 \$00 \$00
1850PWM ECU5 SID\$09 INF\$04 9 M5	\$41 \$6B \$14 \$49 \$04 \$05 \$32 \$35 \$35 \$38
1850PWM ECU5 SID\$09 INF\$04 9 M6	\$41 \$6B \$14 \$49 \$04 \$06 \$39 \$37 \$37 \$37
1850PWM ECU5 SID\$09 INF\$04 9 M7	\$41 \$6B \$14 \$49 \$04 \$07 \$00 \$00 \$00 \$00
1850PWM ECU5 SID\$09 INF\$04 9 M8	\$41 \$6B \$14 \$49 \$04 \$08 \$00 \$00 \$00 \$00
1850PWM ECU5 SID\$09 INF\$04 9 M9	\$41 \$6B \$14 \$49 \$04 \$09 \$33 \$35 \$35 \$38
1850PWM ECU5 SID\$09 INF\$04 9 M10	\$41 \$6B \$14 \$49 \$04 \$0A \$39 \$37 \$37 \$36
1850PWM ECU5 SID\$09 INF\$04 9 M11	\$41 \$6B \$14 \$49 \$04 \$0B \$00 \$00 \$00 \$00
1850PWM ECU5 SID\$09 INF\$04 9 M12	\$41 \$6B \$14 \$49 \$04 \$0C \$00 \$00 \$00 \$00
1850PWM ECU5 SID\$09 INF\$04 9 M13	\$41 \$6B \$14 \$49 \$04 \$0D \$34 \$35 \$35 \$38
1850PWM ECU5 SID\$09 INF\$04 9 M14	\$41 \$6B \$14 \$49 \$04 \$0E \$39 \$37 \$37 \$35
1850PWM ECU5 SID\$09 INF\$04 9 M15	\$41 \$6B \$14 \$49 \$04 \$0F \$00 \$00 \$00 \$00
1850PWM ECU5 SID\$09 INF\$04 9 M16	\$41 \$6B \$14 \$49 \$04 \$10 \$00 \$00 \$00 \$00
1850PWM ECU5 SID\$09 INF\$04 9 M17	\$41 \$6B \$14 \$49 \$04 \$11 \$35 \$35 \$35 \$38
1850PWM ECU5 SID\$09 INF\$04 9 M18	\$41 \$6B \$14 \$49 \$04 \$12 \$39 \$37 \$37 \$34
1850PWM ECU5 SID\$09 INF\$04 9 M19	\$41 \$6B \$14 \$49 \$04 \$13 \$00 \$00 \$00 \$00
1850PWM ECU5 SID\$09 INF\$04 9 M20	\$41 \$6B \$14 \$49 \$04 \$14 \$00 \$00 \$00 \$00
1850PWM ECU5 SID\$09 INF\$04 9 M21	\$41 \$6B \$14 \$49 \$04 \$15 \$36 \$35 \$35 \$38
1850PWM ECU5 SID\$09 INF\$04 9 M22	\$41 \$6B \$14 \$49 \$04 \$16 \$39 \$37 \$37 \$33
1850PWM ECU5 SID\$09 INF\$04 9 M23	\$41 \$6B \$14 \$49 \$04 \$17 \$00 \$00 \$00 \$00

1850PWM ECU5 SID\$09 INF\$04 9 M24	\$41 \$6B \$14 \$49 \$04 \$18 \$00 \$00 \$00 \$00
1850PWM ECU5 SID\$09 INF\$04 9 M25	\$41 \$6B \$14 \$49 \$04 \$19 \$37 \$35 \$35 \$38
1850PWM ECU5 SID\$09 INF\$04 9 M26	\$41 \$6B \$14 \$49 \$04 \$1A \$39 \$37 \$37 \$32
1850PWM ECU5 SID\$09 INF\$04 9 M27	\$41 \$6B \$14 \$49 \$04 \$1B \$00 \$00 \$00 \$00
1850PWM ECU5 SID\$09 INF\$04 9 M28	\$41 \$6B \$14 \$49 \$04 \$1C \$00 \$00 \$00 \$00
1850PWM ECU5 SID\$09 INF\$04 9 M29	\$41 \$6B \$14 \$49 \$04 \$1D \$38 \$35 \$35 \$38
1850PWM ECU5 SID\$09 INF\$04 9 M30	\$41 \$6B \$14 \$49 \$04 \$1E \$39 \$37 \$37 \$31
1850PWM ECU5 SID\$09 INF\$04 9 M31	\$41 \$6B \$14 \$49 \$04 \$1F \$00 \$00 \$00 \$00
1850PWM ECU5 SID\$09 INF\$04 9 M32	\$41 \$6B \$14 \$49 \$04 \$20 \$00 \$00 \$00 \$00
1850PWM ECU5 SID\$09 INF\$04 9 M33	\$41 \$6B \$14 \$49 \$04 \$21 \$39 \$35 \$35 \$38
1850PWM ECU5 SID\$09 INF\$04 9 M34	\$41 \$6B \$14 \$49 \$04 \$22 \$39 \$37 \$37 \$30
1850PWM ECU5 SID\$09 INF\$04 9 M35	\$41 \$6B \$14 \$49 \$04 \$23 \$00 \$00 \$00 \$00
1850PWM ECU5 SID\$09 INF\$04 9 M36	\$41 \$6B \$14 \$49 \$04 \$24 \$00 \$00 \$00 \$00
1850PWM ECU5 SID\$09 INF\$05 9	\$41 \$6B \$14 \$49 \$05 \$09
1850PWM ECU5 SID\$09 INF\$06 9 M1	\$41 \$6B \$14 \$49 \$06 \$01 \$57 \$91 \$BC \$88
1850PWM ECU5 SID\$09 INF\$06 9 M2	\$41 \$6B \$14 \$49 \$06 \$02 \$58 \$91 \$BC \$87
1850PWM ECU5 SID\$09 INF\$06 9 M3	\$41 \$6B \$14 \$49 \$06 \$03 \$59 \$91 \$BC \$86
1850PWM ECU5 SID\$09 INF\$06 9 M4	\$41 \$6B \$14 \$49 \$06 \$04 \$5A \$91 \$BC \$85
1850PWM ECU5 SID\$09 INF\$06 9 M5	\$41 \$6B \$14 \$49 \$06 \$05 \$5B \$91 \$BC \$84
1850PWM ECU5 SID\$09 INF\$06 9 M6	\$41 \$6B \$14 \$49 \$06 \$06 \$5C \$91 \$BC \$83
1850PWM ECU5 SID\$09 INF\$06 9 M7	\$41 \$6B \$14 \$49 \$06 \$07 \$5D \$91 \$BC \$82
1850PWM ECU5 SID\$09 INF\$06 9 M8	\$41 \$6B \$14 \$49 \$06 \$08 \$5E \$55 \$AA \$81
1850PWM ECU5 SID\$09 INF\$06 9 M9	\$41 \$6B \$14 \$49 \$06 \$09 \$5F \$FF \$00 \$80
1850PWM ECU6 SID\$09 INF\$00	\$41 \$6B \$15 \$49 \$00 \$3C \$00 \$00 \$00
1850PWM ECU6 SID\$09 INF\$03 9	\$41 \$6B \$15 \$49 \$03 \$24
1850PWM ECU6 SID\$09 INF\$04 9 M1	\$41 \$6B \$15 \$49 \$04 \$01 \$31 \$36 \$35 \$38
1850PWM ECU6 SID\$09 INF\$04 9 M2	\$41 \$6B \$15 \$49 \$04 \$02 \$39 \$37 \$37 \$38
1850PWM ECU6 SID\$09 INF\$04 9 M3	\$41 \$6B \$15 \$49 \$04 \$03 \$00 \$00 \$00 \$00
1850PWM ECU6 SID\$09 INF\$04 9 M4	\$41 \$6B \$15 \$49 \$04 \$04 \$00 \$00 \$00 \$00
1850PWM ECU6 SID\$09 INF\$04 9 M5	\$41 \$6B \$15 \$49 \$04 \$05 \$32 \$36 \$35 \$38
1850PWM ECU6 SID\$09 INF\$04 9 M6	\$41 \$6B \$15 \$49 \$04 \$06 \$39 \$37 \$37 \$37
1850PWM ECU6 SID\$09 INF\$04 9 M7	\$41 \$6B \$15 \$49 \$04 \$07 \$00 \$00 \$00 \$00
1850PWM ECU6 SID\$09 INF\$04 9 M8	\$41 \$6B \$15 \$49 \$04 \$08 \$00 \$00 \$00 \$00
1850PWM ECU6 SID\$09 INF\$04 9 M9	\$41 \$6B \$15 \$49 \$04 \$09 \$33 \$36 \$35 \$38
1850PWM ECU6 SID\$09 INF\$04 9 M10	\$41 \$6B \$15 \$49 \$04 \$0A \$39 \$37 \$37 \$36
1850PWM ECU6 SID\$09 INF\$04 9 M11	\$41 \$6B \$15 \$49 \$04 \$0B \$00 \$00 \$00 \$00
1850PWM ECU6 SID\$09 INF\$04 9 M12	\$41 \$6B \$15 \$49 \$04 \$0C \$00 \$00 \$00 \$00
1850PWM ECU6 SID\$09 INF\$04 9 M13	\$41 \$6B \$15 \$49 \$04 \$0D \$34 \$36 \$35 \$38
1850PWM ECU6 SID\$09 INF\$04 9 M14	\$41 \$6B \$15 \$49 \$04 \$0E \$39 \$37 \$37 \$35
1850PWM ECU6 SID\$09 INF\$04 9 M15	\$41 \$6B \$15 \$49 \$04 \$0F \$00 \$00 \$00 \$00
1850PWM ECU6 SID\$09 INF\$04 9 M16	\$41 \$6B \$15 \$49 \$04 \$10 \$00 \$00 \$00 \$00
1850PWM ECU6 SID\$09 INF\$04 9 M17	\$41 \$6B \$15 \$49 \$04 \$11 \$35 \$36 \$35 \$38
1850PWM ECU6 SID\$09 INF\$04 9 M18	\$41 \$6B \$15 \$49 \$04 \$12 \$39 \$37 \$37 \$34
1850PWM ECU6 SID\$09 INF\$04 9 M19	\$41 \$6B \$15 \$49 \$04 \$13 \$00 \$00 \$00 \$00
1850PWM ECU6 SID\$09 INF\$04 9 M20	\$41 \$6B \$15 \$49 \$04 \$14 \$00 \$00 \$00 \$00
1850PWM ECU6 SID\$09 INF\$04 9 M21	\$41 \$6B \$15 \$49 \$04 \$15 \$36 \$36 \$35 \$38
1850PWM ECU6 SID\$09 INF\$04 9 M22	\$41 \$6B \$15 \$49 \$04 \$16 \$39 \$37 \$37 \$33
1850PWM ECU6 SID\$09 INF\$04 9 M23	\$41 \$6B \$15 \$49 \$04 \$17 \$00 \$00 \$00 \$00
1850PWM ECU6 SID\$09 INF\$04 9 M24	\$41 \$6B \$15 \$49 \$04 \$18 \$00 \$00 \$00 \$00
1850PWM ECU6 SID\$09 INF\$04 9 M25	\$41 \$6B \$15 \$49 \$04 \$19 \$37 \$36 \$35 \$38
1850PWM ECU6 SID\$09 INF\$04 9 M26	\$41 \$6B \$15 \$49 \$04 \$1A \$39 \$37 \$37 \$32
1850PWM ECU6 SID\$09 INF\$04 9 M27	\$41 \$6B \$15 \$49 \$04 \$1B \$00 \$00 \$00 \$00
1850PWM ECU6 SID\$09 INF\$04 9 M28	\$41 \$6B \$15 \$49 \$04 \$1C \$00 \$00 \$00 \$00
1850PWM ECU6 SID\$09 INF\$04 9 M29	\$41 \$6B \$15 \$49 \$04 \$1D \$38 \$36 \$35 \$38
1850PWM ECU6 SID\$09 INF\$04 9 M30	\$41 \$6B \$15 \$49 \$04 \$1E \$39 \$37 \$37 \$31
1850PWM ECU6 SID\$09 INF\$04 9 M31	\$41 \$6B \$15 \$49 \$04 \$1F \$00 \$00 \$00 \$00
1850PWM ECU6 SID\$09 INF\$04 9 M32	\$41 \$6B \$15 \$49 \$04 \$20 \$00 \$00 \$00 \$00
1850PWM ECU6 SID\$09 INF\$04 9 M33	\$41 \$6B \$15 \$49 \$04 \$21 \$39 \$36 \$35 \$38

1850PWM ECU6 SID\$09 INF\$04 9 M34	\$41 \$6B \$15 \$49 \$04 \$22 \$39 \$37 \$37 \$30
1850PWM ECU6 SID\$09 INF\$04 9 M35	\$41 \$6B \$15 \$49 \$04 \$23 \$00 \$00 \$00 \$00
1850PWM ECU6 SID\$09 INF\$04 9 M36	\$41 \$6B \$15 \$49 \$04 \$24 \$00 \$00 \$00 \$00
1850PWM ECU6 SID\$09 INF\$05 9	\$41 \$6B \$15 \$49 \$05 \$09
1850PWM ECU6 SID\$09 INF\$06 9 M1	\$41 \$6B \$15 \$49 \$06 \$01 \$67 \$91 \$BC \$88
1850PWM ECU6 SID\$09 INF\$06 9 M2	\$41 \$6B \$15 \$49 \$06 \$02 \$68 \$91 \$BC \$87
1850PWM ECU6 SID\$09 INF\$06 9 M3	\$41 \$6B \$15 \$49 \$06 \$03 \$69 \$91 \$BC \$86
1850PWM ECU6 SID\$09 INF\$06 9 M4	\$41 \$6B \$15 \$49 \$06 \$04 \$6A \$91 \$BC \$85
1850PWM ECU6 SID\$09 INF\$06 9 M5	\$41 \$6B \$15 \$49 \$06 \$05 \$6B \$91 \$BC \$84
1850PWM ECU6 SID\$09 INF\$06 9 M6	\$41 \$6B \$15 \$49 \$06 \$06 \$6C \$91 \$BC \$83
1850PWM ECU6 SID\$09 INF\$06 9 M7	\$41 \$6B \$15 \$49 \$06 \$07 \$6D \$91 \$BC \$82
1850PWM ECU6 SID\$09 INF\$06 9 M8	\$41 \$6B \$15 \$49 \$06 \$08 \$6E \$55 \$AA \$81
1850PWM ECU6 SID\$09 INF\$06 9 M9	\$41 \$6B \$15 \$49 \$06 \$09 \$6F \$FF \$00 \$80
1850PWM ECU7 SID\$09 INF\$00	\$41 \$6B \$16 \$49 \$00 \$3C \$00 \$00 \$00
1850PWM ECU7 SID\$09 INF\$03 9	\$41 \$6B \$16 \$49 \$03 \$24
1850PWM ECU7 SID\$09 INF\$04 9 M1	\$41 \$6B \$16 \$49 \$04 \$01 \$31 \$37 \$35 \$38
1850PWM ECU7 SID\$09 INF\$04 9 M2	\$41 \$6B \$16 \$49 \$04 \$02 \$39 \$37 \$37 \$38
1850PWM ECU7 SID\$09 INF\$04 9 M3	\$41 \$6B \$16 \$49 \$04 \$03 \$00 \$00 \$00 \$00
1850PWM ECU7 SID\$09 INF\$04 9 M4	\$41 \$6B \$16 \$49 \$04 \$04 \$00 \$00 \$00 \$00
1850PWM ECU7 SID\$09 INF\$04 9 M5	\$41 \$6B \$16 \$49 \$04 \$05 \$32 \$37 \$35 \$38
1850PWM ECU7 SID\$09 INF\$04 9 M6	\$41 \$6B \$16 \$49 \$04 \$06 \$39 \$37 \$37 \$37
1850PWM ECU7 SID\$09 INF\$04 9 M7	\$41 \$6B \$16 \$49 \$04 \$07 \$00 \$00 \$00 \$00
1850PWM ECU7 SID\$09 INF\$04 9 M8	\$41 \$6B \$16 \$49 \$04 \$08 \$00 \$00 \$00 \$00
1850PWM ECU7 SID\$09 INF\$04 9 M9	\$41 \$6B \$16 \$49 \$04 \$09 \$33 \$37 \$35 \$38
1850PWM ECU7 SID\$09 INF\$04 9 M10	\$41 \$6B \$16 \$49 \$04 \$0A \$39 \$37 \$37 \$36
1850PWM ECU7 SID\$09 INF\$04 9 M11	\$41 \$6B \$16 \$49 \$04 \$0B \$00 \$00 \$00 \$00
1850PWM ECU7 SID\$09 INF\$04 9 M12	\$41 \$6B \$16 \$49 \$04 \$0C \$00 \$00 \$00 \$00
1850PWM ECU7 SID\$09 INF\$04 9 M13	\$41 \$6B \$16 \$49 \$04 \$0D \$34 \$37 \$35 \$38
1850PWM ECU7 SID\$09 INF\$04 9 M14	\$41 \$6B \$16 \$49 \$04 \$0E \$39 \$37 \$37 \$35
1850PWM ECU7 SID\$09 INF\$04 9 M15	\$41 \$6B \$16 \$49 \$04 \$0F \$00 \$00 \$00 \$00
1850PWM ECU7 SID\$09 INF\$04 9 M16	\$41 \$6B \$16 \$49 \$04 \$10 \$00 \$00 \$00 \$00
1850PWM ECU7 SID\$09 INF\$04 9 M17	\$41 \$6B \$16 \$49 \$04 \$11 \$35 \$37 \$35 \$38
1850PWM ECU7 SID\$09 INF\$04 9 M18	\$41 \$6B \$16 \$49 \$04 \$12 \$39 \$37 \$37 \$34
1850PWM ECU7 SID\$09 INF\$04 9 M19	\$41 \$6B \$16 \$49 \$04 \$13 \$00 \$00 \$00 \$00
1850PWM ECU7 SID\$09 INF\$04 9 M20	\$41 \$6B \$16 \$49 \$04 \$14 \$00 \$00 \$00 \$00
1850PWM ECU7 SID\$09 INF\$04 9 M21	\$41 \$6B \$16 \$49 \$04 \$15 \$36 \$37 \$35 \$38
1850PWM ECU7 SID\$09 INF\$04 9 M22	\$41 \$6B \$16 \$49 \$04 \$16 \$39 \$37 \$37 \$33
1850PWM ECU7 SID\$09 INF\$04 9 M23	\$41 \$6B \$16 \$49 \$04 \$17 \$00 \$00 \$00 \$00
1850PWM ECU7 SID\$09 INF\$04 9 M24	\$41 \$6B \$16 \$49 \$04 \$18 \$00 \$00 \$00 \$00
1850PWM ECU7 SID\$09 INF\$04 9 M25	\$41 \$6B \$16 \$49 \$04 \$19 \$37 \$37 \$35 \$38
1850PWM ECU7 SID\$09 INF\$04 9 M26	\$41 \$6B \$16 \$49 \$04 \$1A \$39 \$37 \$37 \$32
1850PWM ECU7 SID\$09 INF\$04 9 M26	\$41 \$6B \$16 \$49 \$04 \$1B \$00 \$00 \$00 \$00
1850PWM ECU7 SID\$09 INF\$04 9 M28	\$41 \$6B \$16 \$49 \$04 \$1C \$00 \$00 \$00 \$00
1850PWM ECU7 SID\$09 INF\$04 9 M29	\$41 \$6B \$16 \$49 \$04 \$1D \$38 \$37 \$35 \$38
1850PWM ECU7 SID\$09 INF\$04 9 M30	\$41 \$6B \$16 \$49 \$04 \$1E \$39 \$37 \$37 \$31
1850PWM ECU7 SID\$09 INF\$04 9 M31	\$41 \$6B \$16 \$49 \$04 \$1F \$00 \$00 \$00 \$00
1850PWM ECU7 SID\$09 INF\$04 9 M32	\$41 \$6B \$16 \$49 \$04 \$20 \$00 \$00 \$00 \$00
1850PWM ECU7 SID\$09 INF\$04 9 M33	\$41 \$6B \$16 \$49 \$04 \$21 \$39 \$37 \$35 \$38
1850PWM ECU7 SID\$09 INF\$04 9 M34	\$41 \$6B \$16 \$49 \$04 \$22 \$39 \$37 \$37 \$30
1850PWM ECU7 SID\$09 INF\$04 9 M35	\$41 \$6B \$16 \$49 \$04 \$23 \$00 \$00 \$00 \$00
1850PWM ECU7 SID\$09 INF\$04 9 M36	\$41 \$6B \$16 \$49 \$04 \$24 \$00 \$00 \$00 \$00
1850PWM ECU7 SID\$09 INF\$05	\$41 \$6B \$16 \$49 \$05 \$09
1850PWM ECU7 SID\$09 INF\$06 9 M1	\$41 \$6B \$16 \$49 \$06 \$01 \$77 \$91 \$BC \$88
1850PWM ECU7 SID\$09 INF\$06 9 M2	\$41 \$6B \$16 \$49 \$06 \$02 \$78 \$91 \$BC \$87
1850PWM ECU7 SID\$09 INF\$06 9 M3	\$41 \$6B \$16 \$49 \$06 \$03 \$79 \$91 \$BC \$86
1850PWM ECU7 SID\$09 INF\$06 9 M4	\$41 \$6B \$16 \$49 \$06 \$04 \$7A \$91 \$BC \$85
1850PWM ECU7 SID\$09 INF\$06 9 M5	\$41 \$6B \$16 \$49 \$06 \$05 \$7B \$91 \$BC \$84
1850PWM ECU7 SID\$09 INF\$06 9 M6	\$41 \$6B \$16 \$49 \$06 \$06 \$7C \$91 \$BC \$83

1850PWM ECU7 SID\$09 INF\$06 9 M7	\$41 \$6B \$16 \$49 \$06 \$07 \$7D \$91 \$BC \$82
1850PWM ECU7 SID\$09 INF\$06 9 M8	\$41 \$6B \$16 \$49 \$06 \$08 \$7E \$55 \$AA \$81
1850PWM ECU7 SID\$09 INF\$06 9 M9	\$41 \$6B \$16 \$49 \$06 \$09 \$7F \$FF \$00 \$80
1850PWM ECU8 SID\$09 INF\$00	\$41 \$6B \$17 \$49 \$00 \$3C \$00 \$00 \$00
1850PWM ECU8 SID\$09 INF\$03 9	\$41 \$6B \$17 \$49 \$03 \$24
1850PWM ECU8 SID\$09 INF\$04 9 M1	\$41 \$6B \$17 \$49 \$04 \$01 \$31 \$38 \$35 \$38
1850PWM ECU8 SID\$09 INF\$04 9 M2	\$41 \$6B \$17 \$49 \$04 \$02 \$39 \$37 \$37 \$38
1850PWM ECU8 SID\$09 INF\$04 9 M3	\$41 \$6B \$17 \$49 \$04 \$03 \$00 \$00 \$00 \$00
1850PWM ECU8 SID\$09 INF\$04 9 M4	\$41 \$6B \$17 \$49 \$04 \$04 \$00 \$00 \$00 \$00
1850PWM ECU8 SID\$09 INF\$04 9 M5	\$41 \$6B \$17 \$49 \$04 \$05 \$32 \$38 \$35 \$38
1850PWM ECU8 SID\$09 INF\$04 9 M6	\$41 \$6B \$17 \$49 \$04 \$06 \$39 \$37 \$37 \$37
1850PWM ECU8 SID\$09 INF\$04 9 M7	\$41 \$6B \$17 \$49 \$04 \$07 \$00 \$00 \$00 \$00
1850PWM ECU8 SID\$09 INF\$04 9 M8	\$41 \$6B \$17 \$49 \$04 \$08 \$00 \$00 \$00 \$00
1850PWM ECU8 SID\$09 INF\$04 9 M9	\$41 \$6B \$17 \$49 \$04 \$09 \$33 \$38 \$35 \$38
1850PWM ECU8 SID\$09 INF\$04 9 M10	\$41 \$6B \$17 \$49 \$04 \$0A \$39 \$37 \$37 \$36
1850PWM ECU8 SID\$09 INF\$04 9 M11	\$41 \$6B \$17 \$49 \$04 \$0B \$00 \$00 \$00 \$00
1850PWM ECU8 SID\$09 INF\$04 9 M12	\$41 \$6B \$17 \$49 \$04 \$0C \$00 \$00 \$00 \$00
1850PWM ECU8 SID\$09 INF\$04 9 M13	\$41 \$6B \$17 \$49 \$04 \$0D \$34 \$38 \$35 \$38
1850PWM ECU8 SID\$09 INF\$04 9 M14	\$41 \$6B \$17 \$49 \$04 \$0E \$39 \$37 \$37 \$35
1850PWM ECU8 SID\$09 INF\$04 9 M15	\$41 \$6B \$17 \$49 \$04 \$0F \$00 \$00 \$00 \$00
1850PWM ECU8 SID\$09 INF\$04 9 M16	\$41 \$6B \$17 \$49 \$04 \$10 \$00 \$00 \$00 \$00
1850PWM ECU8 SID\$09 INF\$04 9 M17	\$41 \$6B \$17 \$49 \$04 \$11 \$35 \$38 \$35 \$38
1850PWM ECU8 SID\$09 INF\$04 9 M18	\$41 \$6B \$17 \$49 \$04 \$12 \$39 \$37 \$37 \$34
1850PWM ECU8 SID\$09 INF\$04 9 M19	\$41 \$6B \$17 \$49 \$04 \$13 \$00 \$00 \$00 \$00
1850PWM ECU8 SID\$09 INF\$04 9 M20	\$41 \$6B \$17 \$49 \$04 \$14 \$00 \$00 \$00 \$00
1850PWM ECU8 SID\$09 INF\$04 9 M21	\$41 \$6B \$17 \$49 \$04 \$15 \$36 \$38 \$35 \$38
1850PWM ECU8 SID\$09 INF\$04 9 M22	\$41 \$6B \$17 \$49 \$04 \$16 \$39 \$37 \$37 \$33
1850PWM ECU8 SID\$09 INF\$04 9 M23	\$41 \$6B \$17 \$49 \$04 \$17 \$00 \$00 \$00 \$00
1850PWM ECU8 SID\$09 INF\$04 9 M24	\$41 \$6B \$17 \$49 \$04 \$17 \$00 \$00 \$00 \$00
1850PWM ECU8 SID\$09 INF\$04 9 M25	\$41 \$6B \$17 \$49 \$04 \$19 \$37 \$38 \$35 \$38
1850PWM ECU8 SID\$09 INF\$04 9 M26	\$41 \$6B \$17 \$49 \$04 \$1A \$39 \$37 \$37 \$32
1850PWM ECU8 SID\$09 INF\$04 9 M27	\$41 \$6B \$17 \$49 \$04 \$1B \$00 \$00 \$00 \$00
1850PWM ECU8 SID\$09 INF\$04 9 M28	\$41 \$6B \$17 \$49 \$04 \$1C \$00 \$00 \$00 \$00
1850PWM ECU8 SID\$09 INF\$04 9 M29	\$41 \$6B \$17 \$49 \$04 \$1D \$38 \$38 \$35 \$38
1850PWM ECU8 SID\$09 INF\$04 9 M30	\$41 \$6B \$17 \$49 \$04 \$1E \$39 \$37 \$37 \$31
1850PWM ECU8 SID\$09 INF\$04 9 M31	\$41 \$6B \$17 \$49 \$04 \$1F \$00 \$00 \$00 \$00
1850PWM ECU8 SID\$09 INF\$04 9 M32	\$41 \$6B \$17 \$49 \$04 \$20 \$00 \$00 \$00 \$00
1850PWM ECU8 SID\$09 INF\$04 9 M33	\$41 \$6B \$17 \$49 \$04 \$21 \$39 \$38 \$35 \$38
1850PWM ECU8 SID\$09 INF\$04 9 M34	\$41 \$6B \$17 \$49 \$04 \$22 \$39 \$37 \$37 \$30
1850PWM ECU8 SID\$09 INF\$04 9 M35	\$41 \$6B \$17 \$49 \$04 \$23 \$00 \$00 \$00 \$00
1850PWM ECU8 SID\$09 INF\$04 9 M36	\$41 \$6B \$17 \$49 \$04 \$24 \$00 \$00 \$00 \$00
1850PWM ECU8 SID\$09 INF\$05 9	\$41 \$6B \$17 \$49 \$05 \$09
1850PWM ECU8 SID\$09 INF\$06 9 M1	\$41 \$6B \$17 \$49 \$06 \$01 \$87 \$91 \$BC \$88
1850PWM ECU8 SID\$09 INF\$06 9 M2	\$41 \$6B \$17 \$49 \$06 \$02 \$88 \$91 \$BC \$87
1850PWM ECU8 SID\$09 INF\$06 9 M3	\$41 \$6B \$17 \$49 \$06 \$03 \$89 \$91 \$BC \$86
1850PWM ECU8 SID\$09 INF\$06 9 M4	\$41 \$6B \$17 \$49 \$06 \$04 \$8A \$91 \$BC \$85
1850PWM ECU8 SID\$09 INF\$06 9 M5	\$41 \$6B \$17 \$49 \$06 \$05 \$8B \$91 \$BC \$84
1850PWM ECU8 SID\$09 INF\$06 9 M6	\$41 \$6B \$17 \$49 \$06 \$06 \$8C \$91 \$BC \$83
1850PWM ECU8 SID\$09 INF\$06 9 M7	\$41 \$6B \$17 \$49 \$06 \$07 \$8D \$91 \$BC \$82
1850PWM ECU8 SID\$09 INF\$06 9 M8	\$41 \$6B \$17 \$49 \$06 \$08 \$8E \$55 \$AA \$81
1850PWM ECU8 SID\$09 INF\$06 9 M9	\$41 \$6B \$17 \$49 \$06 \$09 \$8F \$FF \$00 \$80

**Figure 24 - Pre-defined messages for SAE J1850 PWM**

## 5.4.7.4 SAE J1850 PWM Message Timing

For convenience, the test cases in this document will use the text in the 'Parameter Reference' column when referring to the specific value, detailed in the 'Value' column of the table below. Unless otherwise specified, the following timing values shall be used:

Parameter Reference	Value
P2_MIN	0 ms
P2	25 ms
P2_MAX	100 ms

**Figure 25 - Pre-defined message timing for SAE J1850 PWM**

## 5.4.7.5 SAE J1850 PWM Connected State

The device shall initially be in the 'Power On' state, connected to the SAE J1850 network with the appropriate termination applied (as depicted in 5.4.7.1).

## 5.4.7.6 SAE J1850 PWM (2 ECUs) Initialized State

The OBD network shall have determined to be SAE J1850 PWM at a baud rate of 41600 with two OBD II ECUs having been identified. The DUT may produce a periodic message transmission to keep the network alive. A successful completion of 0 shall define this state.

## 5.4.7.7 SAE J1850 PWM (8 ECUs) Initialized State

The OBD network shall have determined to be SAE J1850 PWM at a baud rate of 41600 with eight OBD II ECUs having been identified. The DUT may produce a periodic message transmission to keep the network alive. A successful completion of 8.6.2 shall define this state.

## 5.4.7.8 SAE J1850 PWM (2 ECUs) SID9 Ready State

The SID \$09 INFOTYPES shall be known for each of the two OBD II ECUs on the network. The DUT may produce a periodic message transmission to keep the network alive. A successful completion of 9.6.17 shall define this state.

## 5.4.7.9 SAE J1850 PWM (2 ECUs) SID9 Diesel Ready State

The SID \$09 INFOTYPES shall be known for each of the two OBD II ECUs on the network. The DUT may produce a periodic message transmission to keep the network alive. A successful completion of 1.1.1 shall define this state.

## 5.4.7.10 SAE J1850 PWM (8 ECUs) SID9 Ready State

The SID \$09 INFOTYPES shall be known for each of the eight OBD II ECUs on the network. The DUT may produce a periodic message transmission to keep the network alive. A successful completion of 0 shall define this state.

## 6. MECHANICAL EVALUATION

This section details test cases that will verify the mechanical attributes of the DUT.

## 6.1 General Tests

These are general mechanical tests, which are not specific to any OBD II network.

## 6.1.1 Verify Mechanical Dimensions for SAE J1962 Connector Head

## 6.1.2 Verify SAE J1962 Cable Length

## 7. ELECTRICAL EVALUATION

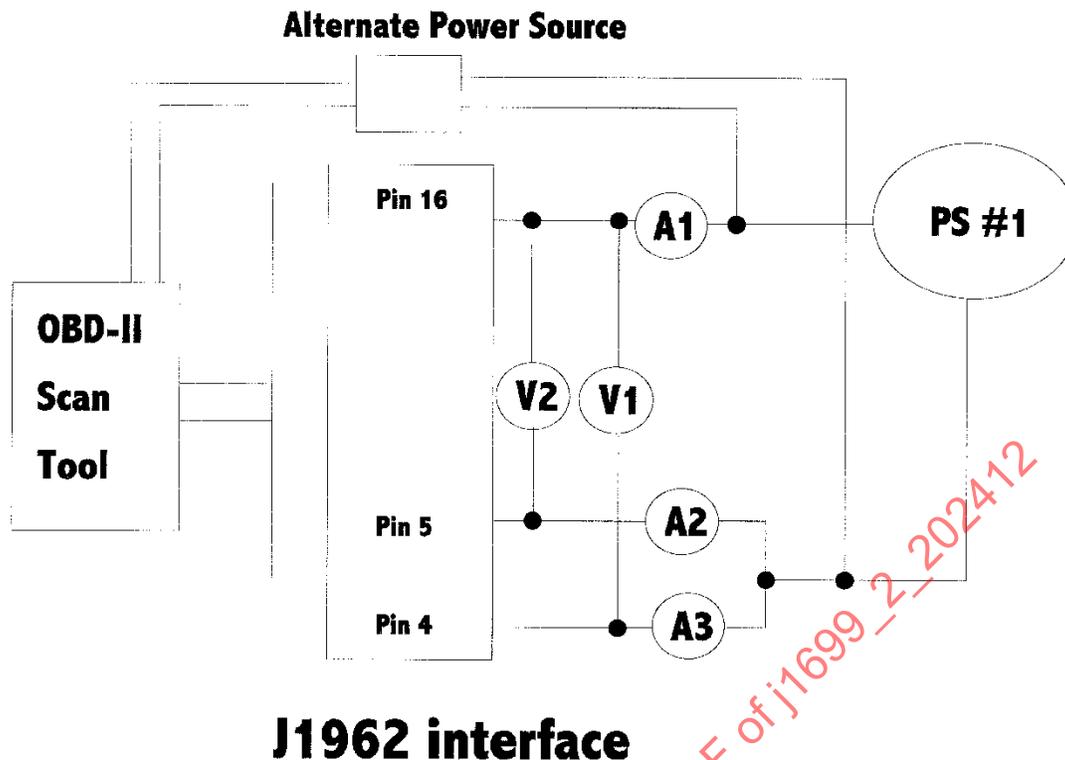
This section details test cases that will verify the electrical attributes of the DUT.

## 7.1 General Tests

These are general electrical tests, which are not specific to any OBD II protocol.

## 7.1.1 Verify the Current Draw of DUT

<b>Test ID:</b>	\$10002000
<b>Purpose:</b>	The purpose of this test is to verify that the DUT will not exceed the maximum 4.0 A current limit over the 8.0 to 18.0 voltage range.
<b>Rationale:</b>	This test will confirm that the DUT never draws too much power.
<b>Set Up:</b>	Connect the DUT to the variable power supply (PS #1) as outlined in Figure 26. The positive terminal of PS #1 shall be connected to pin 16 on the DUT's SAE J1962 connector. The negative terminal of PS #1 shall be connected to pins 4 and 5 on the DUT's SAE J1962 connector. If the DUT requires an external power source (other than the SAE J1962 connector), be sure to connect it and verify that it is providing power to the DUT.
<b>Procedure:</b>	
<u>ACTION- INST</u>	Set the PS #1 to 8 V DC, then measure the voltage V1 and V2 as well as current A1, A2, and A3.
<u>EVALUATION</u>	Pass if all of the following conditions are true: 8.0 V DC <= V1 <= 18.0 V DC, 8.0 V DC <= V2 <= 18.0 V DC, A1 <= 4.0 A, A2 <= 1.5 A, A3 <= 4.0 A.
<u>ACTION- INST</u>	Set the PS #1 to 12.5 V DC, then measure the voltage V1 and V2 as well as current A1, A2, and A3.
<u>EVALUATION</u>	Pass if all of the following conditions are true: 8.0 V DC <= V1 <= 18.0 V DC, 8.0 V DC <= V2 <= 18.0 V DC, A1 <= 4.0 A, A2 <= 1.5 A, A3 <= 4.0 A.
<u>ACTION- INST</u>	Set the PS #1 to 18 V DC, then measure the voltage V1 and V2 as well as current A1, A2, and A3.
<u>EVALUATION</u>	Pass if all of the following conditions are true: 8.0 V DC <= V1 <= 18.0 V DC, 8.0 V DC <= V2 <= 18.0 V DC, A1 <= 4.0 A, A2 <= 1.5 A, A3 <= 4.0 A.



*Figure 26 - DUT current draw validation circuit*

## 7.2 ISO 15765-4 Tests

These electrical tests are specific to ISO 15765-4.

### 7.2.1 Verify Network Termination

<b>Test ID:</b>	\$10104000
<b>Purpose:</b>	The purpose of this test is to determine if the network has the specified termination.
<b>Rationale:</b>	This test will confirm that there is no termination being applied by the DUT.
<b>Set Up:</b>	The DUT shall be in the "ISO 15765 (11-bit, 500K) Connected State" as defined in 5.4.3.7 with the ISO 15765-4 network connected to the ECUSIM. The ECUSIM shall be in the "ISO 15765 (11-bit, 500K) Connected State" as defined in 5.4.3.7.
<b>Procedure:</b>	
<u>ACTION- INST</u>	Measure the resistance between pin 6 (CAN H) and pin 14 (CAN L) on the DUT's SAE J1962 connector.
<u>EVALUATION</u>	Pass if the resistance measurement is 500K Ohm or greater.

## 7.2.2 Verify Capacitance

<b>Test ID:</b>	\$10103000
<b>Purpose:</b>	The purpose of this test is to determine the capacitance applied by the DUT.
<b>Rationale:</b>	This test will confirm that the DUT is providing the correct amount of capacitance
<b>Set Up:</b>	The DUT shall be in the “ISO 15765 (11-bit, 500K) Connected State” as defined in 5.4.3.7 with the ISO 15765-4 network connected to the ECUSIM. The ECUSIM shall be in the “ISO 15765 (11-bit, 500K) Connected State” as defined in 5.4.3.7.
<b>Procedure:</b>	
<u>ACTION- INST</u>	Measure the capacitance between pin 5 (Signal Ground) and pin 6 (CAN H) on the DUT’s SAE J1962 connector.
<u>EVALUATION</u>	Pass if the value is less than 100pF.
<u>ACTION- INST</u>	Measure the capacitance between pin 5 (Signal Ground) and pin 14 (CAN L) on the DUT’s SAE J1962 connector.
<u>EVALUATION</u>	Pass if the value is less than 100pF.
<u>ACTION- INST</u>	Measure the capacitance between pin 6 (CAN H) and pin 14 (CAN L) on the DUT’s SAE J1962 connector.
<u>EVALUATION</u>	Pass if the value is less than 50pF.

## 7.3 ISO 14230-4 Tests

These electrical tests are specific to ISO 14230-4.

## 7.3.1 Verify Network Termination

<b>Test ID:</b>	\$10204000
<b>Purpose:</b>	The purpose of this test is to determine if the network has the specified termination.
<b>Rationale:</b>	This test will confirm that the DUT is providing a pull-up to vehicle battery.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 14230-4 (2 ECUs) Initialized State”, which is detailed in 5.4.4.6.
<b>Procedure:</b>	
<u>ACTION- INST</u>	Measure the voltage between pin 7 (K Line) and pin 4 (chassis ground) on the DUT’s SAE J1962 connector.
<u>EVALUATION</u>	Pass if the voltage is greater than 90% of vehicle battery.
<u>ACTION- INST</u>	Measure the voltage between pin 15 (K Line) and pin 4 (chassis ground) on the DUT’s SAE J1962 connector.
<u>EVALUATION</u>	Pass if the voltage is greater than 90% of vehicle battery.

## 7.3.2 Verify Capacitance

<b>Test ID:</b>	\$10203000
<b>Purpose:</b>	The purpose of this test is to determine the capacitance applied by the DUT.
<b>Rationale:</b>	This test will confirm that the DUT is providing the correct amount of capacitance
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 14230-4 (2 ECUs) Initialized State”, which is detailed in 5.4.4.6.
<b>Procedure:</b>	
<u>ACTION- INST</u>	Measure the capacitance between pin 5 (Signal Ground) and pin 7 (K Line) on the DUT's SAE J1962 connector.
<u>EVALUATION</u>	Pass if the value is less than 2nF.
<u>ACTION- INST</u>	Measure the capacitance between pin 5 (Signal Ground) and pin 15 (L Line) on the DUT's SAE J1962 connector.
<u>EVALUATION</u>	Pass if the value is less than 2nF.

## 7.4 ISO 9141-2 Tests

These electrical tests are specific to ISO 9141-2.

## 7.4.1 Verify Network Termination

<b>Test ID:</b>	\$10304000
<b>Purpose:</b>	The purpose of this test is to determine if the network has the specified termination.
<b>Rationale:</b>	This test will confirm that the DUT is providing a pull-up to vehicle battery.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 9141-2 (2 ECUs) Initialized State”, which is detailed in 5.4.5.6.
<b>Procedure:</b>	
<u>ACTION- INST</u>	Measure the voltage between pin 7 (K Line) and pin 4 (chassis ground) on the DUT's SAE J1962 connector.
<u>EVALUATION</u>	Pass if the voltage is greater than 90% of vehicle battery.
<u>ACTION- INST</u>	Measure the voltage between pin 15 (K Line) and pin 4 (chassis ground) on the DUT's SAE J1962 connector.
<u>EVALUATION</u>	Pass if the voltage is greater than 90% of vehicle battery.

## 7.4.2 Verify Capacitance

<b>Test ID:</b>	\$10304000
<b>Purpose:</b>	The purpose of this test is to determine the capacitance applied by the DUT.
<b>Rationale:</b>	This test will confirm that the DUT is providing the correct amount of capacitance
<b>Set Up:</b>	The DUT and ECUSIM shall be in the "ISO 9141-2 (2 ECUs) Initialized State", which is detailed in 5.4.5.6.
<b>Procedure:</b>	
<u>ACTION- INST</u>	Measure capacitance on the DUT's SAE J1962 connector.
<u>EVALUATION</u>	Pass if the value is less than 2nF

## 7.5 SAE J1850 VPW Tests

These electrical tests are specific to SAE J1850 VPW and are specified in SAE J1699-1.

## 7.6 SAE J1850 PWM Tests

These electrical tests are specific to SAE J1850 PWM and are specified in SAE J1699-1.

## 8. PROTOCOL EVALUATION

This section details test cases that will verify the protocol attributes (such as, correct signal timing, transport layer implementation, etc.) of the DUT.

## 8.1 General Tests

These are general protocol tests, which are not specific to any OBD II protocol.

SAENORM.COM : Click to view the full PDF of J1699\_2\_202412

## 8.1.1 Verify OBD II Initialization Sequence

<b>Test ID:</b>	\$20003000
<b>Purpose:</b>	The purpose of this test is to determine if the DUT is attempting to establish communications on ISO 9141-2, ISO 14230-4, J1850 PWM, J1850 VPW, ISO 15765-4 (11-bit, 500K), and ISO 15765-4 (29-bit, 500K). Where applicable, the appropriate delays shall be checked between attempts for protocols that use the same physical layers.
<b>Rationale:</b>	This test will confirm that all OBD interfaces are checked once per scan and that the polling rates do not exceed those specified ISO 15031-5.
<b>Set Up:</b>	The DUT shall be in the "Power On" state as defined in 5.4.1.1.  An oscilloscope shall be connected to the ISO 9141-2 network to measure the timing of voltage transitions during the initialization process.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the OBD II protocol scan and allow the DUT to make two passes through the protocol scan process.
<u>EVALUATION</u>	Pass if the following is true:  There was an attempt to do a K-Line 5-Baud Initialization (Verify that both K and L Lines at idle [logic '1'] for the time specified in <i>W5</i> . Then both K and L Lines transmit the byte \$33 at 5-bits per second $\pm 0.5\%$ .), ISO 14230-4 Fast Initialization (Verify that the K Line was at logic '1' for at least <i>W5</i> before changing to logic '0' for <i>TiniL</i> then returning to a logic '1' for the remainder of <i>Twup</i> . Then verify that the message <b>14230_START_COMM</b> was transmitted.) at 10.4k baud, J1850 PWM <b>1850PWM_SID\$01_PID\$00</b> message was transmitted at 41.6k baud, J1850 VPW <b>1850VPW_SID\$01_PID\$00</b> message was transmitted at 10.4k baud, ISO 15765-4 (11-bit) <b>15765_11_SID\$01_PID\$00</b> message was transmitted at 500K baud, and ISO 15765-4 (29-bit) <b>15765_29_SID\$01_PID\$00</b> message was transmitted at 500K baud - at least twice for each protocol, in no specific order, without user intervention.  There was at least a 2.6 second delay (2 seconds for the address at 5 baud + <i>W1</i> + <i>W5</i> ) between start of ISO 14230-4 Fast Initialization and the start of K-Line 5-Baud Initialization.

**Figure 27 - Verify OBD II initialization sequence**

## 8.2 ISO 15765-4 Tests

These protocol tests are specific to ISO 15765-4. Unless otherwise specified, all references to Electrical Connections, ECU Addresses, Messages, Message Timing, and pre-defined states will be as described in 5.4.3.

## 8.2.1 Test BS and STMIN are 0 (11-bit, 500K)

<b>Test ID:</b>	\$20102000
<b>Purpose:</b>	The purpose of this test is to verify that the DUT issues the correct flow control message in response to a previous request.
<b>Rationale:</b>	This test will use positive responses from a single ECU.
<b>Set Up:</b>	The DUT shall be in the "ISO 15765 (11-bit, 500K) Connected State" as defined in 5.4.3.7 with the ISO 15765-4 network connected to the ECUSIM. The ECUSIM shall be in the "CAN (11-bit, 500K) Connected State" as defined in 5.4.2.4.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$09 INF \$04 (CALID).
<u>EVALUATION</u>	Pass if the <b>CAN_11_SID\$09_INF\$04</b> message was transmitted
<u>ACTION- ECUSIM</u>	After the <i>P2</i> delay, transmit the message <b>CAN_11_ECU1_SID\$09_INF\$04_FF</b> .
<u>EVALUATION</u>	Pass if the transmission is successful.
<u>ACTION- DUT</u>	In response to the ECUSIM First Frame message, the DUT should automatically issue an ISO 15765 Flow Control frame.
<u>EVALUATION</u>	Pass if the following is true:  The <b>CAN_11_FC_BS0_STMIN0</b> message was transmitted within 1000mS of the successful transmission of the <b>CAN_11_ECU1_SID\$09_INF\$04_FF</b> message.  The DUT recognizes that the message transmission was aborted and automatically recovers.

**Figure 28 - Test ISO 15765-4 protocol BS and STMIN are 0 (11-bit, 500K)**

## 8.2.2 Verify Protocol Initialization (11-bit, 500K)

<b>Test ID:</b>	\$20103000
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can successfully perform an OBD II initialization on the ISO 15765-4 network with 11-bit CAN Identifiers.
<b>Rationale:</b>	This test will use positive responses from two different ECUs.
<b>Set Up:</b>	The DUT shall be in the “ISO 15765 (11-bit, 500K) Connected State” as defined in 5.4.3.7 with the ISO 15765-4 network connected to the ECUSIM. The ECUSIM shall be in the “ISO 15765 (11-bit, 500K) Connected State” as defined in 5.4.3.7.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the OBD II protocol scan.
<u>EVALUATION</u>	Pass if the <b>15765_11_SID\$01_PID\$00</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2</i> delay, transmit the message ISO15765_11_ <b>ECU1_SID\$01_PID\$00</b> , delay <i>P2</i> , then transmit the message <b>15765_11_ECU2_SID\$01_PID\$00</b> .
<u>EVALUATION</u>	Pass if transmission is successful and the DUT identifies the OBD II protocol as ISO 15765-4 with 2 ECUs.

**Figure 29 - Verify ISO 15765-4 protocol initialization (11-bit, 500K)**

## 8.2.3 Verify Protocol Initialization (29-bit, 500K)

<b>Test ID:</b>	\$20103001
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can successfully perform an OBD II initialization on the ISO 15765-4 network with 29-bit CAN Identifiers.
<b>Rationale:</b>	This test will use positive responses from two different ECUs.
<b>Set Up:</b>	The DUT shall be in the “ISO 15765 (29-bit, 500K) Connected State” as defined in 5.4.3.8 with the ISO 15765-4 network connected to the ECUSIM. The ECUSIM shall be in the “ISO 15765 (29-bit, 500K) Connected State” as defined in 5.4.3.8.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the OBD II protocol scan.
<u>EVALUATION</u>	Pass if the <b>15765_29_SID\$01_PID\$00</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2</i> delay, transmit the message <b>15765_29_ECU1_SID\$01_PID\$00</b> , delay <i>P2</i> , then transmit the message <b>15765_29_ECU2_SID\$01_PID\$00</b> .
<u>EVALUATION</u>	Pass if transmission is successful and the DUT identifies the OBD II protocol as ISO 15765-4 with 2 ECUs.

**Figure 30 - Verify ISO 15765-4 protocol initialization (29-bit, 500K)**

## 8.2.4 Verify Protocol Initialization with 8 ECUs (11-bit, 500K)

<b>Test ID:</b>	\$20103002
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can successfully perform an OBD II initialization on the ISO 15765-4 network with 11-bit CAN Identifiers when 8 ECUs are present.
<b>Rationale:</b>	This test will use positive responses from eight different ECUs with ECU3 through ECU8 indicating that no SID \$01 PIDs are supported.
<b>Set Up:</b>	The DUT shall be in the "ISO 15765 (11-bit, 500K) Connected State" as defined in 5.4.3.7 with the ISO 15765-4 network connected to the ECUSIM. The ECUSIM shall be in the "ISO 15765 (11-bit, 500K) Connected State" as defined in 5.4.3.7.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the OBD II protocol scan.
<u>EVALUATION</u>	Pass if the <b>15765_11_SID\$01_PID\$00</b> message was transmitted
<u>ACTION- ECUSIM</u>	
	Delay <i>P2_MIN</i> , then transmit the message <b>15765_11_ECU1_SID\$01_PID\$00</b> .
	Delay <i>P2_MIN</i> , then transmit the message <b>15765_11_ECU2_SID\$01_PID\$00</b> .
	Delay <i>P2_MIN</i> , then transmit the message <b>15765_11_ECU3_SID\$01_PID\$00</b> .
	Delay <i>P2_MIN</i> , then transmit the message <b>15765_11_ECU4_SID\$01_PID\$00</b> .
	Delay <i>P2_MIN</i> , then transmit the message <b>15765_11_ECU5_SID\$01_PID\$00</b> .
	Delay <i>P2_MIN</i> , then transmit the message <b>15765_11_ECU6_SID\$01_PID\$00</b> .
	Delay <i>P2_MIN</i> , then transmit the message <b>15765_11_ECU7_SID\$01_PID\$00</b> .
	Delay <i>P2_MIN</i> , then transmit the message <b>15765_11_ECU8_SID\$01_PID\$00</b> .
<u>EVALUATION</u>	Pass if transmission is successful and the DUT identifies the OBD II protocol as ISO 15765-4 with 8 ECUs.

**Figure 31 - Verify ISO 15765-4 protocol initialization with 8 ECUs (11-bit, 500K)**

## 8.2.5 Verify Protocol Initialization with 8 ECUs (29-bit, 500K)

<b>Test ID:</b>	\$20103003
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can successfully perform an OBD II initialization on the ISO 15765-4 network with 29-bit CAN Identifiers when 8 ECUs are present.
<b>Rationale:</b>	This test will use positive responses from eight different ECUs with ECU3 through ECU8 indicating that no SID \$01 PIDs are supported.
<b>Set Up:</b>	The DUT shall be in the "ISO 15765 (29-bit, 500K) Connected State" as defined in 5.4.3.8 with the ISO 15765-4 network connected to the ECUSIM. The ECUSIM shall be in the "ISO 15765 (29-bit, 500K) Connected State" as defined in 5.4.3.8.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the OBD II protocol scan.
<u>EVALUATION</u>	Pass if the <b>15765_29_SID\$01_PID\$00</b> message was transmitted.
<u>ACTION- ECUSIM</u>	Delay <i>P2_MIN</i> , then transmit the message <b>15765_29_ECU1_SID\$01_PID\$00</b> . Delay <i>P2_MIN</i> , then transmit the message <b>15765_29_ECU2_SID\$01_PID\$00</b> . Delay <i>P2_MIN</i> , then transmit the message <b>15765_29_ECU3_SID\$01_PID\$00</b> . Delay <i>P2_MIN</i> , then transmit the message <b>15765_29_ECU4_SID\$01_PID\$00</b> . Delay <i>P2_MIN</i> , then transmit the message <b>15765_29_ECU5_SID\$01_PID\$00</b> . Delay <i>P2_MIN</i> , then transmit the message <b>15765_29_ECU6_SID\$01_PID\$00</b> . Delay <i>P2_MIN</i> , then transmit the message <b>15765_29_ECU7_SID\$01_PID\$00</b> . Delay <i>P2_MIN</i> , then transmit the message <b>15765_29_ECU8_SID\$01_PID\$00</b> .
<u>EVALUATION</u>	Pass if transmission is successful and the DUT identifies the OBD II protocol as ISO 15765-4 with 8 ECUs.

**Figure 32 - Verify ISO 15765-4 protocol initialization with 8 ECUs (29-bit, 500K)**

## 8.2.6 Test Protocol Initialization with No Acknowledgement (11-bit, 500K)

<b>Test ID:</b>	\$20103004
<b>Purpose:</b>	The purpose of this test is to verify that the DUT will retry transmission for at least 25 ms, the time needed to wake up a deactivated/sleeping module or win the arbitration, when a CAN frame is unacknowledged.
<b>Rationale:</b>	This test will not provide any ECU responses.
<b>Set Up:</b>	The DUT shall be in the "ISO 15765 (11-bit, 500K) Connected State" as defined in 5.4.3.7 with the ISO 15765-4 network connected to the ECUSIM. The ECUSIM shall be in the "ISO 15765 (11-bit, 500K) Monitor State" as defined in 5.4.3.5.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the OBD II protocol scan.
<u>EVALUATION</u>	Pass if the following is true:  The <b>15765_11_SID\$01_PID\$00</b> message was re-transmitted approximately every 260 ms (average transmission time of a single CAN-frame with 11-bit CAN-ID, DLC=8 at 500K Baud) on the bus. The time between the first transmission and the last retry from the DUT is $\geq$ 25 ms (approx. 96 CAN-frames).

**Figure 33 - Test ISO 15765-4 protocol initialization with no acknowledgement (11-bit, 500K)**

SAENORM.COM : Click to view the full PDF of J1699\_2\_202412

## 8.2.7 Verify Protocol Initialization with Delayed Acknowledgement (11-bit, 500K)

<b>Test ID:</b>	\$20103005
<b>Purpose:</b>	The purpose of this test is to verify that the DUT will successfully initialize even when there are unacknowledged CAN frames.
<b>Rationale:</b>	This test will delay (with no In-Frame Acknowledgements) before using positive responses from two different ECUs.
<b>Set Up:</b>	The DUT shall be in the “ISO 15765 (11-bit, 500K) Connected State” as defined in 5.4.3.7 with the ISO 15765-4 network connected to the ECUSIM. The ECUSIM shall be in the “ISO 15765 (11-bit, 500K) Monitor State” as defined in 5.4.3.5.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the OBD II protocol scan.
<u>EVALUATION</u>	Pass if the following is true:  The <b>15765_11_SID\$01_PID\$00</b> message was re-transmitted approximately every 260 ms (average transmission time of a single CAN-frame with 11-bit CAN-ID, DLC=8 at 500K Baud) on the bus.
<u>ACTION- ECUSIM</u>	After a delay of 20 ms, the ECUSIM shall begin generating In-Frame Acknowledgements, delay <i>P2</i> , transmit the message <b>15765_11_ECU1_SID\$01_PID\$00</b> , delay <i>P2</i> , then transmit the message <b>15765_11_ECU2_SID\$01_PID\$00</b> .
<u>EVALUATION</u>	Pass if transmission is successful and the DUT identifies the OBD II protocol as ISO 15765-4 with 2 ECUs.

**Figure 34 - Verify ISO 15765-4 protocol initialization with delayed acknowledgement (11-bit, 500K)**

## 8.2.8 Test Protocol Initialization with Error Frame Detected (11-bit, 500K)

<b>Test ID:</b>	\$20103006
<b>Purpose:</b>	The purpose of this test is to verify that the DUT will detect an error during transmission of a CAN frame and disconnect itself from the bus.
<b>Rationale:</b>	This test will generate an error condition on the ISO 15765-4 network.
<b>Set Up:</b>	The DUT shall be in the “ISO 15765 (11-bit, 500K) Connected State” as defined in 5.4.3.7 with the ISO 15765-4 network connected to the ECUSIM. The ECUSIM shall be in the “ISO 15765 (11-bit, 250K) Monitor State” as defined in 5.4.3.5.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the OBD II protocol scan.
<u>EVALUATION</u>	Pass if the following is true:  The <b>15765_11_SID\$01_PID\$00</b> message stopped transmission after an error frame was transmitted on the bus.

**Figure 35 - Test ISO 15765-4 protocol initialization with error frame detected (11-bit, 500K)**

## 8.2.9 Verify Protocol Initialization with NRC=\$21 (11-bit, 500K)

<b>Test ID:</b>	\$20103007
<b>Purpose:</b>	The purpose of this test is to verify that the DUT will successfully initialize when there are 5 NRC of \$21 in a row.
<b>Rationale:</b>	This test will use a positive response from the first ECU and a negative 'busy' response from a second ECU; five times in a row.
<b>Set Up:</b>	The DUT shall be in the "ISO 15765 (11-bit, 500K) Connected State" as defined in 5.4.3.7 with the ISO 15765-4 network connected to the ECUSIM. The ECUSIM shall be in the "ISO 15765 (11-bit, 500K) Connected State" as defined in 5.4.3.7.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the OBD II protocol scan.
<u>EVALUATION</u>	Pass if the <b>15765_11_SID\$01_PID\$00</b> message was transmitted.
<u>ACTION- ECUSIM</u>	Delay P2, then transmit the <b>ISO15765_11_ECU2_SID\$01_PID\$00</b> message, delay P2, then transmit the <b>ISO15765_11_ECU1_SID\$01_NRC\$21</b> message.
<u>EVALUATION</u>	Pass if the messages were successfully transmitted.
<u>ACTION- DUT</u>	The DUT re-transmits the <b>15765_11_SID\$01_PID\$00</b> message.
<u>EVALUATION</u>	Pass if the message was transmitted $\geq$ 200 ms after the previous response.
<u>ACTION- ECUSIM</u>	Delay P2, then transmit the <b>15765_11_ECU2_SID\$01_PID\$00</b> message, delay P2, then transmit the <b>15765_11_ECU1_SID\$01_NRC\$21</b> message.
<u>EVALUATION</u>	Pass if the messages were successfully transmitted.
<u>ACTION- DUT</u>	The DUT re-transmits the <b>15765_11_SID\$01_PID\$00</b> message.
<u>EVALUATION</u>	Pass if the message was transmitted $\geq$ 200 ms after the previous response.
<u>ACTION- ECUSIM</u>	Delay P2, then transmit the <b>15765_11_ECU2_SID\$01_PID\$00</b> message, delay P2, then transmit the <b>15765_11_ECU1_SID\$01_NRC\$21</b> message.
<u>EVALUATION</u>	Pass if the messages were successfully transmitted.
<u>ACTION- DUT</u>	The DUT re-transmits the <b>15765_11_SID\$01_PID\$00</b> message.
<u>EVALUATION</u>	Pass if the message was transmitted $\geq$ 200 ms after the previous response.
<u>ACTION- ECUSIM</u>	Delay P2, then transmit the <b>15765_11_ECU2_SID\$01_PID\$00</b> message, delay P2, then transmit the <b>15765_11_ECU1_SID\$01_NRC\$21</b> message.
<u>EVALUATION</u>	Pass if the messages were successfully transmitted.
<u>ACTION- DUT</u>	The DUT re-transmits the <b>15765_11_SID\$01_PID\$00</b> message.
<u>EVALUATION</u>	Pass if the message was transmitted $\geq$ 200 ms after the previous response.
<u>ACTION- ECUSIM</u>	Delay P2, then transmit the <b>15765_11_ECU2_SID\$01_PID\$00</b> message, delay P2, then transmit the <b>15765_11_ECU1_SID\$01_NRC\$21</b> message.

<u>EVALUATION</u>	Pass if the messages were successfully transmitted.
<u>ACTION- DUT</u>	The DUT re-transmits the <b>15765_11_SID\$01_PID\$00</b> message.
<u>EVALUATION</u>	Pass if the message was transmitted $\geq$ 200 ms after the previous response.
<u>ACTION- ECUSIM</u>	Delay <i>P2</i> , then transmit the <b>15765_11_ECU2_SID\$01_PID\$00</b> message, delay <i>P2</i> , then transmit the <b>15765_11_ECU1_SID\$01_PID\$00</b> message.
<u>EVALUATION</u>	Pass if the messages were successfully transmitted.
<u>ACTION- DUT</u>	The DUT stops transmitting the <b>15765_11_SID\$01_PID\$00</b> message.
<u>EVALUATION</u>	The DUT identifies the OBD II protocol as ISO 15765-4 with 2 ECUs.

**Figure 36 - Test ISO 15765-4 protocol initialization with NRC=\$21 (11-bit, 500K)**

### 8.3 ISO 14230-4 Tests

These protocol tests are specific to ISO 14230-4. Unless otherwise specified, all references to Electrical Connections, ECU Addresses, Messages, Message Timing, and pre-defined states will be as described in 5.4.3.13.

#### 8.3.1 Verify Bit Times for 10400 Baud Communications

<b>Test ID:</b>	\$20201000
<b>Purpose:</b>	The purpose of this test is to verify that the bit times on the ISO 14230-4 network when the DUT is transmitting at 10400 baud meet the signal and communication specification set forth in ISO 14230-1.
<b>Rationale:</b>	THIS TEST WILL BE ADDED AT A FUTURE DATE.

**Figure 37 - Verify ISO 14230-4 bit times for 10400 baud communications**

## 8.3.2 Verify 5-Baud Initialization with Key Bytes \$8FE9 (2 ECUs)

<b>Test ID:</b>	\$20203000
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can successfully perform a 5-baud OBD II initialization on the ISO 14230-4 network and communicate at 10400 baud when the Key Bytes \$E9 \$8F are returned.
<b>Rationale:</b>	This test will use a positive response from the first ECU with the Key Bytes \$E9 \$8F, then two ECUs will respond positively to the SID \$01 PID \$00 request.
<b>Set Up:</b>	The DUT shall be in the "ISO 14230-4 Connected State" as defined in 5.4.4.5 with the ISO 14230-4 network connected to the ECUSIM. The ECUSIM shall be in the "ISO 14230-4 Connected State" as defined in 5.4.4.5.  An oscilloscope shall be connected to the ISO 14230-4 network to measure the timing of voltage transitions during the initialization process.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	The DUT starts the ISO 14230-4 5-baud initialization sequence.
<u>EVALUATION</u>	Pass if the following events occurred in this order: Both K and L Lines at idle (logic '1') for the time specified in <i>W5</i> . Both K and L Lines transmit the byte \$33 at 5-bits per second $\pm 0.5\%$ .
<u>ACTION- ECUSIM</u>	Using 10400 baud from now on, delay <i>W1</i> , transmit the byte \$55, delay <i>W2</i> , transmit \$E9, delay <i>W3</i> , transmit \$8F.
<u>EVALUATION</u>	Pass if \$70 is received in no less than <i>W4_MIN</i> and no greater than <i>W4_MAX</i> .
<u>ACTION- ECUSIM</u>	After the <i>W4</i> delay, transmit the byte \$CC.
<u>EVALUATION</u>	Pass if transmission is successful.
<u>ACTION- DUT</u>	The DUT issues the Service \$01 PID \$00 request.
<u>EVALUATION</u>	Pass if the message <b>14230_SID\$01_PID\$00</b> was transmitted (at 10400 baud) over the K Line with inter-byte times of at least <i>P4_MIN</i> and no greater than <i>P4_MAX</i> .
<u>ACTION- ECUSIM</u>	After the <i>P2</i> delay, transmit the message <b>14230_ECU1_SID\$01_PID\$00</b> , delay <i>P2</i> , then transmit the message <b>14230_ECU2_SID\$01_PID\$00</b> .
<u>EVALUATION</u>	Pass if transmission was successful and the DUT identifies the OBD II protocol as ISO 14230-4 with 2 ECUs.

**Figure 38 - Verify ISO 14230-4 5-baud initialization with key bytes \$E9 \$8F and 2 ECUs**

## 8.3.3 Verify 5-Baud Initialization with Key Bytes \$8F6B (2 ECUs)

<b>Test ID:</b>	\$20203001
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can successfully perform a 5-baud OBD II initialization on the ISO 14230-4 network and communicate at 10400 baud when the Key Bytes \$6B \$8F are returned.
<b>Rationale:</b>	This test will use a positive response from the first ECU with the Key Bytes \$6B \$8F, then two ECUs will respond positively to the SID \$01 PID \$00 request.
<b>Set Up:</b>	The DUT shall be in the "ISO 14230-4 Connected State" as defined in 5.4.4.5 with the ISO 14230-4 network connected to the ECUSIM. The ECUSIM shall be in the "ISO 14230-4 Connected State" as defined in 5.4.4.5.  An oscilloscope shall be connected to the ISO 14230-4 network to measure the timing of voltage transitions during the initialization process.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	The DUT starts the ISO 14230-4 5-baud initialization sequence.
<u>EVALUATION</u>	Pass if the following events occurred in this order: Both K and L Lines at idle (logic '1') for the time specified in <i>W5</i> . Both K and L Lines transmit the byte \$33 at 5-bits per second $\pm 0.5\%$ .
<u>ACTION- ECUSIM</u>	Using 10400 baud from now on, delay <i>W1</i> , transmit the byte \$55, delay <i>W2</i> , transmit \$6B, delay <i>W3</i> , transmit \$8F.
<u>EVALUATION</u>	Pass if \$70 is received in no less than <i>W4_MIN</i> and no greater than <i>W4_MAX</i> .
<u>ACTION- ECUSIM</u>	After the <i>W4</i> delay, transmit the byte \$CC.
<u>EVALUATION</u>	Pass if transmission is successful.
<u>ACTION- DUT</u>	The DUT issues the Service \$01 PID \$00 request.
<u>EVALUATION</u>	Pass if the message <b>14230_SID\$01_PID\$00</b> was transmitted (at 10400 baud) over the K Line with inter-byte times of at least <i>P4_MIN</i> and no greater than <i>P4_MAX</i> .
<u>ACTION- ECUSIM</u>	After the <i>P2</i> delay, transmit the message <b>14230_ECU1_SID\$01_PID\$00</b> , delay <i>P2</i> , then transmit the message <b>14230_ECU2_SID\$01_PID\$00</b> .
<u>EVALUATION</u>	Pass if transmission was successful and the DUT identifies the OBD II protocol as ISO 14230-4 with 2 ECUs.

**Figure 39 - Verify ISO 14230-4 5-baud initialization with key bytes \$6B \$8F and 2 ECUs**

## 8.3.4 Verify 5-Baud Initialization with Key Bytes \$8F6D (2 ECUs)

<b>Test ID:</b>	\$20203002
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can successfully perform a 5-baud OBD II initialization on the ISO 14230-4 network and communicate at 10400 baud when the Key Bytes \$6D \$8F are returned.
<b>Rationale:</b>	This test will use a positive response from the first ECU with the Key Bytes \$6D \$8F, then two ECUs will respond positively to the SID \$01 PID \$00 request.
<b>Set Up:</b>	The DUT shall be in the "ISO 14230-4 Connected State" as defined in 5.4.4.5 with the ISO 14230-4 network connected to the ECUSIM. The ECUSIM shall be in the "ISO 14230-4 Connected State" as defined in 5.4.4.5.  An oscilloscope shall be connected to the ISO 14230-4 network to measure the timing of voltage transitions during the initialization process.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	The DUT starts the ISO 14230-4 5-baud initialization sequence.
<u>EVALUATION</u>	Pass if the following events occurred in this order: Both K and L Lines at idle (logic '1') for the time specified in <i>W5</i> . Both K and L Lines transmit the byte \$33 at 5-bits per second $\pm 0.5\%$ .
<u>ACTION- ECUSIM</u>	Using 10400 baud from now on, delay <i>W1</i> , transmit the byte \$55, delay <i>W2</i> , transmit \$6D, delay <i>W3</i> , transmit \$8F.
<u>EVALUATION</u>	Pass if \$70 is received in no less than <i>W4_MIN</i> and no greater than <i>W4_MAX</i> .
<u>ACTION- ECUSIM</u>	After the <i>W4</i> delay, transmit the byte \$CC.
<u>EVALUATION</u>	Pass if transmission is successful.
<u>ACTION- DUT</u>	The DUT issues the Service \$01 PID \$00 request.
<u>EVALUATION</u>	Pass if the message <b>14230_SID\$01_PID\$00</b> was transmitted (at 10400 baud) over the K Line with inter-byte times of at least <i>P4_MIN</i> and no greater than <i>P4_MAX</i> .
<u>ACTION- ECUSIM</u>	After the <i>P2</i> delay, transmit the message <b>14230_ECU1_SID\$01_PID\$00</b> , delay <i>P2</i> , then transmit the message <b>14230_ECU2_SID\$01_PID\$00</b> .
<u>EVALUATION</u>	Pass if transmission was successful and the DUT identifies the OBD II protocol as ISO 14230-4 with 2 ECUs.

**Figure 40 - Verify ISO 14230-4 5-baud initialization with key bytes \$6D \$8F and 2 ECUs**

## 8.3.5 Verify 5-Baud Initialization with Key Bytes \$8FEF (2 ECUs)

<b>Test ID:</b>	\$20203003
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can successfully perform a 5-baud OBD II initialization on the ISO 14230-4 network and communicate at 10400 baud when the Key Bytes \$EF \$8F are returned.
<b>Rationale:</b>	This test will use a positive response from the first ECU with the Key Bytes \$EF \$8F, then two ECUs will respond positively to the SID \$01 PID \$00 request.
<b>Set Up:</b>	The DUT shall be in the "ISO 14230-4 Connected State" as defined in 5.4.4.5 with the ISO 14230-4 network connected to the ECUSIM. The ECUSIM shall be in the "ISO 14230-4 Connected State" as defined in 5.4.4.5.  An oscilloscope shall be connected to the ISO 14230-4 network to measure the timing of voltage transitions during the initialization process.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	The DUT starts the ISO 14230-4 5-baud initialization sequence.
<u>EVALUATION</u>	Pass if the following events occurred in this order: Both K and L Lines at idle (logic '1') for the time specified in <i>W5</i> . Both K and L Lines transmit the byte \$33 at 5-bits per second $\pm 0.5\%$ .
<u>ACTION- ECUSIM</u>	Using 10400 baud from now on, delay <i>W1</i> , transmit the byte \$55, delay <i>W2</i> , transmit \$EF, delay <i>W3</i> , transmit \$8F.
<u>EVALUATION</u>	Pass if \$70 is received in no less than <i>W4_MIN</i> and no greater than <i>W4_MAX</i> .
<u>ACTION- ECUSIM</u>	After the <i>W4</i> delay, transmit the byte \$CC.
<u>EVALUATION</u>	Pass if transmission is successful.
<u>ACTION- DUT</u>	The DUT issues the Service \$01 PID \$00 request.
<u>EVALUATION</u>	Pass if the message <b>14230_SID\$01_PID\$00</b> was transmitted (at 10400 baud) over the K Line with inter-byte times of at least <i>P4_MIN</i> and no greater than <i>P4_MAX</i> .
<u>ACTION- ECUSIM</u>	After the <i>P2</i> delay, transmit the message <b>14230_ECU1_SID\$01_PID\$00</b> , delay <i>P2</i> , then transmit the message <b>14230_ECU2_SID\$01_PID\$00</b> .
<u>EVALUATION</u>	Pass if transmission was successful and the DUT identifies the OBD II protocol as ISO 14230-4 with 2 ECUs.

**Figure 41 - Verify ISO 14230-4 fast initialization with key bytes \$EF \$8F and 2 ECUs**

## 8.3.6 Verify Fast Initialization with Key Bytes \$8FE9 (2 ECUs)

<b>Test ID:</b>	\$20203004
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can successfully perform a fast OBD II initialization on the ISO 14230-4 network and communicate at 10400 baud when the Key Bytes \$E9 \$8F are returned.
<b>Rationale:</b>	This test will use a positive response from the first ECU with the Key Bytes \$E9 \$8F, then two ECUs will respond positively to the SID \$01 PID \$00 request.
<b>Set Up:</b>	The DUT shall be in the "ISO 14230-4 Connected State" as defined in 5.4.4.5 with the ISO 14230-4 network connected to the ECUSIM. The ECUSIM shall be in the "ISO 14230-4 Connected State" as defined in 5.4.4.5.  An oscilloscope shall be connected to the ISO 14230-4 network to measure the timing of voltage transitions during the initialization process.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	The DUT starts the ISO 14230-4 fast initialization sequence.
<u>EVALUATION</u>	Pass if the following events occurred in this order: The K Line was at logic '1' for at least $W5$ before changing to logic '0' for $TiniL$ then returning to a logic '1' for the remainder of $Twup$ . The message <b>14230_START_COMM</b> was transmitted.
<u>ACTION- ECUSIM</u>	Delay $P2$ , then transmit the message <b>14230_ECU1_START_COMM_E98F</b> .
<u>EVALUATION</u>	Pass if transmission is successful.
<u>ACTION- DUT</u>	The DUT issues the Service \$01 PID \$00 request.
<u>EVALUATION</u>	Pass if the message <b>14230_SID\$01_PID\$00</b> was transmitted over the K Line with inter-byte times of at least $P4\_MIN$ and no greater than $P4\_MAX$ .
<u>ACTION- ECUSIM</u>	After the $P2$ delay, transmit the message <b>14230_ECU1_SID\$01_PID\$00</b> , delay $P2$ , then transmit the message <b>14230_ECU2_SID\$01_PID\$00</b> .
<u>EVALUATION</u>	Pass if transmission was successful and the DUT identifies the OBD II protocol as 14230-4 with 2 ECUs.

**Figure 42 - Verify ISO 14230-4 fast initialization with key bytes \$E9 \$8F and 2 ECUs**

## 8.3.7 Verify Fast Initialization with Key Bytes \$8F6B (2 ECUs)

<b>Test ID:</b>	\$20203005
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can successfully perform a fast OBD II initialization on the ISO 14230-4 network and communicate at 10400 baud when the Key Bytes \$6B \$8F are returned.
<b>Rationale:</b>	This test will use a positive response from the first ECU with the Key Bytes \$6B \$8F, then two ECUs will respond positively to the SID \$01 PID \$00 request.
<b>Set Up:</b>	The DUT shall be in the "ISO 14230-4 Connected State" as defined in 5.4.4.5 with the ISO 14230-4 network connected to the ECUSIM. The ECUSIM shall be in the "ISO 14230-4 Connected State" as defined in 5.4.4.5.  An oscilloscope shall be connected to the ISO 14230-4 network to measure the timing of voltage transitions during the initialization process.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	The DUT starts the ISO 14230-4 fast initialization sequence.
<u>EVALUATION</u>	Pass if the following events occurred in this order: The K Line was at logic '1' for at least $W5$ before changing to logic '0' for $TiniL$ then returning to a logic '1' for the remainder of $Twup$ . The message <b>14230_START_COMM</b> was transmitted.
<u>ACTION- ECUSIM</u>	Delay $P2$ , then transmit the message <b>14230_ECU1_START_COMM_6B8F</b> .
<u>EVALUATION</u>	Pass if transmission is successful.
<u>ACTION- DUT</u>	The DUT issues the Service \$01 PID \$00 request.
<u>EVALUATION</u>	Pass if the message <b>14230_SID\$01_PID\$00</b> was transmitted over the K Line with inter-byte times of at least $P4\_MIN$ and no greater than $P4\_MAX$ .
<u>ACTION- ECUSIM</u>	After the $P2$ delay, transmit the message <b>14230_ECU1_SID\$01_PID\$00</b> , delay $P2$ , then transmit the message <b>14230_ECU2_SID\$01_PID\$00</b> .
<u>EVALUATION</u>	Pass if transmission was successful and the DUT identifies the OBD II protocol as 14230-4 with 2 ECUs.

**Figure 43 - Verify ISO 14230-4 fast initialization with key bytes \$6B \$8F and 2 ECUs**

## 8.3.8 Verify Fast Initialization with Key Bytes \$8F6D (2 ECUs)

<b>Test ID:</b>	\$20203006
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can successfully perform a fast OBD II initialization on the ISO 14230-4 network and communicate at 10400 baud when the Key Bytes \$6D \$8F are returned.
<b>Rationale:</b>	This test will use a positive response from the first ECU with the Key Bytes \$6D \$8F, then two ECUs will respond positively to the SID \$01 PID \$00 request.
<b>Set Up:</b>	The DUT shall be in the "ISO 14230-4 Connected State" as defined in 5.4.4.5 with the ISO 14230-4 network connected to the ECUSIM. The ECUSIM shall be in the "ISO 14230-4 Connected State" as defined in 5.4.4.5.  An oscilloscope shall be connected to the ISO 14230-4 network to measure the timing of voltage transitions during the initialization process.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	The DUT starts the ISO 14230-4 fast initialization sequence.
<u>EVALUATION</u>	Pass if the following events occurred in this order: The K Line was at logic '1' for at least $W5$ before changing to logic '0' for $TiniL$ then returning to a logic '1' for the remainder of $Twup$ . The message <b>14230_START_COMM</b> was transmitted.
<u>ACTION- ECUSIM</u>	Delay $P2$ , then transmit the message <b>14230_ECU1_START_COMM_6D8F</b> .
<u>EVALUATION</u>	Pass if transmission is successful.
<u>ACTION- DUT</u>	The DUT issues the Service \$01 PID \$00 request.
<u>EVALUATION</u>	Pass if the message <b>14230_SID\$01_PID\$00</b> was transmitted over the K Line with inter-byte times of at least $P4\_MIN$ and no greater than $P4\_MAX$ .
<u>ACTION- ECUSIM</u>	After the $P2$ delay, transmit the message <b>14230_ECU1_SID\$01_PID\$00</b> , delay $P2$ , then transmit the message <b>14230_ECU2_SID\$01_PID\$00</b> .
<u>EVALUATION</u>	Pass if transmission was successful and the DUT identifies the OBD II protocol as 14230-4 with 2 ECUs.

**Figure 44 - Verify ISO 14230-4 fast initialization with key bytes \$6D \$8F and 2 ECUs**

## 8.3.9 Verify Fast Initialization with Key Bytes \$8FEF (2 ECUs)

<b>Test ID:</b>	\$20203007
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can successfully perform a fast OBD II initialization on the ISO 14230-4 network and communicate at 10400 baud when the Key Bytes \$EF \$8F are returned.
<b>Rationale:</b>	This test will use a positive response from the first ECU with the Key Bytes \$EF \$8F, then two ECUs will respond positively to the SID \$01 PID \$00 request.
<b>Set Up:</b>	The DUT shall be in the "ISO 14230-4 Connected State" as defined in 5.4.4.5 with the ISO 14230-4 network connected to the ECUSIM. The ECUSIM shall be in the "ISO 14230-4 Connected State" as defined in 5.4.4.5.  An oscilloscope shall be connected to the ISO 14230-4 network to measure the timing of voltage transitions during the initialization process.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	The DUT starts the ISO 14230-4 fast initialization sequence.
<u>EVALUATION</u>	Pass if the following events occurred in this order: The K Line was at logic '1' for at least $W5$ before changing to logic '0' for $TiniL$ then returning to a logic '1' for the remainder of $Twup$ . The message <b>14230_START_COMM</b> was transmitted.
<u>ACTION- ECUSIM</u>	Delay $P2$ , then transmit the message <b>14230_ECU1_START_COMM_EF8F</b> .
<u>EVALUATION</u>	Pass if transmission is successful.
<u>ACTION- DUT</u>	The DUT issues the Service \$01 PID \$00 request.
<u>EVALUATION</u>	Pass if the message <b>14230_SID\$01_PID\$00</b> was transmitted over the K Line with inter-byte times of at least $P4\_MIN$ and no greater than $P4\_MAX$ .
<u>ACTION- ECUSIM</u>	After the $P2$ delay, transmit the message <b>14230_ECU1_SID\$01_PID\$00</b> , delay $P2$ , then transmit the message <b>14230_ECU2_SID\$01_PID\$00</b> .
<u>EVALUATION</u>	Pass if transmission was successful and the DUT identifies the OBD II protocol as 14230-4 with 2 ECUs.

**Figure 45 - Verify ISO 14230-4 Fast initialization with Key Bytes \$EF \$8F and 2 ECUs**

## 8.3.10 Verify Fast Initialization with Key Bytes \$8FEF (8 ECUs)

<b>Test ID:</b>	\$20203008
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can successfully perform a fast OBD II initialization on the ISO 14230-4 network and communicate at 10400 baud when the Key Bytes \$EF \$8F are returned.
<b>Rationale:</b>	This test will use a positive response from the first ECU with the Key Bytes \$EF \$8F, then 8 ECUs will respond positively to the SID \$01 PID \$00 request with ECU3 through ECU8 indicating that no SID \$01 PIDs are supported.
<b>Set Up:</b>	The DUT shall be in the "ISO 14230-4 Connected State" as defined in 5.4.4.5 with the ISO 14230-4 network connected to the ECUSIM. The ECUSIM shall be in the "ISO 14230-4 Connected State" as defined in 5.4.4.5.  An oscilloscope shall be connected to the ISO 14230-4 network to measure the timing of voltage transitions during the initialization process.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	The DUT starts the ISO 14230-4 fast initialization sequence.
<u>EVALUATION</u>	Pass if the following events occurred in this order: The K Line was at logic '1' for at least $W5$ before changing to logic '0' for $TiniL$ then returning to a logic '1' for the remainder of $Twup$ . The message <b>14230_START_COMM</b> was transmitted.
<u>ACTION- ECUSIM</u>	Delay $P2$ , then transmit the message <b>14230_ECU1_START_COMM_EF8F</b> .
<u>EVALUATION</u>	Pass if transmission is successful.
<u>ACTION- DUT</u>	The DUT issues the Service \$01 PID \$00 request.
<u>EVALUATION</u>	Pass if the message <b>14230_SID\$01_PID\$00</b> was transmitted over the K Line with inter-byte times of at least $P4\_MIN$ and no greater than $P4\_MAX$ .
<u>ACTION- ECUSIM</u>	Delay $P2$ , then transmit the message <b>14230_ECU1_SID\$01_PID\$00</b> . Delay $P2$ , then transmit the message <b>14230_ECU2_SID\$01_PID\$00</b> . Delay $P2$ , then transmit the message <b>14230_ECU3_SID\$01_PID\$00</b> . Delay $P2$ , then transmit the message <b>14230_ECU4_SID\$01_PID\$00</b> . Delay $P2$ , then transmit the message <b>14230_ECU5_SID\$01_PID\$00</b> . Delay $P2$ , then transmit the message <b>14230_ECU6_SID\$01_PID\$00</b> . Delay $P2$ , then transmit the message <b>14230_ECU7_SID\$01_PID\$00</b> . Delay $P2$ , then transmit the message <b>14230_ECU8_SID\$01_PID\$00</b> .
<u>EVALUATION</u>	Pass if transmission was successful and the DUT identifies the OBD II protocol as 14230-4 with 8 ECUs.

**Figure 46 - Verify ISO 14230-4 fast initialization with key bytes \$EF \$8F and 8 ECUS**

## 8.3.11 Verify Fast Initialization with No OBD II Key Bytes (1 ECU)

<b>Test ID:</b>	\$20203009
<b>Purpose:</b>	The purpose of this test is to verify that the DUT will accept specific non-OBD II key bytes as valid on the ISO 14230-4 protocol. (This anomaly is detailed in SAE J1699-4.)
<b>Rationale:</b>	This test will use non-OBD II Key Bytes.
<b>Set Up:</b>	<p>The DUT shall be in the “ISO14230-4 Connected State” as defined in 5.4.4.5 with the ISO 14230-4 network connected to the ECUSIM. The ECUSIM shall be in the “ISO14230-4 Connected State” as defined in 5.4.4.5.</p> <p>An oscilloscope shall be connected to the ISO 14230-4 network to measure the timing of voltage transitions during the initialization process.</p>
<b>Procedure:</b>	
<u>ACTION- DUT</u>	The DUT starts the ISO 14230-4 Fast Initialization sequence.
<u>EVALUATION</u>	<p>Pass if the following events occurred in this order:  The K Line was at logic ‘1’ for at least <i>W5</i> before changing to logic ‘0’ for <i>TiniL</i> then returning to a logic ‘1’ for the remainder of <i>Twup</i>.  The message <b>14230_START_COMM</b> was transmitted.</p>
<u>ACTION- ECUSIM</u>	Delay <i>P2</i> , then transmit the message <b>14230_ECU1_START_COMM_NO_KB</b> .
<u>EVALUATION</u>	Pass if transmission is successful.
<u>ACTION- DUT</u>	The DUT issues the Service \$01 PID \$00 request.
<u>EVALUATION</u>	Pass if the message <b>14230_SID\$01_PID\$00</b> was transmitted over the K Line with inter-byte times of at least <i>P4_MIN</i> and no greater than <i>P4_MAX</i> .
<u>ACTION- ECUSIM</u>	Delay <i>P2</i> , then transmit the message <b>14230_ECU18_SID\$01_PID\$00</b> .
<u>EVALUATION</u>	Pass if transmission was successful and the DUT identifies the OBD II protocol as 14230-4 with 1 ECU.

**Figure 47 - Test ISO 14230-4 fast initialization with no OBD II key bytes and 1 ECU**

## 8.3.12 Verify Fast Initialization with Non-OBD II Key Bytes \$0000 (1 ECU)

<b>Test ID:</b>	\$20203009
<b>Purpose:</b>	The purpose of this test is to verify that the DUT will accept specific non-OBD II key bytes as valid on the ISO 14230-4 protocol. (This anomaly is detailed in SAE J1699-4.)
<b>Rationale:</b>	This test will use non-OBD II Key Bytes.
<b>Set Up:</b>	The DUT shall be in the “ISO14230-4 Connected State” as defined in 5.4.4.5 with the ISO 14230-4 network connected to the ECUSIM. The ECUSIM shall be in the “ISO14230-4 Connected State” as defined in 5.4.4.5.  An oscilloscope shall be connected to the ISO 14230-4 network to measure the timing of voltage transitions during the initialization process.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	The DUT starts the ISO 14230-4 Fast Initialization sequence.
<u>EVALUATION</u>	Pass if the following events occurred in this order: The K Line was at logic ‘1’ for at least <i>W5</i> before changing to logic ‘0’ for <i>TiniL</i> then returning to a logic ‘1’ for the remainder of <i>Twup</i> . The message <b>14230_START_COMM</b> was transmitted.
<u>ACTION- ECUSIM</u>	Delay <i>P2</i> , then transmit the message <b>14230_ECU1_START_COMM_0000</b> .
<u>EVALUATION</u>	Pass if transmission is successful.
<u>ACTION- DUT</u>	The DUT issues the Service \$01 PID \$00 request.
<u>EVALUATION</u>	Pass if the message <b>14230_SID\$01_PID\$00</b> was transmitted over the K Line with inter-byte times of at least <i>P4_MIN</i> and no greater than <i>P4_MAX</i> .
<u>ACTION- ECUSIM</u>	Delay <i>P2</i> , then transmit the message <b>14230_ECU18_SID\$01_PID\$00</b> .
<u>EVALUATION</u>	Pass if transmission was successful and the DUT identifies the OBD II protocol as 14230-4 with 1 ECU.

**Figure 48 - Test ISO 14230-4 fast initialization with non-OBD II key bytes \$00 \$00 and 1 ECU**

## 8.4 ISO 9141-2 Tests

These protocol tests are specific to ISO 9141-2. Unless otherwise specified, all references to Electrical Connections, ECU Addresses, Messages, Message Timing, and pre-defined states will be as described in 5.4.4.7.

## 8.4.1 Verify Bit Times for 10400 Baud Communications

<b>Test ID:</b>	\$20301000
<b>Purpose:</b>	The purpose of this test is to verify that the bit times on the ISO 9141-2 network when the DUT is transmitting at 10400 baud meet the signal and communication specification set forth in ISO 9141-2.
<b>Rationale:</b>	THIS TEST WILL BE ADDED AT A FUTURE DATE.

**Figure 49 - Verify ISO 9141-2 bit times for 10400 baud communications**

## 8.4.2 Verify 5-Baud Initialization with Key Bytes \$0808 (2 ECUs)

<b>Test ID:</b>	\$20303000
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can successfully perform a 5-baud OBD II initialization on the ISO 9141-2 network and communicate at 10400 baud when the Key Bytes \$08 \$08 are returned.
<b>Rationale:</b>	This test will use a positive response from the first ECU with the Key Bytes \$08 \$08, then two ECUs will respond positively to the SID \$01 PID \$00 request.
<b>Set Up:</b>	The DUT shall be in the “ISO9141-2 Connected State” as defined in 5.4.5.5 with the ISO 9141-2 network connected to the ECUSIM. The ECUSIM shall be in the “ISO9141-2 Connected State” as defined in 5.4.5.5.  An oscilloscope shall be connected to the ISO 9141-2 network to measure the timing of voltage transitions during the initialization process.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	The DUT starts the ISO 9141-2 5-baud initialization sequence.
<u>EVALUATION</u>	Pass if the following events occurred in this order: Verify that both K and L Lines at idle (logic ‘1’) for the time specified in W5. Verify that both K and L Lines transmit the byte \$33 at 5-bits per second $\pm 0.5\%$ .
<u>ACTION- ECUSIM</u>	Using 10400 baud from now on, delay W1, transmit the byte \$55, delay W2, transmit \$08, delay W3, transmit \$08.
<u>EVALUATION</u>	Pass if \$F7 is received in no less than W4_MIN and no greater than W4_MAX.
<u>ACTION- ECUSIM</u>	After the W4 delay, transmit the byte \$CC.
<u>EVALUATION</u>	Pass if transmission is successful.
<u>ACTION- DUT</u>	The DUT issues the Service \$01 PID \$00 request.
<u>EVALUATION</u>	Pass if the message <b>9141_SID\$01_PID\$00</b> was transmitted (at 10400 baud) over the K Line with inter-byte times of at least P4_MIN and no greater than P4_MAX.
<u>ACTION- ECUSIM</u>	After the P2 delay, transmit the message <b>9141_ECU1_SID\$01_PID\$00</b> , delay P2, then transmit the message <b>9141_ECU2_SID\$01_PID\$00</b> .
<u>EVALUATION</u>	Pass if transmission was successful and the DUT identifies the OBD II protocol as ISO 9141-2 with 2 ECUs.

**Figure 50 - Verify ISO 9141-2 5-baud initialization with key bytes \$08 \$08 and 2 ECUs**

## 8.4.3 Verify 5-Baud Initialization with Key Bytes \$0808 (8 ECUs)

<b>Test ID:</b>	\$20303001
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can successfully perform a 5-baud OBD II initialization on the ISO 9141-2 network and communicate at 10400 baud when the Key Bytes \$08 \$08 are returned.
<b>Rationale:</b>	This test will use a positive response from the first ECU with the Key Bytes \$08 \$08, then 8 ECUs will respond positively to the SID \$01 PID \$00 request with ECU3 through ECU8 indicating that no SID \$01 PIDs are supported.
<b>Set Up:</b>	The DUT shall be in the “ISO9141-2 Connected State” as defined in 5.4.5.5 with the ISO 9141-2 network connected to the ECUSIM. The ECUSIM shall be in the “ISO9141-2 Connected State” as defined in 5.4.5.5.  An oscilloscope shall be connected to the ISO 9141-2 network to measure the timing of voltage transitions during the initialization process.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	The DUT starts the ISO 9141-2 5-baud initialization sequence.
<u>EVALUATION</u>	Pass if the following events occurred in this order: Verify that both K and L Lines at idle (logic ‘1’) for the time specified in <i>W5</i> . Verify that both K and L Lines transmit the byte \$33 at 5-bits per second $\pm 0.5\%$ .
<u>ACTION- ECUSIM</u>	Using 10400 baud from now on, delay <i>W1</i> , transmit the byte \$55, delay <i>W2</i> , transmit \$08, delay <i>W3</i> , transmit \$08.
<u>EVALUATION</u>	Pass if \$F7 is received in no less than <i>W4 MIN</i> and no greater than <i>W4 MAX</i> .
<u>ACTION- ECUSIM</u>	After the <i>W4</i> delay, transmit the byte \$CC.
<u>EVALUATION</u>	Pass if transmission is successful.
<u>ACTION- DUT</u>	The DUT issues the Service \$01 PID \$00 request.
<u>EVALUATION</u>	Pass if the message <b>9141_SID\$01_PID\$00</b> was transmitted (at 10400 baud) over the K Line with inter-byte times of at least <i>P4 MIN</i> and no greater than <i>P4 MAX</i> .
<u>ACTION- ECUSIM</u>	Delay <i>P2</i> , then transmit the message <b>9141_ECU1_SID\$01_PID\$00</b> . Delay <i>P2</i> , then transmit the message <b>9141_ECU2_SID\$01_PID\$00</b> . Delay <i>P2</i> , then transmit the message <b>9141_ECU3_SID\$01_PID\$00</b> . Delay <i>P2</i> , then transmit the message <b>9141_ECU4_SID\$01_PID\$00</b> . Delay <i>P2</i> , then transmit the message <b>9141_ECU5_SID\$01_PID\$00</b> . Delay <i>P2</i> , then transmit the message <b>9141_ECU6_SID\$01_PID\$00</b> . Delay <i>P2</i> , then transmit the message <b>9141_ECU7_SID\$01_PID\$00</b> . Delay <i>P2</i> , then transmit the message <b>9141_ECU8_SID\$01_PID\$00</b> .
<u>EVALUATION</u>	Pass if transmission was successful and the DUT identifies the OBD II protocol as ISO 9141-2 with 8 ECUs.

**Figure 51 - Verify ISO 9141-2 5-baud initialization with key bytes \$08 \$08 and 8 ECUS**

## 8.4.4 Verify 5-Baud Initialization with Key Bytes \$9494 (2 ECUs)

<b>Test ID:</b>	\$20303002
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can successfully perform a 5-baud OBD II initialization on the ISO 9141-2 network and communicate at 10400 baud when the Key Bytes \$94 \$94 are returned.
<b>Rationale:</b>	This test will use a positive response from the first ECU with the Key Bytes \$94 \$94, then two ECUs will respond positively to the SID \$01 PID \$00 request.
<b>Set Up:</b>	<p>The DUT shall be in the “ISO9141-2 Connected State” as defined in 5.4.5.5 with the ISO 9141-2 network connected to the ECUSIM. The ECUSIM shall be in the “ISO9141-2 Connected State” as defined in 5.4.5.5.</p> <p>An oscilloscope shall be connected to the ISO 9141-2 network to measure the timing of voltage transitions during the initialization process.</p>
<b>Procedure:</b>	
<u>ACTION- DUT</u>	The DUT starts the ISO 9141-2 5-baud initialization sequence.
<u>EVALUATION</u>	<p>Pass if the following events occurred in this order:  Verify that both K and L Lines at idle (logic ‘1’) for the time specified in W5.  Verify that both K and L Lines transmit the byte \$33 at 5-bits per second <math>\pm 0.5\%</math>.</p>
<u>ACTION- ECUSIM</u>	Using 10400 baud from now on, delay W1, transmit the byte \$55, delay W2, transmit \$94, delay W3, transmit \$94.
<u>EVALUATION</u>	Pass if \$6B is received in no less than W4_MIN and no greater than W4_MAX.
<u>ACTION- ECUSIM</u>	After the W4 delay, transmit the byte \$CC.
<u>EVALUATION</u>	Pass if transmission is successful.
<u>ACTION- DUT</u>	The DUT issues the Service \$01 PID \$00 request.
<u>EVALUATION</u>	Pass if the message <b>9141_SID\$01_PID\$00</b> was transmitted (at 10400 baud) over the K Line with inter-byte times of at least P4_MIN and no greater than P4_MAX.
<u>ACTION- ECUSIM</u>	After the P2 delay, transmit the message <b>9141_ECU1_SID\$01_PID\$00</b> , delay P2, then transmit the message <b>9141_ECU2_SID\$01_PID\$00</b> .
<u>EVALUATION</u>	Pass if transmission was successful and the DUT identifies the OBD II protocol as ISO 9141-2 with 2 ECUs.

**Figure 52 - Verify ISO 9141-2 5-baud initialization with key bytes \$94 \$94 and 2 ECUs**

## 8.4.5 Verify Second Attempt at 5-Baud Initialization with Key Bytes \$9494 (2 ECUs)

<b>Test ID:</b>	\$20303003
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can successfully perform a 5-baud OBD II initialization on the ISO 9141-2 network and communicate at 10400 baud when the Key Bytes \$94 \$94 are returned after receiving no response to the first attempt at 5-baud initialization.
<b>Rationale:</b>	This test will have the no response to the first attempt to initialize, but the second attempt will use a positive response from the first ECU with the Key Bytes \$94 \$94, then two ECUs will respond positively to the SID \$01 PID \$00 request.
<b>Set Up:</b>	<p>The DUT shall be in the “ISO9141-2 Connected State” as defined in 5.4.5.5 with the ISO 9141-2 network connected to the ECUSIM. The ECUSIM shall be in the “ISO9141-2 Connected State” as defined in 5.4.5.5.</p> <p>An oscilloscope shall be connected to the ISO 9141-2 network to measure the timing of voltage transitions during the initialization process.</p>
<b>Procedure:</b>	
<u>ACTION- DUT</u>	The DUT starts the ISO 9141-2 5-baud initialization sequence.
<u>EVALUATION</u>	<p>Pass if the following events occurred in this order:  Verify that both K and L Lines at idle (logic ‘1’) for the time specified in W5.  Verify that both K and L Lines transmit the byte \$33 at 5-bits per second <math>\pm 0.5\%</math>.</p>
<u>ACTION- ECUSIM</u>	The ECUSIM shall take no action in response to the first attempt at initialization. The ECUSIM shall wait for the second attempt at initialization.
<u>EVALUATION</u>	<p>Pass if the following events occurred in this order:  Verify that both K and L Lines at idle (logic ‘1’) for the time specified in W5.  Verify that both K and L Lines transmit the byte \$33 at 5-bits per second <math>\pm 0.5\%</math>.</p>
<u>ACTION- ECUSIM</u>	Using 10400 baud from now on, delay W1, transmit the byte \$55, delay W2, transmit \$94, delay W3, transmit \$94.
<u>EVALUATION</u>	Pass if \$6B is received in no less than W4_MIN and no greater than W4_MAX.
<u>ACTION- ECUSIM</u>	After the W4 delay, transmit the byte \$CC.
<u>EVALUATION</u>	Pass if transmission is successful.
<u>ACTION- DUT</u>	The DUT issues the Service \$01 PID \$00 request.
<u>EVALUATION</u>	Pass if the message <b>9141_SID\$01_PID\$00</b> was transmitted (at 10400 baud) over the K Line with inter-byte times of at least P4_MIN and no greater than P4_MAX.
<u>ACTION- ECUSIM</u>	After the P2 delay, transmit the message <b>9141_ECU1_SID\$01_PID\$00</b> , delay P2, then transmit the message <b>9141_ECU2_SID\$01_PID\$00</b> .
<u>EVALUATION</u>	Pass if transmission was successful and the DUT identifies the OBD II protocol as ISO 9141-2 with 2 ECUs.

**Figure 53 - Verify ISO 9141-2 second attempt at 5-baud initialization with key bytes \$94 \$94 and 2 ECUs**

## 8.4.6 Verify 5-Baud Initialization when SID \$01 PID \$00 First Response is Corrupted (2 ECUs)

<b>Test ID:</b>	\$20303004
<b>Purpose:</b>	The purpose of this test is to verify that after the 5-baud initialization, the DUT will re-transmit a request when the first response is corrupted (and there are no subsequent responses) to the SID \$01 PID \$00 request.
<b>Rationale:</b>	This test will have a successful 5-baud initialization, but the first (and only) response to SID \$01 PID \$00 will be corrupted.
<b>Set Up:</b>	<p>The DUT shall be in the “ISO9141-2 Connected State” as defined in 5.4.5.5 with the ISO 9141-2 network connected to the ECUSIM. The ECUSIM shall be in the “ISO9141-2 Connected State” as defined in 5.4.5.5.</p> <p>An oscilloscope shall be connected to the ISO 9141-2 network to measure the timing of voltage transitions during the initialization process.</p>
<b>Procedure:</b>	
<u>ACTION- DUT</u>	The DUT starts the ISO 9141-2 5-baud initialization sequence.
<u>EVALUATION</u>	<p>Pass if the following events occurred in this order:  Verify that both K and L Lines at idle (logic ‘1’) for the time specified in W5.  Verify that both K and L Lines transmit the byte \$33 at 5-bits per second <math>\pm 0.5\%</math>.</p>
<u>ACTION- ECUSIM</u>	Using 10400 baud from now on, delay W1, transmit the byte \$55, delay W2, transmit \$94, delay W3, transmit \$94.
<u>EVALUATION</u>	Pass if \$6B is received in no less than W4_MIN and no greater than W4_MAX.
<u>ACTION- ECUSIM</u>	After the W4 delay, transmit the byte \$CC.
<u>EVALUATION</u>	Pass if transmission is successful.
<u>ACTION- DUT</u>	The DUT issues the Service \$01 PID \$00 request.
<u>EVALUATION</u>	Pass if the message <b>9141_SID\$01_PID\$00</b> was transmitted (at 10400 baud) over the K Line with inter-byte times of at least P4_MIN and no greater than P4_MAX.
<u>ACTION- ECUSIM</u>	After the P2 delay, transmit the message <b>9141_ECU1_SID\$01_PID\$00_BAD</b> .
<u>EVALUATION</u>	Pass if transmission was successful.
<u>ACTION- DUT</u>	Without user intervention, the DUT must redo the ISO 9141-2 5-baud initialization sequence.
<u>EVALUATION</u>	<p>Pass if the following events occurred in this order:  Verify that both K and L Lines at idle (logic ‘1’) for the time specified in W5.  Verify that both K and L Lines transmit the byte \$33 at 5-bits per second <math>\pm 0.5\%</math>.</p>
<u>ACTION- ECUSIM</u>	Using 10400 baud from now on, delay W1, transmit the byte \$55, delay W2, transmit \$94, delay W3, transmit \$94.
<u>EVALUATION</u>	Pass if \$6B is received in no less than W4_MIN and no greater than W4_MAX.
<u>ACTION- ECUSIM</u>	After the W4 delay, transmit the byte \$CC.
<u>EVALUATION</u>	Pass if transmission is successful.

<u>ACTION- DUT</u>	The DUT issues the Service \$01 PID \$00 request.
<u>EVALUATION</u>	Pass if the message <b>9141_SID\$01_PID\$00</b> was transmitted (at 10400 baud) over the K Line with inter-byte times of at least <i>P4_MIN</i> and no greater than <i>P4_MAX</i> .
<u>ACTION- ECUSIM</u>	After the <i>P2</i> delay, transmit the message <b>9141_ECU1_SID\$01_PID\$00</b> , delay <i>P2</i> , then transmit the message <b>9141_ECU2_SID\$01_PID\$00</b> .
<u>EVALUATION</u>	Pass if transmission was successful and the DUT identifies the OBD II protocol as ISO 9141-2 with 2 ECUs.

**Figure 54 - Verify ISO 9141-2 request re-transmission when SID \$01 PID \$00 first response is corrupted**

## 8.5 SAE J1850 VPW Tests

These protocol tests are specific to SAE J1850 VPW. Unless otherwise specified, all references to Electrical Connections, ECU Addresses, Messages, Message Timing, and pre-defined states will be as described in 5.4.5.7.

### 8.5.1 Verify Protocol Initialization (2 ECUs)

<b>Test ID:</b>	\$20403000
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can successfully perform an OBD II initialization on the SAE J1850 VPW network.
<b>Rationale:</b>	This test will use positive responses from two different ECUs.
<b>Set Up:</b>	The DUT shall be in the “SAE J1850 VPW Connected State” as defined in 5.4.6.5 with the SAE J1850 VPW network connected to the ECUSIM. The ECUSIM shall be in the “SAE J1850 VPW Connected State” as defined in 5.4.6.5.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the OBD II protocol scan.
<u>EVALUATION</u>	Pass if the <b>1850VPW_SID\$01_PID\$00</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2</i> delay, transmit the message <b>1850VPW_ECU1_SID\$01_PID\$00</b> , delay <i>P2</i> , then transmit the message <b>1850VPW_ECU2_SID\$01_PID\$00</b> .
<u>EVALUATION</u>	Pass if transmission is successful and the DUT identifies the OBD II protocol as SAE J1850 with 2 ECUs.

**Figure 55 - Verify SAE J1850 VPW protocol initialization**

## 8.5.2 Verify Protocol Initialization (8 ECUs)

<b>Test ID:</b>	\$20403001
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can successfully perform an OBD II initialization on the SAE J1850 VPW network.
<b>Rationale:</b>	This test will use positive responses from eight different ECUs with ECU3 through ECU8 indicating that no SID \$01 PIDs are supported.
<b>Set Up:</b>	The DUT shall be in the “SAE J1850 VPW Connected State” as defined in 5.4.6.5 with the SAE J1850 VPW network connected to the ECUSIM. The ECUSIM shall be in the “SAE J1850 VPW Connected State” as defined in 5.4.6.5.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the OBD II protocol scan.
<u>EVALUATION</u>	Pass if the <b>1850VPW_SID\$01_PID\$00</b> message was transmitted.
<u>ACTION- ECUSIM</u>	
	Delay <i>P2</i> , then transmit the message <b>1850VPW_ECU1_SID\$01_PID\$00</b> .
	Delay <i>P2</i> , then transmit the message <b>1850VPW_ECU2_SID\$01_PID\$00</b> .
	Delay <i>P2</i> , then transmit the message <b>1850VPW_ECU3_SID\$01_PID\$00</b> .
	Delay <i>P2</i> , then transmit the message <b>1850VPW_ECU4_SID\$01_PID\$00</b> .
	Delay <i>P2</i> , then transmit the message <b>1850VPW_ECU5_SID\$01_PID\$00</b> .
	Delay <i>P2</i> , then transmit the message <b>1850VPW_ECU6_SID\$01_PID\$00</b> .
	Delay <i>P2</i> , then transmit the message <b>1850VPW_ECU7_SID\$01_PID\$00</b> .
	Delay <i>P2</i> , then transmit the message <b>1850VPW_ECU8_SID\$01_PID\$00</b> .
<u>EVALUATION</u>	
	Pass if transmission is successful and the DUT identifies the OBD II protocol as SAE J1850 with 8 ECUs.

**Figure 56 - Verify SAE J1850 VPW protocol initialization with 8 ECUs**

## 8.6 SAE J1850 PWM Tests

These protocol tests are specific to SAE J1850 PWM. Unless otherwise specified, all references to Electrical Connections, ECU Addresses, Messages, Message Timing, and pre-defined states will be as described in 5.4.6.7.

## 8.6.1 Verify Protocol Initialization (2 ECUs)

<b>Test ID:</b>	\$20503000
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can successfully perform an OBD II initialization on the SAE J1850 PWM network.
<b>Rationale:</b>	This test will use positive responses from two different ECUs.
<b>Set Up:</b>	The DUT shall be in the “SAE J1850 PWM Connected State” as defined in 5.4.6.5 with the SAE J1850 PWM network connected to the ECUSIM. The ECUSIM shall be in the “SAE J1850 PWM Connected State” as defined in 5.4.6.5.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the OBD II protocol scan.
<u>EVALUATION</u>	Pass if the <b>1850PWM_SID\$01_PID\$00</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2</i> delay, transmit the message <b>1850PWM_ECU1_SID\$01_PID\$00</b> , delay <i>P2</i> , then transmit the message <b>1850PWM_ECU2_SID\$01_PID\$00</b> .
<u>EVALUATION</u>	Pass if transmission is successful and the DUT identifies the OBD II protocol as SAE J1850 with 2 ECUs.

**Figure 57 - Verify SAE J1850 PWM protocol initialization**

## 8.6.2 Verify Protocol Initialization (8 ECUs)

<b>Test ID:</b>	\$20503001
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can successfully perform an OBD II initialization on the SAE J1850 PWM network.
<b>Rationale:</b>	This test will use positive responses from eight different ECUs with ECU3 through ECU8 indicating that no SID \$01 PIDs are supported.
<b>Set Up:</b>	The DUT shall be in the “SAE J1850 PWM Connected State” as defined in 5.4.6.5 with the SAE J1850 PWM network connected to the ECUSIM. The ECUSIM shall be in the “SAE J1850 PWM Connected State” as defined in 5.4.6.5.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the OBD II protocol scan.
<u>EVALUATION</u>	Pass if the <b>1850PWM_SID\$01_PID\$00</b> message was transmitted.
<u>ACTION- ECUSIM</u>	Delay <i>P2</i> , then transmit the message <b>1850PWM_ECU1_SID\$01_PID\$00</b> . Delay <i>P2</i> , then transmit the message <b>1850PWM_ECU2_SID\$01_PID\$00</b> . Delay <i>P2</i> , then transmit the message <b>1850PWM_ECU3_SID\$01_PID\$00</b> . Delay <i>P2</i> , then transmit the message <b>1850PWM_ECU4_SID\$01_PID\$00</b> . Delay <i>P2</i> , then transmit the message <b>1850PWM_ECU5_SID\$01_PID\$00</b> . Delay <i>P2</i> , then transmit the message <b>1850PWM_ECU6_SID\$01_PID\$00</b> . Delay <i>P2</i> , then transmit the message <b>1850PWM_ECU7_SID\$01_PID\$00</b> . Delay <i>P2</i> , then transmit the message <b>1850PWM_ECU8_SID\$01_PID\$00</b> .
<u>EVALUATION</u>	Pass if transmission is successful and the DUT identifies the OBD II protocol as SAE J1850 with 8 ECUs.

**Figure 58 - Verify SAE J1850 PWM protocol initialization with 8 ECUs**

## 9. NETWORK EVALUATION

This section details test cases that will verify the network attributes (such as, message format, responses to incorrect/invalid messages, etc.) of the DUT.

### 9.1 General Tests

These are general network tests, which are not specific to any OBD II protocol.

### 9.2 ISO 15765-4 Tests

These network tests are specific to ISO 15765-4. Unless otherwise specified, all references to Electrical Connections, ECU Addresses, Messages, Message Timing, and pre-defined states will be as described in 5.4.3.

#### 9.2.1 Verify a SID \$01 PID Supported PID Requests (11-bit, 500K; 1 ECU)

<b>Test ID:</b>	\$30101000
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request a SID \$01 PID Supported PIDs.
<b>Rationale:</b>	This test will use positive responses from a single different ECU.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 15765 (11-bit, 500K, 2 ECUs) Initialized State”, which is detailed in 5.4.3.9.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$01 PID Supported PIDs.
<u>EVALUATION</u>	Pass if the <b>15765_11_SID\$01_PID\$00</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2_MIN</i> delay, transmit the message <b>15765_11_ECU1_SID\$01_PID\$00_20</b> .
<u>EVALUATION</u>	Pass if transmission is successful and the DUT transmits the message <b>15765_11_SID\$01_PID\$20</b> .
<u>ACTION- ECUSIM</u>	After the <i>P2_MIN</i> delay, transmit the message <b>15765_11_ECU1_SID\$01_PID\$20</b> .
<u>EVALUATION</u>	Pass if transmission is successful and the DUT transmits the message <b>15765_11_SID\$01_PID\$40</b> .
<u>ACTION- ECUSIM</u>	After the <i>P2_MIN</i> delay, transmit the message <b>15765_11_ECU1_SID\$01_PID\$40</b> .
<u>EVALUATION</u>	Pass if transmission is successful and the DUT indicates that PIDs \$01, \$0C, and \$41 are supported for ECU #1.

**Figure 59 - Verify an ISO 15765-4 SID \$01 PID supported PID request (11-bit, 500K)**

## 9.2.2 Verify a SID \$01 PID Supported PID Requests (29-bit, 500K; 1 ECU)

<b>Test ID:</b>	\$30101001
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request a SID \$01 PID Supported PIDs.
<b>Rationale:</b>	This test will use positive responses from a single different ECU.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the "ISO 15765 (29-bit, 500K, 2 ECUs) Initialized State", which is detailed in 5.4.3.10.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$01 PID Supported PIDs.
<u>EVALUATION</u>	Pass if the <b>15765_29_SID\$01_PID\$00</b> message was transmitted
<u>ACTION- ECUSIM</u>	After the <i>P2_MIN</i> delay, transmit the message <b>15765_29_ECU1_SID\$01_PID\$00_20</b> .
<u>EVALUATION</u>	Pass if transmission is successful and the DUT transmits the message <b>15765_29_SID\$01_PID\$20</b> .
<u>ACTION- ECUSIM</u>	After the <i>P2_MIN</i> delay, transmit the message <b>15765_29_ECU1_SID\$01_PID\$20</b> .
<u>EVALUATION</u>	Pass if transmission is successful and the DUT transmits the message <b>15765_29_SID\$01_PID\$40</b> .
<u>ACTION- ECUSIM</u>	After the <i>P2_MIN</i> delay, transmit the message <b>15765_29_ECU1_SID\$01_PID\$40</b> .
<u>EVALUATION</u>	Pass if transmission is successful and the DUT indicates that PIDs \$01, \$0C, and \$41 are supported for ECU #1.

**Figure 60 - Verify an ISO 15765-4 SID \$01 PID supported PID request (29-bit, 500K)**

## 9.2.3 Verify a SID \$01 PID Supported PID Requests - Group (11-bit, 500K; 1 ECU)

<b>Test ID:</b>	\$30101002
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request a SID \$01 PID Supported PIDs as a group.
<b>Rationale:</b>	This test will use positive responses from a single different ECU using a group request/response.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 15765 (11-bit, 500K, 2 ECUs) Initialized State”, which is detailed in 5.4.3.9.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$01 PID Supported PIDs as a group request.
<u>EVALUATION</u>	Pass if the <b>15765_11_SID\$01_PID\$00-\$E0</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2_MIN</i> delay, transmit the message <b>15765_11_ECU1_SID\$01_PID\$00-\$E0</b> .
<u>EVALUATION</u>	Pass if transmission is successful and the DUT indicates that PIDs \$01, \$0C, and \$41 are supported for ECU #1.

**Figure 61 - Verify an ISO 15765-4 SID \$01 PID supported PID request - group (11-bit, 500K)**

## 9.2.4 Verify a SID \$01 PID Supported PID Requests - Group (29-bit, 500K; 1 ECU)

<b>Test ID:</b>	\$30101003
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request a SID \$01 PID Supported PIDs as a group.
<b>Rationale:</b>	This test will use positive responses from a single different ECU using a group request/response.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 15765 (29-bit, 500K, 2 ECUs) Initialized State”, which is detailed in 5.4.3.10.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$01 PID Supported PIDs as a group request.
<u>EVALUATION</u>	Pass if the <b>15765_29_SID\$01_PID\$00-\$E0</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2_MIN</i> delay, transmit the message <b>15765_29_ECU1_SID\$01_PID\$00-\$E0</b> .
<u>EVALUATION</u>	Pass if transmission is successful and the DUT indicates that PIDs \$01, \$0C, and \$41 are supported for ECU #1.

**Figure 62 - Verify an ISO 15765-4 SID \$01 PID supported PID request - group (29-bit, 500K)**

## 9.2.5 Verify a SID \$01 PID Requests (11-bit, 500K; 1 ECU)

<b>Test ID:</b>	\$30101004
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request several SID \$01 PIDs.
<b>Rationale:</b>	This test will use positive responses from a single different ECU.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the "ISO 15765 (11-bit, 500K, 2 ECUs) Initialized State", which is detailed in 5.4.3.9.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	
Initiate the request for SID \$01 PIDs \$01, \$0C, and \$41 from ECU #1.	
<u>ACTION- ECUSIM</u>	
If the DUT transmits the message <b>15765_11_SID\$01_PID\$01</b> , then after the delay <i>P2_MIN</i> , transmit the message <b>15765_11_ECU1_SID\$01_PID\$01_SPARK</b> . Wait for the remaining message transactions in this ACTION to be completed.	
If the DUT transmits the message <b>15765_11_SID\$01_PID\$0C</b> , then after the delay <i>P2_MIN</i> and transmit the message <b>15765_11_ECU1_SID\$01_PID\$0C</b> . Wait for the remaining message transactions in this ACTION to be completed.	
If the DUT transmits the message <b>15765_11_SID\$01_PID\$41</b> , then after the delay <i>P2_MIN</i> and transmit the message <b>15765_11_ECU1_SID\$01_PID\$41</b> . Wait for the remaining message transactions in this ACTION to be completed.	
<u>EVALUATION</u>	
Pass if the following is true: All three messages were successfully received and the corresponding responses were successfully transmitted. The DUT indicates that PIDs \$01, \$0C, and \$41 are supported for ECU #1. PID \$01 for ECU #1 has the following values: MIL is OFF DCT COUNT is 0 SPARK IGNITION MONITORS are SUPPORTED MISFIRE is SUPPORTED and READY FUEL SYSTEM MONITORING is SUPPORTED and READY COMPREHENSIVE COMPONENT MONITORING is SUPPORTED and READY CATALYST MONITORING is SUPPORTED and NOT READY HEATED CATALYST MONITORING is SUPPORTED and NOT READY EVAPORATIVE SYSTEM MONITORING is SUPPORTED and NOT READY SECONDARY AIR SYSTEM MONITORING is SUPPORTED and NOT READY OXYGEN SENSOR MONITORING is SUPPORTED and NOT READY OXYGEN SENSOR HEATER MONITORING is SUPPORTED and NOT READY EGR/VVT SYSTEM MONITORING is SUPPORTED and NOT READY PID \$0C for ECU #1 indicates that engine RPM is 0. PID \$41 for ECU #1 has the following values: MIL is OFF DCT COUNT is 0 SPARK IGNITION MONITORS are SUPPORTED MISFIRE is SUPPORTED and READY FUEL SYSTEM MONITORING is SUPPORTED and READY COMPREHENSIVE COMPONENT MONITORING is SUPPORTED and READY CATALYST MONITORING is SUPPORTED and NOT READY HEATED CATALYST MONITORING is SUPPORTED and NOT READY EVAPORATIVE SYSTEM MONITORING is SUPPORTED and NOT READY SECONDARY AIR SYSTEM MONITORING is SUPPORTED and NOT READY OXYGEN SENSOR MONITORING is SUPPORTED and NOT READY OXYGEN SENSOR HEATER MONITORING is SUPPORTED and NOT READY EGR/VVT SYSTEM MONITORING is SUPPORTED and NOT READY.	

Figure 63 - Verify an ISO 15765-4 SID \$01 PID request (11-bit, 500K)

## 9.2.6 Verify a SID \$01 PID Requests (29-bit, 500K; 1 ECU)

<b>Test ID:</b>	\$30101005
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request several SID \$01 PIDs.
<b>Rationale:</b>	This test will use positive responses from a single different ECU.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 15765 (29-bit, 500K, 2 ECUs) Initialized State”, which is detailed in 5.4.3.10.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	
	Initiate the request for SID \$01 PIDs \$01, \$0C, and \$41 from ECU #1.
<u>ACTION- ECUSIM</u>	
	If the DUT transmits the message <b>15765_29_SID\$01_PID\$01</b> , then after the delay <i>P2_MIN</i> and transmit the message <b>15765_29_ECU1_SID\$01_PID\$01_SPARK</b> . Wait for the remaining message transactions in this ACTION to be completed.
	If the DUT transmits the message <b>15765_29_SID\$01_PID\$0C</b> , then after the delay <i>P2_MIN</i> and transmit the message <b>15765_29_ECU1_SID\$01_PID\$0C</b> . Wait for the remaining message transactions in this ACTION to be completed.
	If the DUT transmits the message <b>15765_29_SID\$01_PID\$41</b> , then after the delay <i>P2_MIN</i> and transmit the message <b>15765_29_ECU1_SID\$01_PID\$41</b> .
<u>EVALUATION</u>	
	Pass if the following is true: All three messages were successfully received and the corresponding responses were successfully transmitted. The DUT indicates that PIDs \$01, \$0C, and \$41 are supported for ECU #1. PID \$01 for ECU #1 has the following values: MIL is OFF DCT COUNT is 0 SPARK IGNITION MONITORS are SUPPORTED MISFIRE is SUPPORTED and READY FUEL SYSTEM MONITORING is SUPPORTED and READY COMPREHENSIVE COMPONENT MONITORING is SUPPORTED and READY CATALYST MONITORING is SUPPORTED and NOT READY HEATED CATALYST MONITORING is SUPPORTED and NOT READY EVAPORATIVE SYSTEM MONITORING is SUPPORTED and NOT READY SECONDARY AIR SYSTEM MONITORING is SUPPORTED and NOT READY OXYGEN SENSOR MONITORING is SUPPORTED and NOT READY OXYGEN SENSOR HEATER MONITORING is SUPPORTED and NOT READY EGR/VVT SYSTEM MONITORING is SUPPORTED and NOT READY PID \$0C for ECU #1 indicates that engine RPM is 0. PID \$41 for ECU #1 has the following values: MIL is OFF DCT COUNT is 0 SPARK IGNITION MONITORS are SUPPORTED MISFIRE is SUPPORTED and READY FUEL SYSTEM MONITORING is SUPPORTED and READY COMPREHENSIVE COMPONENT MONITORING is SUPPORTED and READY CATALYST MONITORING is SUPPORTED and NOT READY HEATED CATALYST MONITORING is SUPPORTED and NOT READY EVAPORATIVE SYSTEM MONITORING is SUPPORTED and NOT READY SECONDARY AIR SYSTEM MONITORING is SUPPORTED and NOT READY OXYGEN SENSOR MONITORING is SUPPORTED and NOT READY OXYGEN SENSOR HEATER MONITORING is SUPPORTED and NOT READY EGR/VVT SYSTEM MONITORING is SUPPORTED and NOT READY.

Figure 64 - Verify an ISO 15765-4 SID \$01 PID request (29-bit, 500K)

## 9.2.7 Verify a SID \$01 PID Requests - Group Req. (11-bit, 500K; 1 ECU)

<b>Test ID:</b>	\$30101006
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request several SID \$01 PIDs as a group.
<b>Rationale:</b>	This test will use positive responses from a single different ECU using a group request/response.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 15765 (11-bit, 500K, 2 ECUs) Initialized State”, which is detailed in 5.4.3.9.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$01 PIDs \$01, \$0C, and \$41 from ECU #1 as a group request.
<u>EVALUATION</u>	Pass if the DUT transmits the message <b>15765_11_SID\$01_PID\$01-\$0C-\$41</b> .
<u>ACTION- ECUSIM</u>	After the <i>P2_MIN</i> delay, transmit the message <b>15765_11_ECU1_SID\$01_PID\$01-\$0C-\$41</b> .
<u>EVALUATION</u>	<p>Pass if the following is true:</p> <p>Transmission is successful.</p> <p>The DUT indicates that PIDs \$01, \$0C, and \$41 are supported for ECU #1.</p> <p>PID \$01 for ECU #1 has the following values:</p> <ul style="list-style-type: none"> <li>MIL is OFF</li> <li>DCT COUNT is 0</li> <li>SPARK IGNITION MONITORS are SUPPORTED</li> <li>MISFIRE is SUPPORTED and READY</li> <li>FUEL SYSTEM MONITORING is SUPPORTED and READY</li> <li>COMPREHENSIVE COMPONENT MONITORING is SUPPORTED and READY</li> <li>CATALYST MONITORING is SUPPORTED and NOT READY</li> <li>HEATED CATALYST MONITORING is SUPPORTED and NOT READY</li> <li>EVAPORATIVE SYSTEM MONITORING is SUPPORTED and NOT READY</li> <li>SECONDARY AIR SYSTEM MONITORING is SUPPORTED and NOT READY</li> <li>OXYGEN SENSOR MONITORING is SUPPORTED and NOT READY</li> <li>OXYGEN SENSOR HEATER MONITORING is SUPPORTED and NOT READY</li> <li>EGR/VVT SYSTEM MONITORING is SUPPORTED and NOT READY</li> </ul> <p>PID \$0C for ECU #1 indicates that engine RPM is 0.</p> <p>PID \$41 for ECU #1 has the following values:</p> <ul style="list-style-type: none"> <li>MIL is OFF</li> <li>DCT COUNT is 0</li> <li>SPARK IGNITION MONITORS are SUPPORTED</li> <li>MISFIRE is SUPPORTED and READY</li> <li>FUEL SYSTEM MONITORING is SUPPORTED and READY</li> <li>COMPREHENSIVE COMPONENT MONITORING is SUPPORTED and READY</li> <li>CATALYST MONITORING is SUPPORTED and NOT READY</li> <li>HEATED CATALYST MONITORING is SUPPORTED and NOT READY</li> <li>EVAPORATIVE SYSTEM MONITORING is SUPPORTED and NOT READY</li> <li>SECONDARY AIR SYSTEM MONITORING is SUPPORTED and NOT READY</li> <li>OXYGEN SENSOR MONITORING is SUPPORTED and NOT READY</li> <li>OXYGEN SENSOR HEATER MONITORING is SUPPORTED and NOT READY</li> <li>EGR/VVT SYSTEM MONITORING is SUPPORTED and NOT READY.</li> </ul>

**Figure 65 - Verify an ISO 15765-4 SID \$01 PID request - group (11-bit, 500K)**

## 9.2.8 Verify a SID \$01 PID Requests - Group Req. (29-bit, 500K; 1 ECU)

<b>Test ID:</b>	\$30101007
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request several SID \$01 PIDs as a group.
<b>Rationale:</b>	This test will use positive responses from a single different ECU using a group request/response.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 15765 (29-bit, 500K, 2 ECUs) Initialized State”, which is detailed in 5.4.3.10.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$01 PIDs \$01, \$0C, and \$41 from ECU #1 as a group request.
<u>EVALUATION</u>	Pass if the DUT transmits the message <b>15765_29_SID\$01_PID\$01-\$0C-\$41</b> .
<u>ACTION- ECUSIM</u>	After the <i>P2_MIN</i> delay, transmit the message <b>15765_29_ECU1_SID\$01_PID\$01-\$0C-\$41</b> .
<u>EVALUATION</u>	<p>Pass if the following is true:</p> <p>Transmission is successful.</p> <p>The DUT indicates that PIDs \$01, \$0C, and \$41 are supported for ECU #1.</p> <p>PID \$01 for ECU #1 has the following values:</p> <ul style="list-style-type: none"> <li>MIL is OFF</li> <li>DCT COUNT is 0</li> <li>SPARK IGNITION MONITORS are SUPPORTED</li> <li>MISFIRE is SUPPORTED and READY</li> <li>FUEL SYSTEM MONITORING is SUPPORTED and READY</li> <li>COMPREHENSIVE COMPONENT MONITORING is SUPPORTED and READY</li> <li>CATALYST MONITORING is SUPPORTED and NOT READY</li> <li>HEATED CATALYST MONITORING is SUPPORTED and NOT READY</li> <li>EVAPORATIVE SYSTEM MONITORING is SUPPORTED and NOT READY</li> <li>SECONDARY AIR SYSTEM MONITORING is SUPPORTED and NOT READY</li> <li>OXYGEN SENSOR MONITORING is SUPPORTED and NOT READY</li> <li>OXYGEN SENSOR HEATER MONITORING is SUPPORTED and NOT READY</li> <li>EGR/VVT SYSTEM MONITORING is SUPPORTED and NOT READY</li> </ul> <p>PID \$0C for ECU #1 indicates that engine RPM is 0.</p> <p>PID \$41 for ECU #1 has the following values:</p> <ul style="list-style-type: none"> <li>MIL is OFF</li> <li>DCT COUNT is 0</li> <li>SPARK IGNITION MONITORS are SUPPORTED</li> <li>MISFIRE is SUPPORTED and READY</li> <li>FUEL SYSTEM MONITORING is SUPPORTED and READY</li> <li>COMPREHENSIVE COMPONENT MONITORING is SUPPORTED and READY</li> <li>CATALYST MONITORING is SUPPORTED and NOT READY</li> <li>HEATED CATALYST MONITORING is SUPPORTED and NOT READY</li> <li>EVAPORATIVE SYSTEM MONITORING is SUPPORTED and NOT READY</li> <li>SECONDARY AIR SYSTEM MONITORING is SUPPORTED and NOT READY</li> <li>OXYGEN SENSOR MONITORING is SUPPORTED and NOT READY</li> <li>OXYGEN SENSOR HEATER MONITORING is SUPPORTED and NOT READY</li> <li>EGR/VVT SYSTEM MONITORING is SUPPORTED and NOT READY.</li> </ul>

**Figure 66 - Verify an ISO 15765-4 SID \$01 PID request - group (29-bit, 500K)**

## 9.2.9 Verify a SID \$01 PID \$01 Request for Spark Ignition (11-bit, 500K; 2 ECUs w/ Order Swap)

<b>Test ID:</b>	\$30101008
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request a SID \$01 PID, which the ECUSIM indicates is supported.
<b>Rationale:</b>	This test will use positive responses from two different ECUs. The order of ECU responses will be different than that of the original connection order. This test will also verify that the DUT can differentiate a spark ignition response from a compression ignition response.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the "ISO 15765 (11-bit, 500K, 2 ECUs) Initialized State", which is detailed in 5.4.3.9.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$01 PID \$01 (I/M Readiness Bits).
<u>EVALUATION</u>	Pass if the <b>15765_11_SID\$01_PID\$01</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2_MIN</i> delay, transmit the message <b>15765_11_ECU2_SID\$01_PID\$01_SPARK</b> , delay <i>P2_MIN</i> , then transmit the message <b>15765_11_ECU1_SID\$01_PID\$01_SPARK</b> .
<u>EVALUATION</u>	<p>Pass if the following is true:</p> <p>Transmission is successful.</p> <p>The DUT indicates that ECU #1 has the following values:</p> <p>MIL is OFF  DCT COUNT is 0  SPARK IGNITION MONITORS are SUPPORTED  MISFIRE is SUPPORTED and READY  FUEL SYSTEM MONITORING is SUPPORTED and READY  COMPREHENSIVE COMPONENT MONITORING is SUPPORTED and READY  CATALYST MONITORING is SUPPORTED and NOT READY  HEATED CATALYST MONITORING is SUPPORTED and NOT READY  EVAPORATIVE SYSTEM MONITORING is SUPPORTED and NOT READY  SECONDARY AIR SYSTEM MONITORING is SUPPORTED and NOT READY  OXYGEN SENSOR MONITORING is SUPPORTED and NOT READY  OXYGEN SENSOR HEATER MONITORING is SUPPORTED and NOT READY  EGR/VVT SYSTEM MONITORING is SUPPORTED and NOT READY</p> <p>The DUT indicates that ECU #2 has the following values:</p> <p>MIL is ON  DCT COUNT is 1  SPARK IGNITION MONITORS are SUPPORTED  COMPREHENSIVE COMPONENT MONITORING is SUPPORTED and NOT READY</p>

**Figure 67 - Verify an ISO 15765-4 SID \$01 PID \$01 request for spark ignition (11-bit, 500K)**

## 9.2.10 Verify a SID \$01 PID \$01 Request for Spark Ignition (29-bit, 500K; 2 ECUs w/ Order Swap)

<b>Test ID:</b>	\$30101009
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request a SID \$01 PID, which the ECUSIM indicates is supported.
<b>Rationale:</b>	This test will use positive responses from two different ECUs. The order of ECU responses will be different than that of the original connection order. This test will also verify that the DUT can differentiate a spark ignition response from a compression ignition response.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the "ISO 15765 (29-bit, 500K, 2 ECUs) Initialized State", which is detailed in 5.4.3.10.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$01 PID \$01 (I/M Readiness Bits).
<u>EVALUATION</u>	Pass if the <b>15765_29_SID\$01_PID\$01</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2_MIN</i> delay, transmit the message <b>15765_29_ECU2_SID\$01_PID\$01_SPARK</b> , delay <i>P2_MIN</i> , then transmit the message <b>15765_29_ECU1_SID\$01_PID\$01_SPARK</b> .
<u>EVALUATION</u>	<p>Pass if the following is true:</p> <p>Transmission is successful.</p> <p>The DUT indicates that ECU #1 has the following values:</p> <p>MIL is OFF  DCT COUNT is 0  SPARK IGNITION MONITORS are SUPPORTED  MISFIRE is SUPPORTED and READY  FUEL SYSTEM MONITORING is SUPPORTED and READY  COMPREHENSIVE COMPONENT MONITORING is SUPPORTED and READY  CATALYST MONITORING is SUPPORTED and NOT READY  HEATED CATALYST MONITORING is SUPPORTED and NOT READY  EVAPORATIVE SYSTEM MONITORING is SUPPORTED and NOT READY  SECONDARY AIR SYSTEM MONITORING is SUPPORTED and NOT READY  OXYGEN SENSOR MONITORING is SUPPORTED and NOT READY  OXYGEN SENSOR HEATER MONITORING is SUPPORTED and NOT READY  EGR/VVT SYSTEM MONITORING is SUPPORTED and NOT READY</p> <p>The DUT indicates that ECU #2 has the following values:</p> <p>MIL is ON  DCT COUNT is 1  SPARK IGNITION MONITORS are SUPPORTED  COMPREHENSIVE COMPONENT MONITORING is SUPPORTED and NOT READY</p>

**Figure 68 - Verify an ISO 15765-4 SID \$01 PID \$01 request for spark ignition (29-bit, 500K)**

## 9.2.11 Verify a SID \$01 PID \$01 Request for Compression Ignition (11-bit, 500K; 2 ECUs w/ Order Swap)

<b>Test ID:</b>	\$3010100A
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request a SID \$01 PID, which the ECUSIM indicates is supported.
<b>Rationale:</b>	This test will use positive responses from two different ECUs. The order of ECU responses will be different than that of the original connection order. This test will also verify that the DUT can differentiate a spark ignition response from a compression ignition response.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the "ISO 15765 (11-bit, 500K, 2 ECUs) Initialized State", which is detailed in 5.4.3.9.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$01 PID \$01 (I/M Readiness Bits).
<u>EVALUATION</u>	Pass if the <b>15765_11_SID\$01_PID\$01</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2_MIN</i> delay, transmit the message <b>15765_11_ECU2_SID\$01_PID\$01_COMP</b> , delay <i>P2_MIN</i> , then transmit the message <b>15765_11_ECU1_SID\$01_PID\$01_COMP</b> .
<u>EVALUATION</u>	<p>Pass if the following is true:</p> <p>Transmission is successful.</p> <p>The DUT indicates that ECU #1 has the following values:</p> <p>MIL is OFF  DCT COUNT is 0  COMPRESSION IGNITION MONITORS are SUPPORTED  MISFIRE is SUPPORTED and READY  FUEL SYSTEM MONITORING is SUPPORTED and READY  COMPREHENSIVE COMPONENT MONITORING is SUPPORTED and READY  NMHC MONITORING is SUPPORTED and NOT READY  NOX AFTERTREATMENT MONITORING is SUPPORTED and NOT READY  BOOST PRESSURE SYSTEM MONITORING is SUPPORTED and NOT READY  EXHAUST GAS SENSOR MONITORING is SUPPORTED and NOT READY  PM FILTER MONITORING is SUPPORTED and NOT READY  EGR/VVT SYSTEM MONITORING is SUPPORTED and NOT READY</p> <p>The DUT indicates that ECU #2 has the following values:</p> <p>MIL is ON  DCT COUNT is 1  COMPRESSION IGNITION MONITORS are SUPPORTED  COMPREHENSIVE COMPONENT MONITORING is SUPPORTED and NOT READY</p>

**Figure 69 - Verify an ISO 15765-4 SID \$01 PID \$01 request for compression ignition (11-bit, 500K)**

## 9.2.12 Verify a SID \$01 PID \$01 Request for Compression Ignition (29-bit, 500K; 2 ECUs w/ Order Swap)

<b>Test ID:</b>	\$3010100B
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request a SID \$01 PID, which the ECUSIM indicates is supported.
<b>Rationale:</b>	This test will use positive responses from two different ECUs. The order of ECU responses will be different than that of the original connection order. This test will also verify that the DUT can differentiate a spark ignition response from a compression ignition response.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the "ISO 15765 (29-bit, 500K, 2 ECUs) Initialized State", which is detailed in 5.4.3.10.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$01 PID \$01 (I/M Readiness Bits).
<u>EVALUATION</u>	Pass if the <b>15765_29_SID\$01_PID\$01</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2_MIN</i> delay, transmit the message <b>15765_29_ECU2_SID\$01_PID\$01_COMP</b> , delay <i>P2_MIN</i> , then transmit the message <b>15765_29_ECU1_SID\$01_PID\$01_COMP</b> .
<u>EVALUATION</u>	<p>Pass if the following is true:</p> <p>Transmission is successful.</p> <p>The DUT indicates that ECU #1 has the following values:</p> <p>MIL is OFF  DCT COUNT is 0  COMPRESSION IGNITION MONITORS are SUPPORTED  MISFIRE is SUPPORTED and READY  FUEL SYSTEM MONITORING is SUPPORTED and READY  COMPREHENSIVE COMPONENT MONITORING is SUPPORTED and READY  NMHC MONITORING is SUPPORTED and NOT READY  NOX AFTERTREATMENT MONITORING is SUPPORTED and NOT READY  BOOST PRESSURE SYSTEM MONITORING is SUPPORTED and NOT READY  EXHAUST GAS SENSOR MONITORING is SUPPORTED and NOT READY  PM FILTER MONITORING is SUPPORTED and NOT READY  EGR/VVT SYSTEM MONITORING is SUPPORTED and NOT READY</p> <p>The DUT indicates that ECU #2 has the following values:</p> <p>MIL is ON  DCT COUNT is 1  COMPRESSION IGNITION MONITORS are SUPPORTED  COMPREHENSIVE COMPONENT MONITORING is SUPPORTED and NOT READY</p>

**Figure 70 - Verify an ISO 15765-4 SID \$01 PID \$01 request for compression ignition (29-bit, 500K)**

## 9.2.13 Verify a SID \$01 PID \$0C Request (11-bit, 500K; 2 ECUs w/ Order Swap)

<b>Test ID:</b>	\$3010100C
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request a SID \$01 PID, which the ECUSIM indicates is supported.
<b>Rationale:</b>	This test will use positive responses from two different ECUs. The order of ECU responses will be different than that of the original connection order.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 15765 (11-bit, 500K, 2 ECUs) Initialized State”, which is detailed in 5.4.3.9.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$01 PID \$0C (RPM).
<u>EVALUATION</u>	Pass if the <b>15765_11_SID\$01_PID\$0C</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2_MIN</i> delay, transmit the message <b>15765_11_ECU2_SID\$01_PID\$0C_KOEO</b> , delay <i>P2_MIN</i> , then transmit the message <b>15765_11_ECU1_SID\$01_PID\$0C_KOEO</b> .
<u>EVALUATION</u>	Pass if transmission is successful and the DUT indicates that engine RPM is 0.

**Figure 71 - Verify an ISO 15765-4 SID \$01 PID \$0C request (11-bit, 500K)**

## 9.2.14 Verify a SID \$01 PID \$0C Request (29-bit, 500K; 2 ECUs w/ Order Swap)

<b>Test ID:</b>	\$3010100D
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request a SID \$01 PID, which the ECUSIM indicates is supported.
<b>Rationale:</b>	This test will use positive responses from two different ECUs. The order of ECU responses will be different than that of the original connection order.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 15765 (29-bit, 500K, 2 ECUs) Initialized State”, which is detailed in 5.4.3.10.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$01 PID \$0C (RPM).
<u>EVALUATION</u>	Pass if the <b>15765_29_SID\$01_PID\$0C</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2_MIN</i> delay, transmit the message <b>15765_29_ECU2_SID\$01_PID\$0C_KOEO</b> , delay <i>P2_MIN</i> , then transmit the message <b>15765_29_ECU1_SID\$01_PID\$0C_KOEO</b> .
<u>EVALUATION</u>	Pass if transmission is successful and the DUT indicates that engine RPM is 0.

**Figure 72 - Verify an ISO 15765-4 SID \$01 PID \$0C request (29-bit, 500K)**

## 9.2.15 Verify a SID \$02 PID Freeze Frame DTC Request with no DTCs (11-bit, 500K; 2 ECUs w/ Order Swap)

<b>Test ID:</b>	\$30102000
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$02 PID \$02 (indicating that there is no Freeze Frame DTC), which the ECUSIM indicates is supported.
<b>Rationale:</b>	This test will use positive responses from two different ECUs. The order of ECU responses will be different than that of the original connection order.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 15765 (11-bit, 500K, 2 ECUs) Initialized State”, which is detailed in 5.4.3.9.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$02 PID \$02 (the Freeze Frame DTC).
<u>EVALUATION</u>	Pass if the <b>15765_11_SID\$02_PID\$02</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2_MIN</i> delay, transmit the message <b>15765_11_ECU2_SID\$02_PID\$02_NoDTC</b> , delay <i>P2_MIN</i> , then transmit the message <b>15765_11_ECU1_SID\$02_PID\$02_NoDTC</b> .
<u>EVALUATION</u>	Pass if transmission is successful and the DUT indicates that there is no Freeze Frame DTC in either ECU.

**Figure 73 - Verify an ISO 15765-4 SID \$02 PID freeze frame DTC request with no DTCs (11-bit, 500K)**

## 9.2.16 Verify a SID \$02 PID Freeze Frame DTC Request with No DTCs (29-bit, 500K; 2 ECUs w/ Order Swap)

<b>Test ID:</b>	\$30102001
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$02 PID \$02 (indicating that there is no Freeze Frame DTC), which the ECUSIM indicates is supported.
<b>Rationale:</b>	This test will use positive responses from two different ECUs. The order of ECU responses will be different than that of the original connection order.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 15765 (29-bit, 500K, 2 ECUs) Initialized State”, which is detailed in 5.4.3.10.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$02 PID \$02 (the Freeze Frame DTC).
<u>EVALUATION</u>	Pass if the <b>15765_29_SID\$02_PID\$02</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2_MIN</i> delay, transmit the message <b>15765_29_ECU2_SID\$02_PID\$02_NoDTC</b> , delay <i>P2_MIN</i> , then transmit the message <b>15765_29_ECU1_SID\$02_PID\$02_NoDTC</b> .
<u>EVALUATION</u>	Pass if transmission is successful and the DUT indicates that there is no Freeze Frame DTC in either ECU.

**Figure 74 - Verify an ISO 15765-4 SID \$02 PID freeze frame DTC request with no DTCs (29-bit, 500K)**

## 9.2.17 Verify a SID \$02 PID Freeze Frame DTC Request with DTCs (11-bit, 500K; 2 ECUs)

<b>Test ID:</b>	\$30102002
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$02 PID \$02 (indicating that there is a Freeze Frame DTC), which the ECUSIM indicates is supported.
<b>Rationale:</b>	This test will use positive responses from two different ECUs.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 15765 (11-bit, 500K, 2 ECUs) Initialized State”, which is detailed in 5.4.3.9.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$02 PID \$02 (the Freeze Frame DTC).
<u>EVALUATION</u>	Pass if the <b>15765_11_SID\$02_PID\$02</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2_MIN</i> delay, transmit the message <b>15765_11_ECU1_SID\$02_PID\$02_DTC</b> , delay <i>P2_MIN</i> , then transmit the message <b>15765_11_ECU2_SID\$02_PID\$02_NoDTC</b> .
<u>EVALUATION</u>	Pass if the following is true: The transmission is successful. The DUT indicates that ECU #1 has a Freeze Frame DTC of P0107 and ECU #2 has no Freeze Frame DTC.

**Figure 75 - Verify an ISO 15765-4 SID \$02 PID freeze frame DTC request with DTCs (11-bit, 500K)**

## 9.2.18 Verify a SID \$02 PID Freeze Frame DTC Request with DTCs (29-bit, 500K; 2 ECUs)

<b>Test ID:</b>	\$30102003
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$02 PID \$02 (indicating that there is a Freeze Frame DTC), which the ECUSIM indicates is supported.
<b>Rationale:</b>	This test will use positive responses from two different ECUs.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 15765 (29-bit, 500K, 2 ECUs) Initialized State”, which is detailed in 5.4.3.10.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$02 PID \$02 (the Freeze Frame DTC).
<u>EVALUATION</u>	Pass if the <b>15765_29_SID\$02_PID\$02</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2_MIN</i> delay, transmit the message <b>15765_29_ECU1_SID\$02_PID\$02_DTC</b> , delay <i>P2_MIN</i> , then transmit the message <b>15765_29_ECU2_SID\$02_PID\$02_NoDTC</b> .
<u>EVALUATION</u>	Pass if the following is true: The transmission is successful. The DUT indicates that ECU #1 has a Freeze Frame DTC of P0107 and ECU #2 has no Freeze Frame DTC.

**Figure 76 - Verify an ISO 15765-4 SID \$02 PID freeze frame DTC request with DTCs (29-bit, 500K)**

## 9.2.19 Verify a SID \$03 Confirmed DTC Request with No DTCs (11-bit, 500K; 2 ECUs w/ Order Swap)

<b>Test ID:</b>	\$30103000
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$03 and process a 'no DTCs present' response.
<b>Rationale:</b>	This test will use positive responses from two different ECUs. The order of ECU responses will be different than that of the original connection order.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the "ISO 15765 (11-bit, 500K, 2 ECUs) Initialized State", which is detailed in 5.4.3.9.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$03 (Confirmed DTCs).
<u>EVALUATION</u>	Pass if the <b>15765_11_SID\$03</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2_MIN</i> delay, transmit the message <b>15765_11_ECU2_SID\$03_NoDTC</b> , delay <i>P2_MIN</i> , then transmit the message <b>15765_11_ECU1_SID\$03_NoDTC</b> .
<u>EVALUATION</u>	Pass if transmission is successful and the DUT indicates that there are no confirmed DTCs in either ECU.

**Figure 77 - Verify an ISO 15765-4 SID \$03 confirmed DTC request with no DTCs (11-bit, 500K)**

## 9.2.20 Verify a SID \$03 Confirmed DTC Request with No DTCs (29-bit, 500K; 2 ECUs w/ Order Swap)

<b>Test ID:</b>	\$30103001
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$03 and process a 'no DTCs present' response.
<b>Rationale:</b>	This test will use positive responses from two different ECUs. The order of ECU responses will be different than that of the original connection order.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the "ISO 15765 (29-bit, 500K, 2 ECUs) Initialized State", which is detailed in 5.4.3.10.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$03 (Confirmed DTCs).
<u>EVALUATION</u>	Pass if the <b>15765_29_SID\$03</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2_MIN</i> delay, transmit the message <b>15765_29_ECU2_SID\$03_NoDTC</b> , delay <i>P2_MIN</i> , then transmit the message <b>15765_29_ECU1_SID\$03_NoDTC</b> .
<u>EVALUATION</u>	Pass if transmission is successful and the DUT indicates that there are no confirmed DTCs in either ECU.

**Figure 78 - Verify an ISO 15765-4 SID \$03 confirmed DTC request with no DTCs (29-bit, 500K)**

## 9.2.21 Verify a SID \$03 Confirmed DTC Request with DTCs (11-bit, 500K; 2 ECUs)

<b>Test ID:</b>	\$30103002
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$03 and process a 'DTCs present' response.
<b>Rationale:</b>	This test will use positive responses from two different ECUs.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the "ISO 15765 (11-bit, 500K, 2 ECUs) Initialized State", which is detailed in 5.4.3.9.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$03 (Confirmed DTCs).
<u>EVALUATION</u>	Pass if the <b>15765_11_SID\$03</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2_MIN</i> delay, transmit the message <b>15765_11_ECU1_SID\$03_DTC</b> , delay <i>P2_MIN</i> , then transmit the message <b>15765_11_ECU2_SID\$03_NoDTC</b> .
<u>EVALUATION</u>	Pass if the following is true: The transmission is successful. The DUT indicates that ECU #1 has the following confirmed DTCs: P0107, P0110, P0115, and P0124. The DUT indicates that ECU #2 has no confirmed DTCs.

**Figure 79 - Verify an ISO 15765-4 SID \$03 confirmed DTC request with DTCs (11-bit, 500K)**

## 9.2.22 Verify a SID \$03 Confirmed DTC Request with DTCs (29-bit, 500K; 2 ECUs)

<b>Test ID:</b>	\$30103003
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$03 and process a 'DTCs present' response.
<b>Rationale:</b>	This test will use positive responses from two different ECUs.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the "ISO 15765 (29-bit, 500K, 2 ECUs) Initialized State", which is detailed in 5.4.3.10.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$03 (Confirmed DTCs).
<u>EVALUATION</u>	Pass if the <b>15765_29_SID\$03</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2_MIN</i> delay, transmit the message <b>15765_29_ECU1_SID\$03_DTC</b> , delay <i>P2_MIN</i> , then transmit the message <b>15765_29_ECU2_SID\$03_NoDTC</b> .
<u>EVALUATION</u>	Pass if the following is true: The transmission is successful. The DUT indicates that ECU #1 has the following confirmed DTCs: P0107, P0110, P0115, and P0124. The DUT indicates that ECU #2 has no confirmed DTCs.

**Figure 80 - Verify an ISO 15765-4 SID \$03 confirmed DTC request with DTCs (29-bit, 500K)**

## 9.2.23 Verify a SID \$04 Clear Codes Request (11-bit, 500K; 2 ECUs w/ Order Swap)

<b>Test ID:</b>	\$30104000
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$04.
<b>Rationale:</b>	This test will use positive responses from two different ECUs. The order of ECU responses will be different than that of the original connection order.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 15765 (11-bit, 500K, 2 ECUs) Initialized State”, which is detailed in 5.4.3.9.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$04 (Clear DTCs).
<u>EVALUATION</u>	Pass if the <b>15765_11_SID\$04</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2_MIN</i> delay, transmit the message <b>15765_11_ECU2_SID\$04_DONE</b> , delay <i>P2_MIN</i> , then transmit the message <b>15765_11_ECU1_SID\$04_DONE</b> .
<u>EVALUATION</u>	Pass if the transmission is successful and the DUT indicates that DTCs have been cleared.

**Figure 81 - Verify an ISO 15765-4 SID \$04 clear codes request (11-bit, 500K)**

## 9.2.24 Verify a SID \$04 Clear Codes Request (29-bit, 500K; 2 ECUs w/ Order Swap)

<b>Test ID:</b>	\$30104001
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$04.
<b>Rationale:</b>	This test will use positive responses from two different ECUs. The order of ECU responses will be different than that of the original connection order.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 15765 (29-bit, 500K, 2 ECUs) Initialized State”, which is detailed in 5.4.3.10.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$04 (Clear DTCs).
<u>EVALUATION</u>	Pass if the <b>15765_29_SID\$04</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2_MIN</i> delay, transmit the message <b>15765_29_ECU2_SID\$04_DONE</b> , delay <i>P2_MIN</i> , then transmit the message <b>15765_29_ECU1_SID\$04_DONE</b> .
<u>EVALUATION</u>	Pass if the transmission is successful and the DUT indicates that DTCs have been cleared.

**Figure 82 - Verify an ISO 15765-4 SID \$04 clear codes request (29-bit, 500K)**

## 9.2.25 Test a SID \$04 Clear Codes Request with NRC=\$22 (11-bit, 500K; 2 ECUs)

<b>Test ID:</b>	\$30104002
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$04 when an ECU indicates that conditions are not correct.
<b>Rationale:</b>	This test will use negative responses (NRC \$22) from two different ECUs.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 15765 (11-bit, 500K, 2 ECUs) Initialized State”, which is detailed in 5.4.3.9.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$04 (Clear DTCs).
<u>EVALUATION</u>	Pass if the <b>15765_11_SID\$04</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2_MIN</i> delay, transmit the message <b>15765_11_ECU1_SID\$04_NRC22</b> , delay <i>P2_MIN</i> , then transmit the message <b>15765_11_ECU2_SID\$04_NRC22</b> .
<u>EVALUATION</u>	Pass if the transmission is successful and the DUT indicates that DTCs have NOT been cleared.

**Figure 83 - Test an ISO 15765-4 SID \$04 clear codes request with NRC=\$22 (11-bit, 500K)**

## 9.2.26 Test a SID \$04 Clear Codes Request with NRC=\$22 (29-bit, 500K; 2 ECUs)

<b>Test ID:</b>	\$30104003
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$04 when an ECU indicates that conditions are not correct.
<b>Rationale:</b>	This test will use negative responses (NRC \$22) from two different ECUs.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 15765 (29-bit, 500K, 2 ECUs) Initialized State”, which is detailed in 5.4.3.10.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$04 (Clear DTCs).
<u>EVALUATION</u>	Pass if the <b>15765_29_SID\$04</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2_MIN</i> delay, transmit the message <b>15765_29_ECU1_SID\$04_NRC22</b> , delay <i>P2_MIN</i> , then transmit the message <b>15765_29_ECU2_SID\$04_NRC22</b> .
<u>EVALUATION</u>	Pass if the transmission is successful and the DUT indicates that DTCs have NOT been cleared.

**Figure 84 - Test an ISO 15765-4 SID \$04 clear codes request with NRC=\$22 (29-bit, 500K)**

## 9.2.27 Verify a SID \$04 Clear Codes Request with NRC=\$78 (11-bit, 500K; 2 ECUs)

<b>Test ID:</b>	\$30104004
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$04 when an ECU indicates that it is busy then eventually competes.
<b>Rationale:</b>	This test will use positive responses from the first ECU and a response pending (NRC \$78) from a second ECU.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the "ISO 15765 (11-bit, 500K, 2 ECUs) Initialized State", which is detailed in 5.4.3.9.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$04 (Clear DTCs).
<u>EVALUATION</u>	Pass if the <b>15765_11_SID\$04</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2_MIN</i> delay, transmit the message <b>15765_11_ECU1_SID\$04_NRC78</b> , delay <i>P2_MIN</i> , then transmit the message <b>15765_11_ECU2_SID\$04_DONE</b> .
<u>EVALUATION</u>	Pass if the transmission is successful.
<u>ACTION- ECUSIM</u>	After the <i>P2_78</i> delay, transmit the message <b>15765_11_ECU1_SID\$04_DONE</b>
<u>EVALUATION</u>	Pass if the transmission is successful and the DUT indicates that DTCs have been cleared.

**Figure 85 - Verify an ISO 15765-4 SID \$04 clear codes request with NRC=\$78 (11-bit, 500K)**

## 9.2.28 Verify a SID \$04 Clear Codes Request with NRC=\$78 (29-bit, 500K; 2 ECUs)

<b>Test ID:</b>	\$30104005
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$04 when an ECU indicates that it is busy then eventually competes.
<b>Rationale:</b>	This test will use positive responses from the first ECU and a response pending (NRC \$78) from a second ECU.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the "ISO 15765 (29-bit, 500K, 2 ECUs) Initialized State", which is detailed in 5.4.3.10.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$04 (Clear DTCs).
<u>EVALUATION</u>	Pass if the <b>15765_29_SID\$04</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2_MIN</i> delay, transmit the message <b>15765_29_ECU1_SID\$04_NRC78</b> , delay <i>P2_MIN</i> , then transmit the message <b>15765_29_ECU2_SID\$04_DONE</b> .
<u>EVALUATION</u>	Pass if the transmission is successful.
<u>ACTION- ECUSIM</u>	After the <i>P2_78</i> delay, transmit the message <b>15765_29_ECU1_SID\$04_DONE</b>
<u>EVALUATION</u>	Pass if the transmission is successful and the DUT indicates that DTCs have been cleared.

**Figure 86 - Verify an ISO 15765-4 SID \$04 clear codes request with NRC=\$78 (29-bit, 500K)**

## 9.2.29 Verify a SID \$06 MID Supported MID Request (11-bit, 500K; 1 ECU)

<b>Test ID:</b>	\$30106000
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$06 MID Supported MIDs.
<b>Rationale:</b>	This test will use a positive response from a single ECU.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 15765 (11-bit, 500K, 2 ECUs) Initialized State”, which is detailed in 5.4.3.9.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$06 MID \$00 (MID Supported MIDs).
<u>EVALUATION</u>	Pass if the <b>15765_11_SID\$06_MID\$00</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2_MIN</i> delay, transmit the message <b>15765_11_ECU1_SID\$06_MID\$00</b> .
<u>EVALUATION</u>	Pass if the transmission is successful and the DUT indicates that ECU #1 supports MIDs \$01, \$02, \$05, and \$06.

**Figure 87 - Verify an ISO 15765-4 SID \$06 MID supported MID request (11-bit, 500K)**

## 9.2.30 Verify a SID \$06 MID Supported MID Request (29-bit, 500K; 1 ECU)

<b>Test ID:</b>	\$30106001
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$06 MID Supported MIDs.
<b>Rationale:</b>	This test will use a positive response from a single ECU.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 15765 (29-bit, 500K, 2 ECUs) Initialized State”, which is detailed in 5.4.3.10.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$06 MID \$00 (MID Supported MIDs).
<u>EVALUATION</u>	Pass if the <b>15765_29_SID\$06_MID\$00</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2_MIN</i> delay, transmit the message <b>15765_29_ECU1_SID\$06_MID\$00</b> .
<u>EVALUATION</u>	Pass if the transmission is successful and the DUT indicates that ECU #1 supports MIDs \$01, \$02, \$05, and \$06.

**Figure 88 - Verify an ISO 15765-4 SID \$06 MID supported MID request (29-bit, 500K)**

## 9.2.31 Verify a SID \$07 Pending DTC Request with No DTCs (11-bit, 500K; 2 ECUs w/ Order Swap)

<b>Test ID:</b>	\$30107000
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$07 and process a 'no DTCs present' response.
<b>Rationale:</b>	This test will use positive responses from two different ECUs. The order of ECU responses will be different than that of the original connection order.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the "ISO 15765 (11-bit, 500K, 2 ECUs) Initialized State", which is detailed in 5.4.3.9.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$07 (Pending DTCs).
<u>EVALUATION</u>	Pass if the <b>15765_11_SID\$07</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2_MIN</i> delay, transmit the message <b>15765_11_ECU2_SID\$07_NoDTC</b> , delay <i>P2_MIN</i> , then transmit the message <b>15765_11_ECU1_SID\$07_NoDTC</b> .
<u>EVALUATION</u>	Pass if transmission is successful and the DUT indicates that there are no pending DTCs in either ECU.

**Figure 89 - Verify an ISO 15765-4 SID \$07 pending DTC request with no DTCs (11-bit, 500K)**

## 9.2.32 Verify a SID \$07 Pending DTC Request with No DTCs (29-bit, 500K; 2 ECUs w/ Order Swap)

<b>Test ID:</b>	\$30107001
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$07 and process a 'no DTCs present' response.
<b>Rationale:</b>	This test will use positive responses from two different ECUs. The order of ECU responses will be different than that of the original connection order.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the "ISO 15765 (29-bit, 500K, 2 ECUs) Initialized State", which is detailed in 5.4.3.10.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$07 (Pending DTCs).
<u>EVALUATION</u>	Pass if the <b>15765_29_SID\$07</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2_MIN</i> delay, transmit the message <b>15765_29_ECU2_SID\$07_NoDTC</b> , delay <i>P2_MIN</i> , then transmit the message <b>15765_29_ECU1_SID\$07_NoDTC</b> .
<u>EVALUATION</u>	Pass if transmission is successful and the DUT indicates that there are no pending DTCs in either ECU.

**Figure 90 - Verify an ISO 15765-4 SID \$07 pending DTC request with no DTCs (29-bit, 500K)**

## 9.2.33 Verify a SID \$07 Pending DTC Request with DTCs (11-bit, 500K; 2 ECUs)

<b>Test ID:</b>	\$30107002
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$07 and process a 'DTCs present' response.
<b>Rationale:</b>	This test will use positive responses from two different ECUs.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the "ISO 15765 (11-bit, 500K, 2 ECUs) Initialized State", which is detailed in 5.4.3.9.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$07 (Pending DTCs).
<u>EVALUATION</u>	Pass if the <b>15765_11_SID\$07</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2_MIN</i> delay, transmit the message <b>15765_11_ECU1_SID\$07_DTC</b> , delay <i>P2_MIN</i> , then transmit the message <b>15765_11_ECU2_SID\$07_NoDTC</b> .
<u>EVALUATION</u>	Pass if the following is true: The transmission is successful. The DUT indicates that ECU #1 has the following pending DTCs: P0107, P0110, P0115, and P0124. The DUT indicates that ECU #2 has no pending DTCs.

**Figure 91 - Verify an ISO 15765-4 SID \$07 pending DTC request with DTCs (11-bit, 500K)**

## 9.2.34 Verify a SID \$07 Pending DTC Request with DTCs (29-bit, 500K; 2 ECUs)

<b>Test ID:</b>	\$30107003
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$07 and process a 'DTCs present' response.
<b>Rationale:</b>	This test will use positive responses from two different ECUs.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the "ISO 15765 (29-bit, 500K, 2 ECUs) Initialized State", which is detailed in 5.4.3.10.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$07 (Pending DTCs).
<u>EVALUATION</u>	Pass if the <b>15765_29_SID\$07</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2_MIN</i> delay, transmit the message <b>15765_29_ECU1_SID\$07_DTC</b> , delay <i>P2_MIN</i> , then transmit the message <b>15765_29_ECU2_SID\$07_NoDTC</b> .
<u>EVALUATION</u>	Pass if the following is true: The transmission is successful. The DUT indicates that ECU #1 has the following pending DTCs: P0107, P0110, P0115, and P0124. The DUT indicates that ECU #2 has no pending DTCs.

**Figure 92 - Verify an ISO 15765-4 SID \$07 pending DTC request with DTCs (29-bit, 500K)**

## 9.2.35 Verify a SID \$08 TID Supported TID Request (11-bit, 500K; 1 ECU)

<b>Test ID:</b>	\$30108000
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$08 TID Supported TIDs.
<b>Rationale:</b>	This test will use a positive response from a single ECU.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 15765 (11-bit, 500K, 2 ECUs) Initialized State”, which is detailed in 5.4.3.9.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$08 TID \$00 (TID Supported TIDs).
<u>EVALUATION</u>	Pass if the <b>15765_11_SID\$08_PID\$00</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2_MIN</i> delay, transmit the message <b>15765_11_ECU1_SID\$08_PID\$00</b> .
<u>EVALUATION</u>	Pass if the transmission is successful and the DUT indicates that ECU #1 supports TID \$01 (the leak test).

**Figure 93 - Verify an ISO 15765-4 SID \$08 TID supported TID request (11-bit, 500K)**

## 9.2.36 Verify a SID \$08 TID Supported TID Request (29-bit, 500K; 1 ECU)

<b>Test ID:</b>	\$30108001
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$08 TID Supported TIDs.
<b>Rationale:</b>	This test will use a positive response from a single ECU.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 15765 (29-bit, 500K, 2 ECUs) Initialized State”, which is detailed in 5.4.3.10.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$08 TID \$00 (TID Supported TIDs).
<u>EVALUATION</u>	Pass if the <b>15765_29_SID\$08_PID\$00</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2_MIN</i> delay, transmit the message <b>15765_29_ECU1_SID\$08_PID\$00</b> .
<u>EVALUATION</u>	Pass if the transmission is successful and the DUT indicates that ECU #1 supports TID \$01 (the leak test).

**Figure 94 - Verify an ISO 15765-4 SID \$08 TID supported TID request (29-bit, 500K)**

## 9.2.37 Verify a SID \$09 INF Supported INF Request (11-bit, 500K; 2 ECUs w/ Order Swap)

<b>Test ID:</b>	\$30109000
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$09 INF Supported INFs.
<b>Rationale:</b>	This test will use positive responses from two different ECUs. The order of ECU responses will be different than that of the original connection order.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 15765 (11-bit, 500K, 2 ECUs) Initialized State”, which is detailed in 5.4.3.9.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$09 INF \$00 (INF Supported INFs).
<u>EVALUATION</u>	Pass if the <b>15765_11_SID\$09_PID\$00</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2_MIN</i> delay, transmit the message <b>15765_11_ECU2_SID\$09_INF\$00</b> , delay <i>P2_MIN</i> , then transmit the message <b>15765_11_ECU1_SID\$09_INF\$00</b> .
<u>EVALUATION</u>	Pass if the transmission is successful and the DUT indicates that ECU #1 supports INF \$02 (VIN), INF \$04 (CALID), INF \$06 (CVN), INF \$08 (IPT), and INF \$0A (ECU Name) while ECU #2 supports INF \$04 (CALID), INF \$06 (CVN), and INF \$0A (ECU Name).

**Figure 95 - Verify an ISO 15765-4 SID \$09 INF supported INF request (11-bit, 500K)**

## 9.2.38 Verify a SID \$09 INF Supported INF Request Diesel (11-bit, 500K; 2 ECUs w/ Order Swap)

<b>Test ID:</b>	\$30109001
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$09 INF Supported INFs.
<b>Rationale:</b>	This test will use positive responses from two different ECUs. The order of ECU responses will be different than that of the original connection order.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 15765 (11-bit, 500K, 2 ECUs) Initialized State”, which is detailed in 5.4.3.9.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$09 INF \$00 (INF Supported INFs).
<u>EVALUATION</u>	Pass if the <b>15765_11_SID\$09_PID\$00</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2_MIN</i> delay, transmit the message <b>15765_11_ECU2_SID\$09_INF\$00_DIESEL</b> , delay <i>P2_MIN</i> , then transmit the message <b>15765_11_ECU1_SID\$09_INF\$00_DIESEL</b> .
<u>EVALUATION</u>	Pass if the transmission is successful and the DUT indicates that ECU #1 supports INF \$02 (VIN), INF \$04 (CALID), INF \$06 (CVN), INF \$0A (ECU Name), INF \$0B (IPT), INF \$0D (ESN), and INF \$0F (EROTAN) while ECU #2 supports INF \$04 (CALID), INF \$06 (CVN), INF \$0A (ECU Name), INF \$0B (IPT), INF \$0D (ESN), and INF \$0F (EROTAN).

**Figure 96 - Verify an ISO 15765-4 SID \$09 INF supported INF request (11-bit, 500K)**

## 9.2.39 Verify a SID \$09 INF Supported INF Request (29-bit, 500K; 2 ECUs w/ Order Swap)

<b>Test ID:</b>	\$30109002
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$09 INF Supported INFs.
<b>Rationale:</b>	This test will use positive responses from two different ECUs. The order of ECU responses will be different than that of the original connection order.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 15765 (29-bit, 500K, 2 ECUs) Initialized State”, which is detailed in 5.4.3.10.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$09 INF \$00 (INF Supported INFs).
<u>EVALUATION</u>	Pass if the <b>15765_29_SID\$09_PID\$00</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2_MIN</i> delay, transmit the message <b>15765_29_ECU2_SID\$09_INF\$00</b> , delay <i>P2_MIN</i> , then transmit the message <b>15765_29_ECU1_SID\$09_INF\$00</b> .
<u>EVALUATION</u>	Pass if the transmission is successful and the DUT indicates that: ECU #1 supports INF \$02 (VIN), INF \$04 (CALID), INF \$06 (CVN), INF \$08 (IPT), and INF \$0A (ECU Name) ECU #2 supports INF \$04 (CALID), INF \$06 (CVN), and INF \$0A (ECU Name).

**Figure 97 - Verify an ISO 15765-4 SID \$09 INF supported INF request (29-bit, 500K)**

## 9.2.40 Verify a SID \$09 INF Supported INF Request Diesel (29-bit, 500K; 2 ECUs w/ Order Swap)

<b>Test ID:</b>	\$30109003
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$09 INF Supported INFs.
<b>Rationale:</b>	This test will use positive responses from two different ECUs. The order of ECU responses will be different than that of the original connection order.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 15765 (29-bit, 500K, 2 ECUs) Initialized State”, which is detailed in 5.4.3.10.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$09 INF \$00 (INF Supported INFs).
<u>EVALUATION</u>	Pass if the <b>15765_29_SID\$09_PID\$00</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2_MIN</i> delay, transmit the message <b>15765_29_ECU2_SID\$09_INF\$00_DIESEL</b> , delay <i>P2_MIN</i> , then transmit the message <b>15765_29_ECU1_SID\$09_INF\$00_DIESEL</b> .
<u>EVALUATION</u>	Pass if the transmission is successful and the DUT indicates that: ECU #1 supports INF \$02 (VIN), INF \$04 (CALID), INF \$06 (CVN), INF \$0A (ECU Name), INF \$0B (IPT), INF \$0D (ESN), and INF \$0F (EROTAN) ECU #2 supports INF \$04 (CALID), INF \$06 (CVN), INF \$0A (ECU Name), INF \$0B (IPT), INF \$0D (ESN), and INF \$0F (EROTAN).

**Figure 98 - Verify an ISO 15765-4 SID \$09 INF supported INF request (29-bit, 500K)**

## 9.2.41 Verify a SID \$09 INF Supported INF Request (11-bit, 500K; 8 ECUs w/ Order Swap)

<b>Test ID:</b>	\$30109004
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$09 INF Supported INFs.
<b>Rationale:</b>	This test will use positive responses from eight different ECUs. The order of ECU responses will be different than that of the original connection order.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 15765 (11-bit, 500K, 8 ECUs) Initialized State”, which is detailed in 5.4.3.11.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$09 INF \$00 (INF Supported INFs).
<u>EVALUATION</u>	Pass if the <b>15765_11_SID\$09_PID\$00</b> message was transmitted.
<u>ACTION- ECUSIM</u>	
	Delay <i>P2_MIN</i> before transmitting each of the following messages <b>15765_11_ECU5_SID\$09_INF\$00, 15765_11_ECU2_SID\$09_INF\$00,  15765_11_ECU6_SID\$09_INF\$00, 15765_11_ECU3_SID\$09_INF\$00,  15765_11_ECU7_SID\$09_INF\$00, 15765_11_ECU4_SID\$09_INF\$00,  15765_11_ECU1_SID\$09_INF\$00 and 15765_11_ECU8_SID\$09_INF\$00.</b>
<u>EVALUATION</u>	Pass if the transmission is successful and the DUT indicates that: ECU #1 supports INF \$02 (VIN), INF \$04 (CALID), INF \$06 (CVN), INF \$08 (IPT), and INF \$0A (ECU Name). ECU #2 supports INF \$04 (CALID), INF \$06 (CVN), and INF \$0A (ECU Name). ECU #3 supports INF \$04 (CALID), INF \$06 (CVN), and INF \$0A (ECU Name). ECU #4 supports INF \$04 (CALID), INF \$06 (CVN), and INF \$0A (ECU Name). ECU #5 supports INF \$04 (CALID), INF \$06 (CVN), and INF \$0A (ECU Name). ECU #6 supports INF \$04 (CALID), INF \$06 (CVN), and INF \$0A (ECU Name). ECU #7 supports INF \$04 (CALID), INF \$06 (CVN), and INF \$0A (ECU Name). ECU #8 supports INF \$04 (CALID), INF \$06 (CVN), and INF \$0A (ECU Name).

**Figure 99 - Verify an ISO 15765-4 SID \$09 INF supported INF request (11-bit, 500K)**

## 9.2.42 Verify a SID \$09 INF Supported INF Request Diesel (11-bit, 500K; 8 ECUs w/ Order Swap)

<b>Test ID:</b>	\$30109005
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$09 INF Supported INFs.
<b>Rationale:</b>	This test will use positive responses from eight different ECUs. The order of ECU responses will be different than that of the original connection order.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 15765 (11-bit, 500K, 8 ECUs) Initialized State”, which is detailed in 5.4.3.11.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$09 INF \$00 (INF Supported INFs).
<u>EVALUATION</u>	Pass if the <b>15765_11_SID\$09_PID\$00</b> message was transmitted.
<u>ACTION- ECUSIM</u>	
	Delay <i>P2_MIN</i> before transmitting each of the following messages <b>15765_11_ECU5_SID\$09_INF\$00_DIESEL, 15765_11_ECU2_SID\$09_INF\$00_DIESEL,  15765_11_ECU6_SID\$09_INF\$00_DIESEL, 15765_11_ECU3_SID\$09_INF\$00_DIESEL,  15765_11_ECU7_SID\$09_INF\$00_DIESEL, 15765_11_ECU4_SID\$09_INF\$00_DIESEL,  15765_11_ECU1_SID\$09_INF\$00_DIESEL and  15765_11_ECU8_SID\$09_INF\$00_DIESEL.</b>
<u>EVALUATION</u>	Pass if the transmission is successful and the DUT indicates that: ECU #1 supports INF \$02 (VIN), INF \$04 (CALID), INF \$06 (CVN), INF \$0A (ECU Name), INF \$0B (IPT), INF \$0D (ESN), and INF \$0F (EROTAN). ECU #2 supports INF \$04 (CALID), INF \$06 (CVN), INF \$0A (ECU Name), INF \$0B (IPT), INF \$0D (ESN), and INF \$0F (EROTAN). ECU #3 supports INF \$04 (CALID), INF \$06 (CVN), INF \$0A (ECU Name), INF \$0B (IPT), INF \$0D (ESN), and INF \$0F (EROTAN). ECU #4 supports INF \$04 (CALID), INF \$06 (CVN), INF \$0A (ECU Name), INF \$0B (IPT), INF \$0D (ESN), and INF \$0F (EROTAN). ECU #5 supports INF \$04 (CALID), INF \$06 (CVN), INF \$0A (ECU Name), INF \$0B (IPT), INF \$0D (ESN), and INF \$0F (EROTAN). ECU #6 supports INF \$04 (CALID), INF \$06 (CVN), INF \$0A (ECU Name), INF \$0B (IPT), INF \$0D (ESN), and INF \$0F (EROTAN). ECU #7 supports INF \$04 (CALID), INF \$06 (CVN), INF \$0A (ECU Name), INF \$0B (IPT), INF \$0D (ESN), and INF \$0F (EROTAN). ECU #8 supports INF \$04 (CALID), INF \$06 (CVN), INF \$0A (ECU Name), INF \$0B (IPT), INF \$0D (ESN), and INF \$0F (EROTAN).

**Figure 100 - Verify an ISO 15765-4 SID \$09 INF supported INF request (11-bit, 500K)**

## 9.2.43 Verify a SID \$09 INF Supported INF Request (29-bit, 500K; 8 ECUs w/ Order Swap)

<b>Test ID:</b>	\$30109006
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$09 INF Supported INFs.
<b>Rationale:</b>	This test will use positive responses from eight different ECUs. The order of ECU responses will be different than that of the original connection order.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 15765 (29-bit, 500K, 8 ECUs) Initialized State”, which is detailed in 5.4.3.12.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$09 INF \$00 (INF Supported INFs).
<u>EVALUATION</u>	Pass if the <b>15765_29_SID\$09_PID\$00</b> message was transmitted.
<u>ACTION- ECUSIM</u>	
	Delay <i>P2_MIN</i> before transmitting each of the following messages <b>15765_29_ECU5_SID\$09_INF\$00, 15765_29_ECU2_SID\$09_INF\$00,  15765_29_ECU6_SID\$09_INF\$00, 15765_29_ECU3_SID\$09_INF\$00,  15765_29_ECU7_SID\$09_INF\$00, 15765_29_ECU4_SID\$09_INF\$00,  15765_29_ECU1_SID\$09_INF\$00 and 15765_29_ECU8_SID\$09_INF\$00.</b>
<u>EVALUATION</u>	Pass if the transmission is successful and the DUT indicates that: ECU #1 supports INF \$02 (VIN), INF \$04 (CALID), INF \$06 (CVN), INF \$08 (IPT), and INF \$0A (ECU Name). ECU #2 supports INF \$04 (CALID), INF \$06 (CVN), and INF \$0A (ECU Name). ECU #3 supports INF \$04 (CALID), INF \$06 (CVN), and INF \$0A (ECU Name). ECU #4 supports INF \$04 (CALID), INF \$06 (CVN), and INF \$0A (ECU Name). ECU #5 supports INF \$04 (CALID), INF \$06 (CVN), and INF \$0A (ECU Name). ECU #6 supports INF \$04 (CALID), INF \$06 (CVN), and INF \$0A (ECU Name). ECU #7 supports INF \$04 (CALID), INF \$06 (CVN), and INF \$0A (ECU Name). ECU #8 supports INF \$04 (CALID), INF \$06 (CVN), and INF \$0A (ECU Name).

**Figure 101 - Verify an ISO 15765-4 SID \$09 INF supported INF request (29-bit, 500K)**

## 9.2.44 Verify a SID \$09 INF Supported INF Request Diesel (29-bit, 500K; 8 ECUs w/ Order Swap)

<b>Test ID:</b>	\$30109007
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$09 INF Supported INFs.
<b>Rationale:</b>	This test will use positive responses from eight different ECUs. The order of ECU responses will be different than that of the original connection order.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 15765 (29-bit, 500K, 8 ECUs) Initialized State”, which is detailed in 5.4.3.12.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$09 INF \$00 (INF Supported INFs).
<u>EVALUATION</u>	Pass if the <b>15765_29_SID\$09_PID\$00</b> message was transmitted.
<u>ACTION- ECUSIM</u>	
	Delay <i>P2_MIN</i> before transmitting each of the following messages <b>15765_29_ECU5_SID\$09_INF\$00_DIESEL, 15765_29_ECU2_SID\$09_INF\$00_DIESEL, 15765_29_ECU6_SID\$09_INF\$00_DIESEL, 15765_29_ECU3_SID\$09_INF\$00_DIESEL, 15765_29_ECU7_SID\$09_INF\$00_DIESEL, 15765_29_ECU4_SID\$09_INF\$00_DIESEL, 15765_29_ECU1_SID\$09_INF\$00_DIESEL and 15765_29_ECU8_SID\$09_INF\$00_DIESEL.</b>
<u>EVALUATION</u>	Pass if the transmission is successful and the DUT indicates that: ECU #1 supports INF \$02 (VIN), INF \$04 (CALID), INF \$06 (CVN), INF \$0A (ECU Name), INF \$0B (IPT), INF \$0D (ESN), and INF \$0F (EROTAN). ECU #2 supports INF \$04 (CALID), INF \$06 (CVN), INF \$0A (ECU Name), INF \$0B (IPT), INF \$0D (ESN), and INF \$0F (EROTAN). ECU #3 supports INF \$04 (CALID), INF \$06 (CVN), INF \$0A (ECU Name), INF \$0B (IPT), INF \$0D (ESN), and INF \$0F (EROTAN). ECU #4 supports INF \$04 (CALID), INF \$06 (CVN), INF \$0A (ECU Name), INF \$0B (IPT), INF \$0D (ESN), and INF \$0F (EROTAN). ECU #5 supports INF \$04 (CALID), INF \$06 (CVN), INF \$0A (ECU Name), INF \$0B (IPT), INF \$0D (ESN), and INF \$0F (EROTAN). ECU #6 supports INF \$04 (CALID), INF \$06 (CVN), INF \$0A (ECU Name), INF \$0B (IPT), INF \$0D (ESN), and INF \$0F (EROTAN). ECU #7 supports INF \$04 (CALID), INF \$06 (CVN), INF \$0A (ECU Name), INF \$0B (IPT), INF \$0D (ESN), and INF \$0F (EROTAN). ECU #8 supports INF \$04 (CALID), INF \$06 (CVN), INF \$0A (ECU Name), INF \$0B (IPT), INF \$0D (ESN), and INF \$0F (EROTAN).

**Figure 102 - Verify an ISO 15765-4 SID \$09 INF supported INF request (29-bit, 500K)**

## 9.2.45 Verify a SID \$09 VIN Request (11-bit, 500K; 1 ECU)

<b>Test ID:</b>	\$30109008
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$09 INF \$02.
<b>Rationale:</b>	This test will use a positive response from a single ECU.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 15765 (11-bit, 500K, 2 ECUs) SID9 Ready State”, which is detailed in 5.4.3.13.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$09 INF \$02 (VIN).
<u>EVALUATION</u>	Pass if the <b>15765_11_SID\$09_INF\$02</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2_MIN</i> delay, transmit the message <b>15765_11_ECU1_SID\$09_INF\$02</b> .
<u>EVALUATION</u>	Pass if the transmission is successful and the DUT indicates that VIN is 1GYDE63A7A0000000.

**Figure 103 - Verify an ISO 15765-4 SID \$09 VIN request (11-bit, 500K)**

## 9.2.46 Verify a SID \$09 VIN Request (29-bit, 500K; 1 ECU)

<b>Test ID:</b>	\$30109009
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$09 INF \$02.
<b>Rationale:</b>	This test will use a positive response from a single ECU.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 15765 (29-bit, 500K, 2 ECUs) SID9 Ready State”, which is detailed in 5.4.3.15.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$09 INF \$02 (VIN).
<u>EVALUATION</u>	Pass if the <b>15765_29_SID\$09_INF\$02</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2_MIN</i> delay, transmit the message <b>15765_29_ECU1_SID\$09_INF\$02</b> .
<u>EVALUATION</u>	Pass if the transmission is successful and the DUT indicates that VIN is 1GYDE63A7A0000000.

**Figure 104 - Verify an ISO 15765-4 SID \$09 VIN request (29-bit, 500K)**

## 9.2.47 Verify a SID \$09 VIN Request with NRC=\$78 (11-bit, 500K; 1 ECU)

<b>Test ID:</b>	\$3010900A
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$09 INF \$02 when the ECU has an initial NRC = \$78.
<b>Rationale:</b>	This test will use a response pending (NRC \$78) from a single ECU.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 15765 (11-bit, 500K, 2 ECUs) SID9 Ready State”, which is detailed in 5.4.3.13.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$09 INF \$02 (VIN).
<u>EVALUATION</u>	Pass if the <b>15765_11_SID\$09_INF\$02</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2_MIN</i> delay, transmit the message <b>15765_11_ECU1_SID\$09_NRC78</b> .
<u>EVALUATION</u>	Pass if the <b>15765_11_ECU1_SID\$09_NRC78</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2_MIN</i> delay, transmit the message <b>15765_11_ECU1_SID\$09_INF\$02</b> .
<u>EVALUATION</u>	Pass if the transmission is successful and the DUT indicates that VIN is 1GYDE63A7A0000000.

**Figure 105 - Verify an ISO 15765-4 SID \$09 VIN request with NRC=\$78 (11-bit, 500K)**

## 9.2.48 Verify a SID \$09 VIN Request with NRC=\$78 (29-bit, 500K; 1 ECU)

<b>Test ID:</b>	\$3010900B
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$09 INF \$02 when the ECU has an initial NRC = \$78.
<b>Rationale:</b>	This test will use a response pending (NRC \$78) from a single ECU.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 15765 (29-bit, 500K, 2 ECUs) SID9 Ready State”, which is detailed in 5.4.3.15.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$09 INF \$02 (VIN).
<u>EVALUATION</u>	Pass if the <b>15765_29_SID\$09_INF\$02</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2_MIN</i> delay, transmit the message <b>15765_29_ECU1_SID\$09_NRC78</b> .
<u>EVALUATION</u>	Pass if the <b>15765_29_ECU1_SID\$09_NRC78</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2_MIN</i> delay, transmit the message <b>15765_29_ECU1_SID\$09_INF\$02</b> .
<u>EVALUATION</u>	Pass if the transmission is successful and the DUT indicates that VIN is 1GYDE63A7A0000000.

**Figure 106 - Verify an ISO 15765-4 SID \$09 VIN request with NRC=\$78 (29-bit, 500K)**

## 9.2.49 Verify a SID \$09 CALID Request (11-bit, 500K; 2 ECUs w/ Order Swap)

<b>Test ID:</b>	\$3010900C
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$09 INF \$04.
<b>Rationale:</b>	This test will use positive responses from two different ECUs. The order of ECU responses will be different than that of the original connection order.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 15765 (11-bit, 500K, 2 ECUs) SID9 Ready State”, which is detailed in 5.4.3.13.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$09 INF \$04 (CALID).
<u>EVALUATION</u>	Pass if the <b>15765_11_SID\$09_INF\$04</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2_MIN</i> delay, transmit the message <b>15765_11_ECU2_SID\$09_INF\$04</b> , delay <i>P2_MIN</i> , then transmit the message <b>15765_11_ECU1_SID\$09_INF\$04</b> .
<u>EVALUATION</u>	Pass if the transmission is successful and the DUT indicates that the CALID for ECU #1 is 12589772 while CALID for ECU #2 is 24221419.

**Figure 107 - Verify an ISO 15765-4 SID \$09 CALID request (11-bit, 500K)**

## 9.2.50 Verify a SID \$09 CALID Request (29-bit, 500K; 2 ECUs w/ Order Swap)

<b>Test ID:</b>	\$3010900D
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$09 INF \$04.
<b>Rationale:</b>	This test will use positive responses from two different ECUs. The order of ECU responses will be different than that of the original connection order.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 15765 (29-bit, 500K, 2 ECUs) SID9 Ready State”, which is detailed in 5.4.3.15.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$09 INF \$04 (CALID).
<u>EVALUATION</u>	Pass if the <b>15765_29_SID\$09_INF\$04</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2_MIN</i> delay, transmit the message <b>15765_29_ECU2_SID\$09_INF\$04</b> , delay <i>P2_MIN</i> , then transmit the message <b>15765_29_ECU1_SID\$09_INF\$04</b> .
<u>EVALUATION</u>	Pass if the transmission is successful and the DUT indicates that the CALID for ECU #1 is 12589772 while CALID for ECU #2 is 24221419.

**Figure 108 - Verify an ISO 15765-4 SID \$09 CALID request (29-bit, 500K)**

## 9.2.51 Verify a SID \$09 CALID Request with 9 CALIDs (11-bit, 500K; 8 ECUs w/ Order Swap)

<b>Test ID:</b>	\$3010900E
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$09 INF \$04 when 8 ECUs respond with 9 CALIDs each.
<b>Rationale:</b>	This test will use positive responses from eight different ECUs. The order of ECU responses will be different than that of the original connection order with ECU3 through ECU8 indicating that no SID \$01 PIDs are supported.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the "ISO 15765 (11-bit, 500K, 8 ECUs) SID9 Ready State", which is detailed in 5.4.3.17.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$09 INF \$04 (CALID).
<u>EVALUATION</u>	Pass if the <b>15765_11_SID\$09_INF\$04</b> message was transmitted.
<u>ACTION- ECUSIM</u>	Delay <i>P2_MIN</i> before transmitting each of the following messages <b>15765_11_ECU5_SID\$09_INF\$04_9, 15765_11_ECU2_SID\$09_INF\$04_9,  15765_11_ECU6_SID\$09_INF\$04_9, 15765_11_ECU3_SID\$09_INF\$04_9,  15765_11_ECU7_SID\$09_INF\$04_9, 15765_11_ECU4_SID\$09_INF\$04_9,  15765_11_ECU1_SID\$09_INF\$04_9, 15765_11_ECU8_SID\$09_INF\$04_9.</b>
<u>EVALUATION</u>	Pass if the following is true: Transmission is successful. The DUT indicates that the CALID for ECU #1 are: "11589778", "21589777", "31589776", "41589775", "51589774", "61589773", "71589772", "81589771", "91589770". The DUT indicates that the CALID for ECU #2 are: "12589778", "22589777", "32589776", "42589775", "52589774", "62589773", "72589772", "82589771", "92589770". The DUT indicates that the CALID for ECU #3 are: "13589778", "23589777", "33589776", "43589775", "53589774", "63589773", "73589772", "83589771", "93589770". The DUT indicates that the CALID for ECU #4 are: "14589778", "24589777", "34589776", "44589775", "54589774", "64589773", "74589772", "84589771", "94589770". The DUT indicates that the CALID for ECU #5 are: "15589778", "25589777", "35589776", "45589775", "55589774", "65589773", "75589772", "85589771", "95589770". The DUT indicates that the CALID for ECU #6 are: "16589778", "26589777", "36589776", "46589775", "56589774", "66589773", "76589772", "86589771", "96589770". The DUT indicates that the CALID for ECU #7 are: "17589778", "27589777", "37589776", "47589775", "57589774", "67589773", "77589772", "87589771", "97589770". The DUT indicates that the CALID for ECU #8 are: "18589778", "28589777", "38589776", "48589775", "58589774", "68589773", "78589772", "88589771", "98589770".

**Figure 109 - Verify an ISO 15765-4 SID \$09 CALID request with 9 CALIDs on an ECU (11-bit, 500K)**

## 9.2.52 Verify a SID \$09 CALID Request with 9 CALIDs (29-bit, 500K; 8 ECUs w/ Order Swap)

<b>Test ID:</b>	\$3010900F
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$09 INF \$04 when 8 ECUs responds with 9 CALIDs each.
<b>Rationale:</b>	This test will use positive responses from eight different ECUs. The order of ECU responses will be different than that of the original connection order with ECU3 through ECU8 indicating that no SID \$01 PIDs are supported.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the "ISO 15765 (29-bit, 500K, 8 ECUs) SID9 Ready State", which is detailed in 5.4.3.19.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$09 INF \$04 (CALID).
<u>EVALUATION</u>	Pass if the <b>15765_29_SID\$09_INF\$04</b> message was transmitted.
<u>ACTION- ECUSIM</u>	Delay <i>P2_MIN</i> before transmitting each of the following messages <b>15765_29_ECU5_SID\$09_INF\$04_9, 15765_29_ECU2_SID\$09_INF\$04_9,  15765_29_ECU6_SID\$09_INF\$04_9, 15765_29_ECU3_SID\$09_INF\$04_9,  15765_29_ECU7_SID\$09_INF\$04_9, 15765_29_ECU4_SID\$09_INF\$04_9,  15765_29_ECU1_SID\$09_INF\$04_9, 15765_29_ECU8_SID\$09_INF\$04_9.</b>
<u>EVALUATION</u>	Pass if the following is true: Transmission is successful. The DUT indicates that the CALID for ECU #1 are: "11589778", "21589777", "31589776", "41589775", "51589774", "61589773", "71589772", "81589771", "91589770". The DUT indicates that the CALID for ECU #2 are: "12589778", "22589777", "32589776", "42589775", "52589774", "62589773", "72589772", "82589771", "92589770". The DUT indicates that the CALID for ECU #3 are: "13589778", "23589777", "33589776", "43589775", "53589774", "63589773", "73589772", "83589771", "93589770". The DUT indicates that the CALID for ECU #4 are: "14589778", "24589777", "34589776", "44589775", "54589774", "64589773", "74589772", "84589771", "94589770". The DUT indicates that the CALID for ECU #5 are: "15589778", "25589777", "35589776", "45589775", "55589774", "65589773", "75589772", "85589771", "95589770". The DUT indicates that the CALID for ECU #6 are: "16589778", "26589777", "36589776", "46589775", "56589774", "66589773", "76589772", "86589771", "96589770". The DUT indicates that the CALID for ECU #7 are: "17589778", "27589777", "37589776", "47589775", "57589774", "67589773", "77589772", "87589771", "97589770". The DUT indicates that the CALID for ECU #8 are: "18589778", "28589777", "38589776", "48589775", "58589774", "68589773", "78589772", "88589771", "98589770".

**Figure 110 - Verify an ISO 15765-4 SID \$09 CALID request with 9 CALIDs on an ECU (29-bit, 500K)**

## 9.2.53 Verify a SID \$09 CVN Request (11-bit, 500K; 2 ECU w/ Order Swap)

<b>Test ID:</b>	\$30109010
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$09 INF \$06 when 2 ECUs responds with 1 CVN each.
<b>Rationale:</b>	This test will use positive responses from two different ECUs. The order of ECU responses will be different than that of the original connection order.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 15765 (11-bit, 500K, 2 ECUs) SID9 Ready State”, which is detailed in 5.4.3.13.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$09 INF \$06 (CVN).
<u>EVALUATION</u>	Pass if the <b>15765_11_SID\$09_INF\$06</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2_MIN</i> delay, transmit the message <b>15765_11_ECU2_SID\$09_INF\$06</b> , delay <i>P2_MIN</i> , then transmit the message <b>15765_11_ECU1_SID\$09_INF\$06</b> .
<u>EVALUATION</u>	Pass if the transmission is successful and the DUT indicates that the CVN for ECU #1 is \$1791BC88 and the CVN for ECU #2 is \$2791BC88.

**Figure 111 - Verify an ISO 15765-4 SID \$09 CVN request (11-bit, 500K)**

## 9.2.54 Verify a SID \$09 CVN Request (29-bit, 500K; 2 ECU w/ Order Swap)

<b>Test ID:</b>	\$30109011
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$09 INF \$06 when 2 ECUs responds with 1 CVN each.
<b>Rationale:</b>	This test will use positive responses from two different ECUs. The order of ECU responses will be different than that of the original connection order.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 15765 (29-bit, 500K, 2 ECUs) SID9 Ready State”, which is detailed in 5.4.3.15.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$09 INF \$06 (CVN).
<u>EVALUATION</u>	Pass if the <b>15765_29_SID\$09_INF\$06</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2_MIN</i> delay, transmit the message <b>15765_29_ECU2_SID\$09_INF\$06</b> , delay <i>P2_MIN</i> , then transmit the message <b>15765_29_ECU1_SID\$09_INF\$06</b> .
<u>EVALUATION</u>	Pass if the transmission is successful and the DUT indicates that the CVN for ECU #1 is \$1791BC88 and the CVN for ECU #2 is \$2791BC88.

**Figure 112 - Verify an ISO 15765-4 SID \$09 CVN request (29-bit, 500K)**

## 9.2.55 Verify a SID \$09 CVN Request with 9 CVNs (11-bit, 500K; 8 ECUs w/ Order Swap)

<b>Test ID:</b>	\$30109012
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$09 INF \$06 when 8 ECUs responds with 9 CVNs each.
<b>Rationale:</b>	This test will use positive responses from eight different ECUs. The order of ECU responses will be different than that of the original connection order with ECU3 through ECU8 indicating that no SID \$01 PIDs are supported.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 15765 (11-bit, 500K, 8 ECUs) SID9 Ready State”, which is detailed in 5.4.3.17.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$09 INF \$06 (CVN).
<u>EVALUATION</u>	Pass if the <b>15765_11_SID\$09_INF\$06</b> message was transmitted.
<u>ACTION- ECUSIM</u>	Delay <i>P2_MIN</i> before transmitting each of the following messages <b>15765_11_ECU8_SID\$09_INF\$06_9, 15765_11_ECU1_SID\$09_INF\$06_9,  15765_11_ECU7_SID\$09_INF\$06_9, 15765_11_ECU2_SID\$09_INF\$06_9,  15765_11_ECU6_SID\$09_INF\$06_9, 15765_11_ECU3_SID\$09_INF\$06_9,  15765_11_ECU5_SID\$09_INF\$06_9, 15765_11_ECU4_SID\$09_INF\$06_9.</b>
<u>EVALUATION</u>	Pass if the following is true: Transmission is successful. The DUT indicates that the CVN for ECU #1 is \$1791BC88, \$1891BC87, \$1991BC86, \$1A91BC85, \$1B91BC84, \$1C91BC83, \$1D91BC82, \$1E55AA81, \$1FFF0080. The DUT indicates that the CVN for ECU #2 is \$2791BC88, \$2891BC87, \$2991BC86, \$2A91BC85, \$2B91BC84, \$2C91BC83, \$2D91BC82, \$2E55AA81, \$2FFF0080. The DUT indicates that the CVN for ECU #3 is \$3791BC88, \$3891BC87, \$3991BC86, \$3A91BC85, \$3B91BC84, \$3C91BC83, \$3D91BC82, \$3E55AA81, \$3FFF0080. The DUT indicates that the CVN for ECU #4 is \$4791BC88, \$4891BC87, \$4991BC86, \$4A91BC85, \$4B91BC84, \$4C91BC83, \$4D91BC82, \$4E55AA81, \$4FFF0080. The DUT indicates that the CVN for ECU #5 is \$5791BC88, \$5891BC87, \$5991BC86, \$5A91BC85, \$5B91BC84, \$5C91BC83, \$5D91BC82, \$5E55AA81, \$5FFF0080. The DUT indicates that the CVN for ECU #6 is \$6791BC88, \$6891BC87, \$6991BC86, \$6A91BC85, \$6B91BC84, \$6C91BC83, \$6D91BC82, \$6E55AA81, \$6FFF0080. The DUT indicates that the CVN for ECU #7 is \$7791BC88, \$7891BC87, \$7991BC86, \$7A91BC85, \$7B91BC84, \$7C91BC83, \$7D91BC82, \$7E55AA81, \$7FFF0080. The DUT indicates that the CVN for ECU #8 is \$8791BC88, \$8891BC87, \$8991BC86, \$8A91BC85, \$8B91BC84, \$8C91BC83, \$8D91BC82, \$8E55AA81, \$8FFF0080.

**Figure 113 - Verify an ISO 15765-4 SID \$09 CVN request with 9 CVNs on an ECU (11-bit, 500K)**

## 9.2.56 Verify a SID \$09 CVN Request with 9 CVNs (29-bit, 500K; 8 ECUs w/ Order Swap)

<b>Test ID:</b>	\$30109013
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$09 INF \$06 when 8 ECUs responds with 9 CVNs each.
<b>Rationale:</b>	This test will use positive responses from eight different ECUs. The order of ECU responses will be different than that of the original connection order with ECU3 through ECU8 indicating that no SID \$01 PIDs are supported.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 15765 (29-bit, 500K, 8 ECUs) SID9 Ready State”, which is detailed in 5.4.3.19.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$09 INF \$06 (CVN).
<u>EVALUATION</u>	Pass if the <b>15765_29_SID\$09_INF\$06</b> message was transmitted.
<u>ACTION- ECUSIM</u>	Delay <i>P2_MIN</i> before transmitting each of the following messages <b>15765_29_ECU8_SID\$09_INF\$06_9, 15765_29_ECU1_SID\$09_INF\$06_9,  15765_29_ECU7_SID\$09_INF\$06_9, 15765_29_ECU2_SID\$09_INF\$06_9,  15765_29_ECU6_SID\$09_INF\$06_9, 15765_29_ECU3_SID\$09_INF\$06_9,  15765_29_ECU5_SID\$09_INF\$06_9, 15765_29_ECU4_SID\$09_INF\$06_9.</b>
<u>EVALUATION</u>	Pass if the following is true: Transmission is successful. The DUT indicates that the CVN for ECU #1 is \$1791BC88, \$1891BC87, \$1991BC86, \$1A91BC85, \$1B91BC84, \$1C91BC83, \$1D91BC82, \$1E55AA81, \$1FFF0080. The DUT indicates that the CVN for ECU #2 is \$2791BC88, \$2891BC87, \$2991BC86, \$2A91BC85, \$2B91BC84, \$2C91BC83, \$2D91BC82, \$2E55AA81, \$2FFF0080. The DUT indicates that the CVN for ECU #3 is \$3791BC88, \$3891BC87, \$3991BC86, \$3A91BC85, \$3B91BC84, \$3C91BC83, \$3D91BC82, \$3E55AA81, \$3FFF0080. The DUT indicates that the CVN for ECU #4 is \$4791BC88, \$4891BC87, \$4991BC86, \$4A91BC85, \$4B91BC84, \$4C91BC83, \$4D91BC82, \$4E55AA81, \$4FFF0080. The DUT indicates that the CVN for ECU #5 is \$5791BC88, \$5891BC87, \$5991BC86, \$5A91BC85, \$5B91BC84, \$5C91BC83, \$5D91BC82, \$5E55AA81, \$5FFF0080. The DUT indicates that the CVN for ECU #6 is \$6791BC88, \$6891BC87, \$6991BC86, \$6A91BC85, \$6B91BC84, \$6C91BC83, \$6D91BC82, \$6E55AA81, \$6FFF0080. The DUT indicates that the CVN for ECU #7 is \$7791BC88, \$7891BC87, \$7991BC86, \$7A91BC85, \$7B91BC84, \$7C91BC83, \$7D91BC82, \$7E55AA81, \$7FFF0080. The DUT indicates that the CVN for ECU #8 is \$8791BC88, \$8891BC87, \$8991BC86, \$8A91BC85, \$8B91BC84, \$8C91BC83, \$8D91BC82, \$8E55AA81, \$8FFF0080.

**Figure 114 - Verify an ISO 15765-4 SID \$09 CVN request with 9 CVNs on an ECU (29-bit, 500K)**

## 9.2.57 Verify a SID \$09 IPT (INF\$08, 32 bytes) Request (11-bit, 500K; 2 ECUs w/ Order Swap)

<b>Test ID:</b>	\$30109014
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$09 INF \$08 (32 bytes).
<b>Rationale:</b>	This test will use positive responses from two ECUs. The order of ECU responses will be different than that of the original connection order.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the "ISO 15765 (11-bit, 500K, 2 ECUs) SID9 Ready State", which is detailed in 5.4.3.13.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$09 INF \$08 (IPT).
<u>EVALUATION</u>	Pass if the <b>15765_11_SID\$09_INF\$08</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2_MIN</i> delay, transmit the message <b>15765_11_ECU2_SID\$09_INF\$08_32</b> , delay <i>P2_MIN</i> , then transmit the message <b>15765_11_ECU1_SID\$09_INF\$08_32</b> .
<u>EVALUATION</u>	<p>Pass if the following is true:  Transmission is successful.  The DUT indicates that the IPT for ECU #1 is 32 bytes and has the following values:  OBDCOND = 100  IGNCNTR = 105  CATCOMP1 = 10  CATCOND1 = 15  CATCOMP2 = 00  CATCOND2 = 00  O2COMP1 = 30  O2COND1 = 35  O2COMP2 = 00  O2COND2 = 00  EGRCOMP = 50  EGRCOND = 55  AIRCOMP = 60  AIRCOND = 65  EVAPCOND = 70  EVAPCOND = 75</p> <p>The DUT indicates that the IPT for ECU #2 is 32 bytes and has the following values:  OBDCOND = 100  IGNCNTR = 105  CATCOMP1 = 00  CATCOND1 = 00  CATCOMP2 = 20  CATCOND2 = 25  O2COMP1 = 00  O2COND1 = 00  O2COMP2 = 40  O2COND2 = 45  EGRCOMP = 00  EGRCOND = 00  AIRCOMP = 00  AIRCOND = 00  EVAPCOND = 00  EVAPCOND = 00</p>

Figure 115 - Verify an ISO 15765-4 SID \$09 IPT (INF\$08, 32 bytes) request (11-bit, 500K)

## 9.2.58 Verify a SID \$09 IPT (INF\$08, 32 bytes) Request (29-bit, 500K; 2 ECUs w/ Order Swap)

<b>Test ID:</b>	\$30109015
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$09 INF \$08 (32 bytes).
<b>Rationale:</b>	This test will use positive responses from two ECUs. The order of ECU responses will be different than that of the original connection order.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the "ISO 15765 (29-bit, 500K, 2 ECUs) SID9 Ready State", which is detailed in 5.4.3.15.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$09 INF \$08 (IPT).
<u>EVALUATION</u>	Pass if the <b>15765_29_SID\$09_INF\$08</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2_MIN</i> delay, transmit the message <b>15765_29_ECU2_SID\$09_INF\$08_32</b> , delay <i>P2_MIN</i> , then transmit the message <b>15765_29_ECU1_SID\$09_INF\$08_32</b> .
<u>EVALUATION</u>	<p>Pass if the following is true:  Transmission is successful.  The DUT indicates that the IPT for ECU #1 is 32 bytes and has the following values:  OBDCOND = 100  IGNCNTR = 105  CATCOMP1 = 10  CATCOND1 = 15  CATCOMP2 = 00  CATCOND2 = 00  O2COMP1 = 30  O2COND1 = 35  O2COMP2 = 00  O2COND2 = 00  EGRCOMP = 50  EGRCOND = 55  AIRCOMP = 60  AIRCOND = 65  EVAPCOND = 70  EVAPCOND = 75</p> <p>The DUT indicates that the IPT for ECU #2 is 32 bytes and has the following values:  OBDCOND = 100  IGNCNTR = 105  CATCOMP1 = 00  CATCOND1 = 00  CATCOMP2 = 20  CATCOND2 = 25  O2COMP1 = 00  O2COND1 = 00  O2COMP2 = 40  O2COND2 = 45  EGRCOMP = 00  EGRCOND = 00  AIRCOMP = 00  AIRCOND = 00  EVAPCOND = 00  EVAPCOND = 00</p>

Figure 116 - Verify an ISO 15765-4 SID \$09 IPT (INF\$08, 32 bytes) request (29-bit, 500K)

## 9.2.59 Verify a SID \$09 IPT (INF\$08, 40 bytes) Request (11-bit, 500K; 2 ECUs w/ Order Swap)

<b>Test ID:</b>	\$30109016
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$09 INF \$08 (40 bytes).
<b>Rationale:</b>	This test will use positive responses from two ECUs. The order of ECU responses will be different than that of the original connection order.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 15765 (11-bit, 500K, 2 ECUs) SID9 Ready State”, which is detailed in 5.4.3.13.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$09 INF \$08 (IPT).
<u>EVALUATION</u>	Pass if the <b>15765_11_SID\$09_INF\$08</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2_MIN</i> delay, transmit the message <b>15765_11_ECU2_SID\$09_INF\$08_40</b> , delay <i>P2_MIN</i> , then transmit the message <b>15765_11_ECU1_SID\$09_INF\$08_40</b> .
<u>EVALUATION</u>	<p>Pass if the following is true:  Transmission is successful.  The DUT indicates that the IPT for ECU #1 is 40 bytes and has the following values:  OBDCOND = 100  IGNCNTR = 105  CATCOMP1 = 10  CATCOND1 = 15  CATCOMP2 = 00  CATCOND2 = 00  O2COMP1 = 30  O2COND1 = 35  O2COMP2 = 00  O2COND2 = 00  EGRCOMP = 50  EGRCOND = 55  AIRCOMP = 60  AIRCOND = 65  EVAPCOND = 70  EVAPCOND = 75  SO2SCOMP1 = 90  SO2SCOND1 = 95  SO2SCOMP2 = 00  SO2SCOND2 = 00</p> <p>The DUT indicates that the IPT for ECU #2 is 40 bytes and has the following values:  OBDCOND = 100  IGNCNTR = 105  CATCOMP1 = 00  CATCOND1 = 00  CATCOMP2 = 20  CATCOND2 = 25  O2COMP1 = 00  O2COND1 = 00  O2COMP2 = 40  O2COND2 = 45  EGRCOMP = 00  EGRCOND = 00</p>

```

AIRCOMP = 00
AIRCOND = 00
EVAPCOND = 00
EVAPCOND = 00
SO2SCOMP1 = 00
SO2SCOND1 = 00
SO2SCOMP2 = 90
SO2SCOND2 = 95

```

**Figure 117 - Verify an ISO 15765-4 SID \$09 IPT (INF\$08, 40 bytes) request (11-bit, 500K)**

9.2.60 Verify a SID \$09 IPT (INF\$08, 40 bytes) Request (29-bit, 500K; 2 ECUs w/ Order Swap)

<b>Test ID:</b>	\$30109017
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$09 INF \$08 (40 bytes).
<b>Rationale:</b>	This test will use positive responses from two ECUs. The order of ECU responses will be different than that of the original connection order.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the "ISO 15765 (29-bit, 500K, 2 ECUs) SID9 Ready State", which is detailed in 5.4.3.15.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$09 INF \$08 (IPT).
<u>EVALUATION</u>	Pass if the <b>15765_29_SID\$09_INF\$08</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2_MIN</i> delay, transmit the message <b>15765_29_ECU2_SID\$09_INF\$08_40</b> , delay <i>P2_MIN</i> , then transmit the message <b>15765_29_ECU1_SID\$09_INF\$08_40</b> .
<u>EVALUATION</u>	<p>Pass if the following is true:  Transmission is successful.  The DUT indicates that the IPT for ECU #1 is 40 bytes and has the following values:  OBDCOND = 100  IGNCNTR = 105  CATCOMP1 = 10  CATCOND1 = 15  CATCOMP2 = 00  CATCOND2 = 00  O2COMP1 = 30  O2COND1 = 35  O2COMP2 = 00  O2COND2 = 00  EGRCOMP = 50  EGRCOND = 55  AIRCOMP = 60  AIRCOND = 65  EVAPCOND = 70  EVAPCOND = 75  SO2SCOMP1 = 90  SO2SCOND1 = 95  SO2SCOMP2 = 00  SO2SCOND2 = 00</p> <p>The DUT indicates that the IPT for ECU #2 is 40 bytes and has the following values:  OBDCOND = 100</p>

```

IGNCNTR = 105
CATCOMP1 = 00
CATCOND1 = 00
CATCOMP2 = 20
CATCOND2 = 25
O2COMP1 = 00
O2COND1 = 00
O2COMP2 = 40
O2COND2 = 45
EGRCOMP = 00
EGRCOND = 00
AIRCOMP = 00
AIRCOND = 00
EVAPCOND = 00
EVAPCOND = 00
SO2SCOMP1 = 00
SO2SCOND1 = 00
SO2SCOMP2 = 90
SO2SCOND2 = 95

```

**Figure 118 - Verify an ISO 15765-4 SID \$09 IPT (INF\$08, 40 bytes) request (29-bit, 500K)**

9.2.61 Verify a SID \$09 ECU Name Request (11-bit, 500K; 8 ECUs w/ Order Swap)

<b>Test ID:</b>	\$30109018
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$09 INF \$0A.
<b>Rationale:</b>	This test will use positive responses from eight different ECUs. The order of ECU responses will be different than that of the original connection order.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the "ISO 15765 (11-bit, 500K, 8 ECUs) SID9 Ready State", which is detailed in 5.4.3.17.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$09 INF \$0A (ECU Name).
<u>EVALUATION</u>	Pass if the <b>15765_11_SID\$09_INF\$0A</b> message was transmitted.
<u>ACTION- ECUSIM</u>	Delay <i>P2_MIN</i> before transmitting each of the following messages <b>15765_11_ECU4_SID\$09_INF\$0A, 15765_11_ECU5_SID\$09_INF\$0A,  15765_11_ECU6_SID\$09_INF\$0A, 15765_11_ECU3_SID\$09_INF\$0A,  15765_11_ECU2_SID\$09_INF\$0A, 15765_11_ECU7_SID\$09_INF\$0A,  15765_11_ECU1_SID\$09_INF\$0A, 15765_11_ECU8_SID\$09_INF\$0A.</b>
<u>EVALUATION</u>	Pass if the following are true: Transmission is successful. The DUT indicates that the ECU Name for ECU #1 is "ECM1-EngineControl1". The DUT indicates that the ECU Name for ECU #2 is "ECM2-EngineControl2". The DUT indicates that the ECU Name for ECU #3 is "TCM1-TransmisCtrl1". The DUT indicates that the ECU Name for ECU #4 is "ECC1-EmisCritlInfo1". The DUT indicates that the ECU Name for ECU #5 is "ECC2-EmisCritlInfo2". The DUT indicates that the ECU Name for ECU #6 is "TCM2-TransmisCtrl2". The DUT indicates that the ECU Name for ECU #7 is "TCC1-TransfCaseCtrl1". The DUT indicates that the ECU Name for ECU #8 is "TCC2-TransfCaseCtrl2".

**Figure 119 - Verify an ISO 15765-4 SID \$09 ECU name request (11-bit, 500K)**

## 9.2.62 Verify a SID \$09 ECU Name Request (29-bit, 500K; 8 ECUs w/ Order Swap)

<b>Test ID:</b>	\$30109019
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$09 INF \$0A.
<b>Rationale:</b>	This test will use positive responses from eight different ECUs. The order of ECU responses will be different than that of the original connection order.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 15765 (29-bit, 500K, 8 ECUs) SID9 Ready State”, which is detailed in 5.4.3.19.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$09 INF \$0A (ECU Name).
<u>EVALUATION</u>	Pass if the <b>15765_29_SID\$09_INF\$0A</b> message was transmitted.
<u>ACTION- ECUSIM</u>	
	Delay <i>P2_MIN</i> before transmitting each of the following messages <b>15765_29_ECU4_SID\$09_INF\$0A, 15765_29_ECU5_SID\$09_INF\$0A,  15765_29_ECU6_SID\$09_INF\$0A, 15765_29_ECU3_SID\$09_INF\$0A,  15765_29_ECU2_SID\$09_INF\$0A, 15765_29_ECU7_SID\$09_INF\$0A,  15765_29_ECU1_SID\$09_INF\$0A, 15765_29_ECU8_SID\$09_INF\$0A.</b>
<u>EVALUATION</u>	Pass if the following are true: Transmission is successful. The DUT indicates that the ECU Name for ECU #1 is “ECM1-EngineControl1”. The DUT indicates that the ECU Name for ECU #2 is “ECM2-EngineControl2”. The DUT indicates that the ECU Name for ECU #3 is “TCM1-TransmisCtrl1”. The DUT indicates that the ECU Name for ECU #4 is “ECC1-EmisCritInfo1”. The DUT indicates that the ECU Name for ECU #5 is “ECC2-EmisCritInfo2”. The DUT indicates that the ECU Name for ECU #6 is “TCM2-TransmisCtrl2”. The DUT indicates that the ECU Name for ECU #7 is “TCC1-TransfCaseCtrl1”. The DUT indicates that the ECU Name for ECU #8 is “TCC2-TransfCaseCtrl2”.

**Figure 120 - Verify an ISO 15765-4 SID \$09 ECU name request (29-bit, 500K)**

## 9.2.63 Verify a SID \$09 IPT (INF\$0B, 32 bytes) Request (11-bit, 500K; 2 ECUs w/ Order Swap)

<b>Test ID:</b>	\$3010901A
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$09 INF \$0B (32 bytes).
<b>Rationale:</b>	This test will use positive responses from two ECUs. The order of ECU responses will be different than that of the original connection order.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 15765 (11-bit, 500K, 2 ECUs) SID9 Diesel Ready State”, which is detailed in 5.4.3.14.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$09 INF \$0B (IPT).
<u>EVALUATION</u>	Pass if the <b>15765_11_SID\$09_INF\$0B</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2_MIN</i> delay, transmit the message <b>15765_11_ECU2_SID\$09_INF\$0B_32</b> , delay <i>P2_MIN</i> , then transmit the message <b>15765_11_ECU1_SID\$09_INF\$0B_32</b> .
<u>EVALUATION</u>	<p>Pass if the following is true:  Transmission is successful.  The DUT indicates that the IPT for ECU #1 is 32 bytes and has the following values:  OBDCOND = 100  IGNCNTR = 105  HCCATCOMP1 = 10  HCCATCOND1 = 15  NCATCOMP2 = 00  NCATCOND2 = 00  NADSCOMP1 = 00  NADSCOND1 = 00  PMCOMP2 = 00  PMCOND2 = 00  EGSCOMP = 50  EGSCOND = 55  EGRCOMP = 60  EGRCOND = 65  BPCOND = 70  BPCOND = 75</p> <p>The DUT indicates that the IPT for ECU #2 is 32 bytes and has the following values:  OBDCOND = 100  IGNCNTR = 105  HCCATCOMP1 = 00  HCCATCOND1 = 00  NCATCOMP2 = 20  NCATCOND2 = 25  NADSCOMP1 = 00  NADSCOND1 = 00  PMCOMP2 = 40  PMCOND2 = 45  EGSCOMP = 00  EGSCOND = 00  EGRCOMP = 00  EGRCOND = 00  BPCOND = 00  BPCOND = 00</p>

Figure 121 - Verify an ISO 15765-4 SID \$09 IPT (INF\$0B, 32 bytes) request (11-bit, 500K)

## 9.2.64 Verify a SID \$09 IPT (INF\$0B, 32 bytes) Request (29-bit, 500K; 2 ECUs w/ Order Swap)

<b>Test ID:</b>	\$3010901B
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$09 INF \$0B (32 bytes).
<b>Rationale:</b>	This test will use positive responses from two ECUs. The order of ECU responses will be different than that of the original connection order.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 15765 (29-bit, 500K, 2 ECUs) SID9 Diesel Ready State”, which is detailed in 5.4.3.16.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$09 INF \$0B (IPT).
<u>EVALUATION</u>	Pass if the <b>15765_29_SID\$09_INF\$0B</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2_MIN</i> delay, transmit the message <b>15765_29_ECU2_SID\$09_INF\$0B_32</b> , delay <i>P2_MIN</i> , then transmit the message <b>15765_29_ECU1_SID\$09_INF\$0B_32</b> .
<u>EVALUATION</u>	<p>Pass if the following is true:  Transmission is successful.  The DUT indicates that the IPT for ECU #1 is 32 bytes and has the following values:  OBDCOND = 100  IGNCNTR = 105  HCCATCOMP1 = 10  HCCATCOND1 = 15  NCATCOMP2 = 00  NCATCOND2 = 00  NADSCOMP1 = 00  NADSCOND1 = 00  PMCOMP2 = 00  PMCOND2 = 00  EGSCOMP = 50  EGSCOND = 55  EGRCOMP = 60  EGRCOND = 65  BPCOND = 70  BPCOND = 75</p> <p>The DUT indicates that the IPT for ECU #2 is 32 bytes and has the following values:  OBDCOND = 100  IGNCNTR = 105  HCCATCOMP1 = 00  HCCATCOND1 = 00  NCATCOMP2 = 20  NCATCOND2 = 25  NADSCOMP1 = 00  NADSCOND1 = 00  PMCOMP2 = 40  PMCOND2 = 45  EGSCOMP = 00  EGSCOND = 00  EGRCOMP = 00  EGRCOND = 00  BPCOND = 00  BPCOND = 00</p>

Figure 122 - Verify an ISO 15765-4 SID \$09 IPT (INF\$0B, 32 bytes) request (29-bit, 500K)

## 9.2.65 Verify a SID \$09 IPT (INF\$0B, 36 bytes) Request (11-bit, 500K; 2 ECUs w/ Order Swap)

<b>Test ID:</b>	\$3010901C
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$09 INF \$0B (36 bytes).
<b>Rationale:</b>	This test will use positive responses from two ECUs. The order of ECU responses will be different than that of the original connection order.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the "ISO 15765 (11-bit, 500K, 2 ECUs) SID9 Diesel Ready State", which is detailed in 5.4.3.14.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$09 INF \$0B (IPT).
<u>EVALUATION</u>	Pass if the <b>15765_11_SID\$09_INF\$0B</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2_MIN</i> delay, transmit the message <b>15765_11_ECU2_SID\$09_INF\$0B_36</b> , delay <i>P2_MIN</i> , then transmit the message <b>15765_11_ECU1_SID\$09_INF\$0B_36</b> .
<u>EVALUATION</u>	<p>Pass if the following is true:  Transmission is successful.  The DUT indicates that the IPT for ECU #1 is 36 bytes and has the following values:  OBDCOND = 100  IGNCNTR = 105  HCCATCOMP1 = 10  HCCATCOND1 = 15  NCATCOMP2 = 00  NCATCOND2 = 00  NADSCOMP1 = 00  NADSCOND1 = 00  PMCOMP2 = 00  PMCOND2 = 00  EGSCOMP = 50  EGSCOND = 55  EGRCOMP = 60  EGRCOND = 65  BPCOND = 70  BPCOND = 75  FUELCOND = 80  FUELCOND = 85</p> <p>The DUT indicates that the IPT for ECU #2 is 36 bytes and has the following values:  OBDCOND = 100  IGNCNTR = 105  HCCATCOMP1 = 00  HCCATCOND1 = 00  NCATCOMP2 = 20  NCATCOND2 = 25  NADSCOMP1 = 00  NADSCOND1 = 00  PMCOMP2 = 40  PMCOND2 = 45  EGSCOMP = 00  EGSCOND = 00  EGRCOMP = 00  EGRCOND = 00</p>

BPCOND = 00  
 BPCOND = 00  
 FUELCOND = 00  
 FUELCOND = 00

**Figure 123 - Verify an ISO 15765-4 SID \$09 IPT (INF\$0B, 36 bytes) request (11-bit, 500K)**

9.2.66 Verify a SID \$09 IPT (INF\$0B, 36 bytes) Request (29-bit, 500K; 2 ECUs w/ Order Swap)

<b>Test ID:</b>	\$3010901D
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$09 INF \$0B (36 bytes).
<b>Rationale:</b>	This test will use positive responses from two ECUs. The order of ECU responses will be different than that of the original connection order.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the "ISO 15765 (29-bit, 500K, 2 ECUs) SID9 Diesel Ready State", which is detailed in 5.4.3.16.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$09 INF \$0B (IPT).
<u>EVALUATION</u>	Pass if the <b>15765_29_SID\$09_INF\$0B</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2_MIN</i> delay, transmit the message <b>15765_29_ECU2_SID\$09_INF\$0B_36</b> , delay <i>P2_MIN</i> , then transmit the message <b>15765_29_ECU1_SID\$09_INF\$0B_36</b> .
<u>EVALUATION</u>	<p>Pass if the following is true:          Transmission is successful.          The DUT indicates that the IPT for ECU #1 is 36 bytes and has the following values:          OBDCOND = 100          IGNCNTR = 105          HCCATCOMP1 = 10          HCCATCOND1 = 15          NCATCOMP2 = 00          NCATCOND2 = 00          NADSCOMP1 = 00          NADSCOND1 = 00          PMCOMP2 = 00          PMCOND2 = 00          EGSCOMP = 50          EGSCOND = 55          EGRCOMP = 60          EGRCOND = 65          BPCOND = 70          BPCOND = 75          FUELCOND = 80          FUELCOND = 85</p> <p>The DUT indicates that the IPT for ECU #2 is 36 bytes and has the following values:          OBDCOND = 100          IGNCNTR = 105          HCCATCOMP1 = 00          HCCATCOND1 = 00          NCATCOMP2 = 20          NCATCOND2 = 25          NADSCOMP1 = 00</p>

NADSCOND1 = 00
PMCOMP2 = 40
PMCOND2 = 45
EGSCOMP = 00
EGSCOND = 00
EGRCOMP = 00
EGRCOND = 00
BPCOND = 00
BPCOND = 00
FUELCOND = 00
FUELCOND = 00

**Figure 124 - Verify an ISO 15765-4 SID \$09 IPT (INF\$0B, 36 bytes) request (29-bit, 500K)**

9.2.67 Verify a SID \$09 ESN Request (11-bit, 500K; 8 ECUs)

<b>Test ID:</b>	\$3010901E
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$09 INF \$0D.
<b>Rationale:</b>	This test will use positive responses from eight different ECUs. The order of ECU responses will be the same as that of the original connection order.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 15765 (11-bit, 500K, 8 ECUs) SID9 Diesel Ready State”, which is detailed in 5.4.3.18.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$09 INF \$0D (ESN).
<u>EVALUATION</u>	Pass if the <b>15765_11_SID\$09_INF\$0D</b> message was transmitted.
<u>ACTION- ECUSIM</u>	Delay <i>P2_MIN</i> before transmitting each of the following messages <b>15765_11_ECU1_SID\$09_INF\$0D, 15765_11_ECU2_SID\$09_INF\$0D,  15765_11_ECU3_SID\$09_INF\$0D, 15765_11_ECU4_SID\$09_INF\$0D,  15765_11_ECU5_SID\$09_INF\$0D, 15765_11_ECU6_SID\$09_INF\$0D,  15765_11_ECU7_SID\$09_INF\$0D, 15765_11_ECU8_SID\$09_INF\$0D.</b>
<u>EVALUATION</u>	Pass if the following are true: Transmission is successful The DUT indicates that the ESN for ECU #1 is “VALESN987651”. The DUT indicates that the ESN for ECU #2 is “VALESN987652”. The DUT indicates that the ESN for ECU #3 is “VALESN987653”. The DUT indicates that the ESN for ECU #4 is “VALESN987654”. The DUT indicates that the ESN for ECU #5 is “VALESN987655”. The DUT indicates that the ESN for ECU #6 is “VALESN987656”. The DUT indicates that the ESN for ECU #7 is “VALESN987657”. The DUT indicates that the ESN for ECU #8 is “VALESN987658”.

**Figure 125 - Verify an ISO 15765-4 SID \$09 ESN request (11-bit, 500K)**

## 9.2.68 Verify a SID \$09 ESN Request (29-bit, 500K; 8 ECUs)

<b>Test ID:</b>	\$3010901F
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$09 INF \$0D.
<b>Rationale:</b>	This test will use positive responses from eight different ECUs. The order of ECU responses will be the same as that of the original connection order.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 15765 (29-bit, 500K, 8 ECUs) SID9 Diesel Ready State”, which is detailed in 5.4.3.20.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$09 INF \$0D (ESN).
<u>EVALUATION</u>	Pass if the <b>15765_29_SID\$09_INF\$0D</b> message was transmitted.
<u>ACTION- ECUSIM</u>	
	Delay <i>P2_MIN</i> before transmitting each of the following messages <b>15765_29_ECU1_SID\$09_INF\$0D, 15765_29_ECU2_SID\$09_INF\$0D,  15765_29_ECU3_SID\$09_INF\$0D, 15765_29_ECU4_SID\$09_INF\$0D,  15765_29_ECU5_SID\$09_INF\$0D, 15765_29_ECU6_SID\$09_INF\$0D,  15765_29_ECU7_SID\$09_INF\$0D, 15765_29_ECU8_SID\$09_INF\$0D.</b>
<u>EVALUATION</u>	Pass if the following are true: Transmission is successful The DUT indicates that the ESN for ECU #1 is “VALESN987651”. The DUT indicates that the ESN for ECU #2 is “VALESN987652”. The DUT indicates that the ESN for ECU #3 is “VALESN987653”. The DUT indicates that the ESN for ECU #4 is “VALESN987654”. The DUT indicates that the ESN for ECU #5 is “VALESN987655”. The DUT indicates that the ESN for ECU #6 is “VALESN987656”. The DUT indicates that the ESN for ECU #7 is “VALESN987657”. The DUT indicates that the ESN for ECU #8 is “VALESN987658”.

**Figure 126 - Verify an ISO 15765-4 SID \$09 ESN request (29-bit, 500K)**

## 9.2.69 Verify a SID \$09 EROTAN Request (11-bit, 500K; 8 ECUs w/ Order Swap)

<b>Test ID:</b>	\$30109020
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$09 INF \$0F.
<b>Rationale:</b>	This test will use positive responses from eight different ECUs. The order of ECU responses will be different than that of the original connection order.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the "ISO 15765 (11-bit, 500K, 8 ECUs) SID9 Diesel Ready State", which is detailed in 5.4.3.18.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$09 INF \$0F (EROTAN).
<u>EVALUATION</u>	Pass if the <b>15765_11_SID\$09_INF\$0F</b> message was transmitted.
<u>ACTION- ECUSIM</u>	
	Delay <i>P2_MIN</i> before transmitting each of the following messages <b>15765_11_ECU8_SID\$09_INF\$0F, 15765_11_ECU7_SID\$09_INF\$0F,  15765_11_ECU6_SID\$09_INF\$0F, 15765_11_ECU5_SID\$09_INF\$0F,  15765_11_ECU4_SID\$09_INF\$0F, 15765_11_ECU3_SID\$09_INF\$0F,  15765_11_ECU2_SID\$09_INF\$0F, 15765_11_ECU1_SID\$09_INF\$0F.</b>
<u>EVALUATION</u>	Pass if the following are true: Transmission is successful. The DUT indicates that the EROTAN for ECU #1 is "VALERTAN1". The DUT indicates that the EROTAN for ECU #2 is "VALERTAN2". The DUT indicates that the EROTAN for ECU #3 is "VALERTAN3". The DUT indicates that the EROTAN for ECU #4 is "VALERTAN4". The DUT indicates that the EROTAN for ECU #5 is "VALERTAN5". The DUT indicates that the EROTAN for ECU #6 is "VALERTAN6". The DUT indicates that the EROTAN for ECU #7 is "VALERTAN7". The DUT indicates that the EROTAN for ECU #8 is "VALERTAN8".

**Figure 127 - Verify an ISO 15765-4 SID \$09 EROTAN request (11-bit, 500K)**

## 9.2.70 Verify a SID \$09 EROTAN Request (29-bit, 500K; 8 ECUs w/ Order Swap)

<b>Test ID:</b>	\$30109021
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$09 INF \$0F.
<b>Rationale:</b>	This test will use positive responses from eight different ECUs. The order of ECU responses will be different than that of the original connection order.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the "ISO 15765 (29-bit, 500K, 8 ECUs) SID9 Diesel Ready State", which is detailed in 5.4.3.20.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$09 INF \$0F (EROTAN).
<u>EVALUATION</u>	Pass if the <b>15765_29_SID\$09_INF\$0F</b> message was transmitted.
<u>ACTION- ECUSIM</u>	Delay <i>P2_MIN</i> before transmitting each of the following messages <b>15765_29_ECU8_SID\$09_INF\$0F, 15765_29_ECU7_SID\$09_INF\$0F,  15765_29_ECU6_SID\$09_INF\$0F, 15765_29_ECU5_SID\$09_INF\$0F,  15765_29_ECU4_SID\$09_INF\$0F, 15765_29_ECU3_SID\$09_INF\$0F,  15765_29_ECU2_SID\$09_INF\$0F, 15765_29_ECU1_SID\$09_INF\$0F.</b>
<u>EVALUATION</u>	Pass if the following are true: Transmission is successful. The DUT indicates that the EROTAN for ECU #1 is "VALERTAN1". The DUT indicates that the EROTAN for ECU #2 is "VALERTAN2". The DUT indicates that the EROTAN for ECU #3 is "VALERTAN3". The DUT indicates that the EROTAN for ECU #4 is "VALERTAN4". The DUT indicates that the EROTAN for ECU #5 is "VALERTAN5". The DUT indicates that the EROTAN for ECU #6 is "VALERTAN6". The DUT indicates that the EROTAN for ECU #7 is "VALERTAN7". The DUT indicates that the EROTAN for ECU #8 is "VALERTAN8".

Figure 128 - Verify an ISO 15765-4 SID \$09 EROTAN request (29-bit, 500K)

## 9.2.71 Verify a SID \$0A Permanent DTC Request with No DTCs (11-bit, 500K; 2 ECUs w/ Order Swap)

<b>Test ID:</b>	\$3010A000
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$0A and process a 'no DTCs present' response.
<b>Rationale:</b>	This test will use positive responses from two different ECUs. The order of ECU responses will be different than that of the original connection order.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the "ISO 15765 (11-bit, 500K, 2 ECUs) Initialized State", which is detailed in 5.4.3.9.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$0A (Permanent DTCs).
<u>EVALUATION</u>	Pass if the <b>15765_11_SID\$0A</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2_MIN</i> delay, transmit the message <b>15765_11_ECU2_SID\$0A_NoDTC</b> , delay <i>P2_MIN</i> , then transmit the message <b>15765_11_ECU1_SID\$0A_NoDTC</b> .
<u>EVALUATION</u>	Pass if transmission is successful and the DUT indicates that there are no permanent DTCs in either ECU.

**Figure 129 - Verify an ISO 15765-4 SID \$0A permanent DTC request with no DTCs (11-bit, 500K)**

## 9.2.72 Verify a SID \$0A Permanent DTC Request with No DTCs (29-bit, 500K; 2 ECUs w/ Order Swap)

<b>Test ID:</b>	\$3010A001
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$0A and process a 'no DTCs present' response.
<b>Rationale:</b>	This test will use positive responses from two different ECUs. The order of ECU responses will be different than that of the original connection order.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the "ISO 15765 (29-bit, 500K, 2 ECUs) Initialized State", which is detailed in 5.4.3.10.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$0A (Permanent DTCs).
<u>EVALUATION</u>	Pass if the <b>15765_29_SID\$0A</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2_MIN</i> delay, transmit the message <b>15765_29_ECU2_SID\$0A_NoDTC</b> , delay <i>P2_MIN</i> , then transmit the message <b>15765_29_ECU1_SID\$0A_NoDTC</b> .
<u>EVALUATION</u>	Pass if transmission is successful and the DUT indicates that there are no permanent DTCs in either ECU.

**Figure 130 - Verify an ISO 15765-4 SID \$0A permanent DTC request with no DTCs (29-bit, 500K)**

## 9.2.73 Verify a SID \$0A Permanent DTC Request with DTCs (11-bit, 500K; 2 ECUs)

<b>Test ID:</b>	\$3010A002
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$0A and process a 'DTCs present' response.
<b>Rationale:</b>	This test will use positive responses from two different ECUs.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the "ISO 15765 (11-bit, 500K, 2 ECUs) Initialized State", which is detailed in 5.4.3.9.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$0A (Permanent DTCs).
<u>EVALUATION</u>	Pass if the <b>15765_11_SID\$0A</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2_MIN</i> delay, transmit the message <b>15765_11_ECU1_SID\$0A_DTC</b> , delay <i>P2_MIN</i> , then transmit the message <b>15765_11_ECU2_SID\$0A_NoDTC</b> .
<u>EVALUATION</u>	Pass if the following is true: The transmission is successful. The DUT indicates that ECU #1 has the following permanent DTCs: P0107, P0110, P0115, and P0124. The DUT indicates that ECU #2 has no permanent DTCs.

**Figure 131 - Verify an ISO 15765-4 SID \$0A permanent DTC request with DTCs (11-bit, 500K)**

## 9.2.74 Verify a SID \$0A Permanent DTC Request with DTCs (29-bit, 500K; 2 ECUs)

<b>Test ID:</b>	\$3010A003
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$0A and process a 'DTCs present' response.
<b>Rationale:</b>	This test will use positive responses from two different ECUs.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the "ISO 15765 (29-bit, 500K, 2 ECUs) Initialized State", which is detailed in 5.4.3.10.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$0A (Permanent DTCs).
<u>EVALUATION</u>	Pass if the <b>15765_29_SID\$0A</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2_MIN</i> delay, transmit the message <b>15765_29_ECU1_SID\$0A_DTC</b> , delay <i>P2_MIN</i> , then transmit the message <b>15765_29_ECU2_SID\$0A_NoDTC</b> .
<u>EVALUATION</u>	Pass if the following is true: The transmission is successful. The DUT indicates that ECU #1 has the following permanent DTCs: P0107, P0110, P0115, and P0124. The DUT indicates that ECU #2 has no permanent DTCs.

**Figure 132 - Verify an ISO 15765-4 SID \$0A permanent DTC request with DTCs (29-bit, 500K)**

## 9.3 ISO 14230-4 Tests

These network tests are specific to ISO 14230-4. Unless otherwise specified, all references to Electrical Connections, ECU Addresses, Messages, Message Timing, and pre-defined states will be as described in 5.4.3.13.

## 9.3.1 Verify a SID \$01 PID Supported PIDs Requests (1 ECU)

<b>Test ID:</b>	\$30201000
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request a SID \$01 PID Supported PIDs.
<b>Rationale:</b>	This test will use positive responses from a single different ECU.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 14230-4 (2 ECUs) Initialized State”, which is detailed in 5.4.4.6.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$01 PID Supported PIDs.
<u>EVALUATION</u>	Pass if the <b>14230_SID\$01_PID\$00</b> or <b>14230_SID\$01_PID\$20</b> or <b>14230_SID\$01_PID\$40</b> message was transmitted.
<u>ACTION- ECUSIM</u>	<p>If the DUT transmitted <b>14230_SID\$01_PID\$00</b> then after the <i>P2</i> delay, transmit <b>14230_ECU1_SID\$01_PID\$00_20</b>, delay <i>P2</i> and transmit <b>14230_ECU2_SID\$01_PID\$00</b>. Wait for the remaining message transactions in this ACTION to be completed.</p> <p>If the DUT transmitted <b>14230_SID\$01_PID\$20</b> then after the <i>P2</i> delay, transmit <b>14230_ECU1_SID\$01_PID\$20</b>, and wait for the remaining message transactions in this ACTION to be completed.</p> <p>If the DUT transmitted <b>14230_SID\$01_PID\$40</b> then after the <i>P2</i> delay, transmit <b>14230_ECU1_SID\$01_PID\$40</b>, and wait for the remaining message transactions in this ACTION to be completed.</p>
<u>EVALUATION</u>	<p>Pass if all transmissions by the DUT and ECUSIM were successful and the DUT indicates that:</p> <p>ECU #1 supports PIDs \$01(Readiness Bits), \$0C (RPM), and \$41 (Monitor Status this drive cycle) are supported.</p> <p>ECU #2 supports PIDs \$01(Readiness Bits) and \$0C (RPM) are supported.</p>

**Figure 133 - Verify an ISO 14230-4 SID \$01 PID supported PID request**

## 9.3.2 Verify a SID \$01 PID \$01 Request for Spark Ignition (2 ECUs w/ Order Swap)

<b>Test ID:</b>	\$30201001
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request a SID \$01 PID, which the ECUSIM indicates is supported.
<b>Rationale:</b>	This test will use positive responses from two different ECUs. The order of ECU responses will be different than that of the original connection order. This test will also verify that the DUT can differentiate a spark ignition response from a compression ignition response.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 14230-4 (2 ECUs) Initialized State”, which is detailed in 5.4.4.6 with the ISO 14230-4 network connected to the ECUSIM. The ECUSIM shall be in the “ISO 14230-4 (2 ECUs) Initialized State” as defined in 5.4.4.6.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$01 PID \$01 (I/M Readiness Bits).
<u>EVALUATION</u>	Pass if the <b>14230_SID\$01_PID\$01</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2</i> delay, transmit the message <b>14230_ECU2_SID\$01_PID\$01_SPARK</b> , delay <i>P2</i> , then transmit the message <b>14230_ECU1_SID\$01_PID\$01_SPARK</b> .
<u>EVALUATION</u>	<p>Pass if the following is true:</p> <p>Transmission is successful.</p> <p>The DUT indicates that ECU #1 has the following values:</p> <p>MIL is OFF  DCT COUNT is 0  SPARK IGNITION MONITORS are SUPPORTED  MISFIRE is SUPPORTED and READY  FUEL SYSTEM MONITORING is SUPPORTED and READY  COMPREHENSIVE COMPONENT MONITORING is SUPPORTED and READY  CATALYST MONITORING is SUPPORTED and NOT READY  HEATED CATALYST MONITORING is SUPPORTED and NOT READY  EVAPORATIVE SYSTEM MONITORING is SUPPORTED and NOT READY  SECONDARY AIR SYSTEM MONITORING is SUPPORTED and NOT READY  OXYGEN SENSOR MONITORING is SUPPORTED and NOT READY  OXYGEN SENSOR HEATER MONITORING is SUPPORTED and NOT READY  EGR/VVT SYSTEM MONITORING is SUPPORTED and NOT READY</p> <p>The DUT indicates that ECU #2 has the following values:</p> <p>MIL is ON  DCT COUNT is 1  SPARK IGNITION MONITORS are SUPPORTED  COMPREHENSIVE COMPONENT MONITORING is SUPPORTED and NOT READY</p>

**Figure 134 - Verify an ISO 14230-4 SID \$01 PID \$01 request for spark ignition**

## 9.3.3 Verify a SID \$01 PID \$01 Request for Compression Ignition (2 ECUs w/ Order Swap)

<b>Test ID:</b>	\$30201002
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request a SID \$01 PID, which the ECUSIM indicates is supported.
<b>Rationale:</b>	This test will use positive responses from two different ECUs. The order of ECU responses will be different than that of the original connection order. This test will also verify that the DUT can differentiate a spark ignition response from a compression ignition response.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 14230-4 (2 ECUs) Initialized State”, which is detailed in 5.4.4.6.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$01 PID \$01 (I/M Readiness Bits).
<u>EVALUATION</u>	Pass if the <b>14230_SID\$01_PID\$01</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2</i> delay, transmit the message <b>14230_ECU2_SID\$01_PID\$01_COMP</b> , delay <i>P2</i> , then transmit the message <b>14230_ECU1_SID\$01_PID\$01_COMP</b> .
<u>EVALUATION</u>	<p>Pass if the following is true:</p> <p>Transmission is successful.</p> <p>The DUT indicates that ECU #1 has the following values:</p> <p>MIL is OFF  DCT COUNT is 0  COMPRESSION IGNITION MONITORS are SUPPORTED  MISFIRE is SUPPORTED and READY  FUEL SYSTEM MONITORING is SUPPORTED and READY  COMPREHENSIVE COMPONENT MONITORING is SUPPORTED and READY  NMHC MONITORING is SUPPORTED and NOT READY  NOX AFTERTREATMENT MONITORING is SUPPORTED and NOT READY  BOOST PRESSURE SYSTEM MONITORING is SUPPORTED and NOT READY  EXHAUST GAS SENSOR MONITORING is SUPPORTED and NOT READY  PM FILTER MONITORING is SUPPORTED and NOT READY  EGR/VVT SYSTEM MONITORING is SUPPORTED and NOT READY</p> <p>The DUT indicates that ECU #2 has the following values:</p> <p>MIL is ON  DCT COUNT is 1  COMPRESSION IGNITION MONITORS are SUPPORTED  COMPREHENSIVE COMPONENT MONITORING is SUPPORTED and NOT READY</p>

**Figure 135 - Verify an ISO 14230-4 SID \$01 PID \$01 request for compression ignition**

## 9.3.4 Verify a SID \$01 PID \$0C Request (2 ECUs w/ Order Swap)

<b>Test ID:</b>	\$30201003
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request a SID \$01 PID, which the ECUSIM indicates is supported.
<b>Rationale:</b>	This test will use positive responses from two different ECUs. The order of ECU responses will be different than that of the original connection order.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 14230-4 (2 ECUs) Initialized State”, which is detailed in 5.4.4.6.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$01 PID \$0C (RPM).
<u>EVALUATION</u>	Pass if the <b>14230_SID\$01_PID\$0C</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2</i> delay, transmit the message <b>14230_ECU2_SID\$01_PID\$0C_KOEO</b> , delay <i>P2</i> , then transmit the message <b>14230_ECU1_SID\$01_PID\$0C_KOEO</b> .
<u>EVALUATION</u>	Pass if transmission is successful and the DUT indicates that engine RPM is 0.

**Figure 136 - Verify an ISO 14230-4 SID \$01 PID \$0C request**

## 9.3.5 Verify a SID \$02 PID Freeze Frame DTC Request with No DTCs (2 ECUs w/ Order Swap)

<b>Test ID:</b>	\$30202000
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$02 PID \$02 (indicating that there is no Freeze Frame DTC), which the ECUSIM indicates is supported.
<b>Rationale:</b>	This test will use positive responses from two different ECUs. The order of ECU responses will be different than that of the original connection order.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 14230-4 (2 ECUs) Initialized State”, which is detailed in 5.4.4.6.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$02 PID \$02 (the Freeze Frame DTC).
<u>EVALUATION</u>	Pass if the <b>14230_SID\$02_PID\$02</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2</i> delay, transmit the message <b>14230_ECU2_SID\$02_PID\$02_NoDTC</b> , delay <i>P2</i> , then transmit the message <b>14230_ECU1_SID\$02_PID\$02_NoDTC</b> .
<u>EVALUATION</u>	Pass if transmission is successful and the DUT indicates that there is no Freeze Frame DTC in either ECU.

**Figure 137 - Verify an ISO 14230-4 SID \$02 PID freeze frame DTC request with no DTCs**

## 9.3.6 Verify a SID \$02 PID Freeze Frame DTC Request with DTCs (2 ECUs)

<b>Test ID:</b>	\$30202001
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$02 PID \$02 (indicating that there is a Freeze Frame DTC), which the ECUSIM indicates is supported.
<b>Rationale:</b>	This test will use positive responses from two different ECUs.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 14230-4 (2 ECUs) Initialized State”, which is detailed in 5.4.4.6.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$02 PID \$02 (the Freeze Frame DTC).
<u>EVALUATION</u>	Pass if the <b>14230_SID\$02_PID\$02</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2</i> delay, transmit the message <b>14230_ECU1_SID\$02_PID\$02_DTC</b> , delay <i>P2</i> , then transmit the message <b>14230_ECU2_SID\$02_PID\$02_NoDTC</b> .
<u>EVALUATION</u>	Pass if the following is true: The transmission is successful. The DUT indicates that ECU #1 has a Freeze Frame DTC of P0107 and ECU #2 has no Freeze Frame DTC.

**Figure 138 - Verify an ISO 14230-4 SID \$02 PID freeze frame DTC request with DTCs**

## 9.3.7 Verify a SID \$03 Confirmed DTC Request with No DTCs (2 ECUs w/ Order Swap)

<b>Test ID:</b>	\$30203000
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$03 and process a ‘no DTCs present’ response.
<b>Rationale:</b>	This test will use positive responses from two different ECUs. The order of ECU responses will be different than that of the original connection order.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 14230-4 (2 ECUs) Initialized State”, which is detailed in 5.4.4.6.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$03 (Confirmed DTCs).
<u>EVALUATION</u>	Pass if the <b>14230_SID\$03</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2</i> delay, transmit the message <b>14230_ECU2_SID\$03_NoDTC</b> , delay <i>P2</i> , then transmit the message <b>14230_ECU1_SID\$03_NoDTC</b> .
<u>EVALUATION</u>	Pass if transmission is successful and the DUT indicates that there are no confirmed DTCs in either ECU.

**Figure 139 - Verify an ISO 14230-4 SID \$03 confirmed DTC request with no DTCs**

## 9.3.8 Verify a SID \$03 Confirmed DTC Request with DTCs (2 ECUs)

<b>Test ID:</b>	\$30203001
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$03 and process a 'DTCs present' response.
<b>Rationale:</b>	This test will use positive responses from two different ECUs.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the "ISO 14230-4 (2 ECUs) Initialized State", which is detailed in 5.4.4.6.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$03 (Confirmed DTCs).
<u>EVALUATION</u>	Pass if the <b>14230_SID\$03</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2</i> delay, transmit the following messages (with a <i>P2</i> delay between each message): <b>14230_ECU1_SID\$03_DTC_M1, 14230_ECU1_SID\$03_DTC_M2, 14230_ECU2_SID\$03_NoDTC.</b>
<u>EVALUATION</u>	Pass if the following is true: The transmission is successful. The DUT indicates that ECU #1 has the following confirmed DTCs: P0107, P0110, P0115, and P0124. The DUT indicates that ECU #2 has no confirmed DTCs.

**Figure 140 - Verify an ISO 14230-4 SID \$03 confirmed DTC request with DTCs**

## 9.3.9 Verify a SID \$04 Clear Codes Request (2 ECUs w/ Order Swap)

<b>Test ID:</b>	\$30204000
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$04.
<b>Rationale:</b>	This test will use positive responses from two different ECUs. The order of ECU responses will be different than that of the original connection order.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the "ISO 14230-4 (2 ECUs) Initialized State", which is detailed in 5.4.4.6.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$04 (Clear DTCs).
<u>EVALUATION</u>	Pass if the <b>14230_SID\$04</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2</i> delay, transmit the message <b>14230_ECU2_SID\$04_DONE</b> , delay <i>P2</i> , then transmit the message <b>14230_ECU1_SID\$04_DONE</b> .
<u>EVALUATION</u>	Pass if the transmission is successful and the DUT indicates that DTCs have been cleared.

**Figure 141 - Verify an ISO 14230-4 SID \$04 clear codes request**

## 9.3.10 Test a SID \$04 Clear Codes Request with NRC=\$22 (2 ECUs)

<b>Test ID:</b>	\$30204001
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$04 when an ECU indicates that conditions are not correct.
<b>Rationale:</b>	This test will use negative responses (NRC \$22) from two different ECUs.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 14230-4 (2 ECUs) Initialized State”, which is detailed in 5.4.4.6.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$04 (Clear DTCs).
<u>EVALUATION</u>	Pass if the <b>14230_SID\$04</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2</i> delay, transmit the message <b>14230_ECU1_SID\$04_NRC22</b> , delay <i>P2</i> , then transmit the message <b>14230_ECU2_SID\$04_NRC22</b> .
<u>EVALUATION</u>	Pass if the transmission is successful and the DUT indicates that DTCs have NOT been cleared.

**Figure 142 - Test an ISO 14230-4 SID \$04 clear codes request with NRC=\$22**

## 9.3.11 Verify a SID \$04 Clear Codes Request with NRC=\$78 (2 ECUs)

<b>Test ID:</b>	\$30204002
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$04 when an ECU indicates that it is busy then eventually competes.
<b>Rationale:</b>	This test will use positive responses from the first ECU and a response pending (NRC \$78) from a second ECU.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 14230-4 (2 ECUs) Initialized State”, which is detailed in 5.4.4.6.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$04 (Clear DTCs).
<u>EVALUATION</u>	Pass if the <b>14230_SID\$04</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2</i> delay, transmit the message <b>14230_ECU1_SID\$04_NRC78</b> , delay <i>P2</i> , then transmit the message <b>14230_ECU2_SID\$04_DONE</b> .
<u>EVALUATION</u>	Pass if the transmission is successful.
<u>ACTION- ECUSIM</u>	After the <i>P2_78</i> delay, transmit the message <b>14230_ECU1_SID\$04_DONE</b>
<u>EVALUATION</u>	Pass if the transmission is successful and the DUT indicates that DTCs have been cleared.

**Figure 143 - Verify an ISO 14230-4 SID \$04 clear codes request with NRC=\$78**

## 9.3.12 Verify a SID \$05 TID \$01 Request (1 ECU)

<b>Test ID:</b>	\$30205000
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$05 TID \$01.
<b>Rationale:</b>	This test will use a positive response from a single ECU.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 14230-4 (2 ECUs) Initialized State”, which is detailed in 5.4.4.6.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$05 TID \$01.
<u>EVALUATION</u>	Pass if the <b>14230_SID\$05_TID\$01</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2</i> delay, transmit the message <b>14230_ECU1_SID\$05_TID\$01</b> .
<u>EVALUATION</u>	Pass if the transmission is successful and the DUT indicates that ECU #1 O2 Bank 1 Sensor 2 has a test limit of 450 mV.

**Figure 144 - Verify an ISO 14230-4 SID \$05 TID \$01 request**

## 9.3.13 Verify a SID \$06 MID Supported MID Request (1 ECU)

<b>Test ID:</b>	\$30206000
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$06 MID Supported MIDs.
<b>Rationale:</b>	This test will use a positive response from a single ECU.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 14230-4 (2 ECUs) Initialized State”, which is detailed in 5.4.4.6.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$06 MID \$00 (MID Supported MIDs).
<u>EVALUATION</u>	Pass if the <b>14230_SID\$06_MID\$00</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2</i> delay, transmit the message <b>14230_ECU1_SID\$06_MID\$00</b> .
<u>EVALUATION</u>	Pass if the transmission is successful and the DUT indicates that ECU #1 supports MIDs \$01, \$02, \$05, and \$06.

**Figure 145 - Verify an ISO 14230-4 SID \$06 MID supported MID request**

## 9.3.14 Verify a SID \$07 Pending DTC Request with No DTCs (2 ECUs w/ Order Swap)

<b>Test ID:</b>	\$30207000
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$07 and process a 'no DTCs present' response.
<b>Rationale:</b>	This test will use positive responses from two different ECUs. The order of ECU responses will be different than that of the original connection order.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the "ISO 14230-4 (2 ECUs) Initialized State", which is detailed in 5.4.4.6.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$07 (Pending DTCs).
<u>EVALUATION</u>	Pass if the <b>14230_SID\$07</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2</i> delay, transmit the message <b>14230_ECU2_SID\$07_NoDTC</b> , delay <i>P2</i> , then transmit the message <b>14230_ECU1_SID\$07_NoDTC</b> .
<u>EVALUATION</u>	Pass if transmission is successful and the DUT indicates that there are no pending DTCs in either ECU.

**Figure 146 - Verify an ISO 14230-4 SID \$07 pending DTC request with no DTCs**

## 9.3.15 Verify a SID \$07 Pending DTC Request with DTCs (2 ECUs)

<b>Test ID:</b>	\$30207001
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$07 and process a 'DTCs present' response.
<b>Rationale:</b>	This test will use positive responses from two different ECUs.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the "ISO 14230-4 (2 ECUs) Initialized State", which is detailed in 5.4.4.6.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$07 (Pending DTCs).
<u>EVALUATION</u>	Pass if the <b>14230_SID\$07</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2</i> delay, transmit the following messages (with a <i>P2</i> delay between each message): <b>14230_ECU1_SID\$07_DTC_M1</b> , <b>14230_ECU1_SID\$07_DTC_M2</b> , <b>14230_ECU2_SID\$07_NoDTC</b> .
<u>EVALUATION</u>	Pass if the following is true: The transmission is successful. The DUT indicates that ECU #1 has the following pending DTCs: P0107, P0110, P0115, and P0124. The DUT indicates that ECU #2 has no pending DTCs.

**Figure 147 - Verify an ISO 14230-4 SID \$07 pending DTC request with DTCs**

## 9.3.16 Verify a SID \$08 TID Supported TID Request (1 ECU)

<b>Test ID:</b>	\$30208000
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$08 TID Supported TIDs.
<b>Rationale:</b>	This test will use a positive response from a single ECU.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 14230-4 (2 ECUs) Initialized State”, which is detailed in 5.4.4.6.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$08 TID \$00 (TID Supported TIDs).
<u>EVALUATION</u>	Pass if the <b>14230_SID\$08_PID\$00</b> message was transmitted
<u>ACTION- ECUSIM</u>	After the <i>P2</i> delay, transmit the message <b>14230_ECU1_SID\$08_PID\$00</b> .
<u>EVALUATION</u>	Pass if the transmission is successful and the DUT indicates that ECU #1 supports TID \$01 (the leak test).

**Figure 148 - Verify an ISO 14230-4 SID \$08 TID supported TID request**

## 9.3.17 Verify a SID \$09 INF Supported INF Request (2 ECUs w/ Order Swap)

<b>Test ID:</b>	\$30209000
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$09 INF Supported INFs.
<b>Rationale:</b>	This test will use positive responses from two different ECUs. The order of ECU responses will be different than that of the original connection order.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 14230-4 (2 ECUs) Initialized State”, which is detailed in 5.4.4.6.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$09 INF \$00 (INF Supported INFs).
<u>EVALUATION</u>	Pass if the <b>14230_SID\$09_PID\$00</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2</i> delay, transmit the message <b>14230_ECU2_SID\$09_INF\$00</b> , delay <i>P2</i> , then transmit the message <b>14230_ECU1_SID\$09_INF\$00</b> .
<u>EVALUATION</u>	Pass if the transmission is successful and the DUT indicates that: ECU #1 supports INF \$01 (MC_VIN), INF \$02 (VIN), INF \$03 (MC_CALID), INF \$04 (CALID), INF \$05 (MC_CVN), INF \$06 (CVN), INF \$07 (MC_IPT), INF \$08 (IPT) ECU #2 supports INF \$03 (MC_CALID), INF \$04 (CALID), INF \$05 (MC_CVN), INF \$06 (CVN), INF \$07 (MC_IPT), INF \$08 (IPT)

**Figure 149 - Verify an ISO 14230-4 SID \$09 INF supported INF request**

## 9.3.18 Verify a SID \$09 INF Supported INF Request Diesel (2 ECUs w/ Order Swap)

<b>Test ID:</b>	\$30209001
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$09 INF Supported INFs.
<b>Rationale:</b>	This test will use positive responses from two different ECUs. The order of ECU responses will be different than that of the original connection order.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 14230-4 (2 ECUs) Initialized State”, which is detailed in 5.4.4.6.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$09 INF \$00 (INF Supported INFs).
<u>EVALUATION</u>	Pass if the <b>14230_SID\$09_PID\$00</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2</i> delay, transmit the message <b>14230_ECU2_SID\$09_INF\$00_DIESEL</b> , delay <i>P2</i> , then transmit the message <b>14230_ECU1_SID\$09_INF\$00_DIESEL</b> .
<u>EVALUATION</u>	Pass if the transmission is successful and the DUT indicates that: ECU #1 supports INF \$01 (MC_VIN), INF \$02 (VIN), INF \$03 (MC_CALID), INF \$04 (CALID), INF \$05 (MC_CVN), INF \$06 (CVN), INF \$07 (MC_IPT), INF \$0B (IPT) ECU #2 supports INF \$03 (MC_CALID), INF \$04 (CALID), INF \$05 (MC_CVN), INF \$06 (CVN), INF \$07 (MC_IPT), INF \$0B (IPT)

**Figure 150 - Verify an ISO 14230-4 SID \$09 INF supported INF request**

## 9.3.19 Verify a SID \$09 INF Supported INF Request (8 ECUs w/ Order Swap)

<b>Test ID:</b>	\$30209002
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$09 INF Supported INFs.
<b>Rationale:</b>	This test will use positive responses from eight different ECUs. The order of ECU responses will be different than that of the original connection order.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 14230-4 (8 ECUs) Initialized State”, which is detailed in 5.4.4.7.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$09 INF \$00 (INF Supported INFs).
<u>EVALUATION</u>	Pass if the <b>14230_SID\$09_PID\$00</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2</i> delay, transmit the following messages (with a <i>P2</i> delay between each message): <b>14230_ECU2_SID\$09_INF\$00, 14230_ECU1_SID\$09_INF\$00, 14230_ECU4_SID\$09_INF\$00, 14230_ECU3_SID\$09_INF\$00, 14230_ECU6_SID\$09_INF\$00, 14230_ECU5_SID\$09_INF\$00, 14230_ECU8_SID\$09_INF\$00, and 14230_ECU7_SID\$09_INF\$00.</b>
<u>EVALUATION</u>	Pass if the transmission is successful and the DUT indicates that:

<p>ECU #1 supports INF \$01 (MC_VIN), INF \$02 (VIN), INF \$03 (MC_CALID), INF \$04 (CALID), INF \$05 (MC_CVN), INF \$06 (CVN), INF \$07 (MC_IPT), INF \$08 (IPT)</p> <p>ECU #2 supports INF \$03 (MC_CALID), INF \$04 (CALID), INF \$05 (MC_CVN), INF \$06 (CVN), INF \$07 (MC_IPT), INF \$08 (IPT)</p> <p>ECU #3 supports INF \$03 (MC_CALID), INF \$04 (CALID), INF \$05 (MC_CVN), INF \$06 (CVN)</p> <p>ECU #4 supports INF \$03 (MC_CALID), INF \$04 (CALID), INF \$05 (MC_CVN), INF \$06 (CVN)</p> <p>ECU #5 supports INF \$03 (MC_CALID), INF \$04 (CALID), INF \$05 (MC_CVN), INF \$06 (CVN)</p> <p>ECU #6 supports INF \$03 (MC_CALID), INF \$04 (CALID), INF \$05 (MC_CVN), INF \$06 (CVN)</p> <p>ECU #7 supports INF \$03 (MC_CALID), INF \$04 (CALID), INF \$05 (MC_CVN), INF \$06 (CVN)</p> <p>ECU #8 supports INF \$03 (MC_CALID), INF \$04 (CALID), INF \$05 (MC_CVN), INF \$06 (CVN)</p>
--

**Figure 151 - Verify an ISO 14230-4 SID \$09 INF supported INF request**

### 9.3.20 Verify a SID \$09 VIN Request (1 ECU)

<b>Test ID:</b>	\$30209003
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$09 INF \$02.
<b>Rationale:</b>	This test will use a positive response from a single ECU.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the "ISO 14230-4 (2 ECUs) SID9 Ready State", which is detailed in 5.4.4.8.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$09 INF \$02 (VIN).
<u>EVALUATION</u>	Pass if the <b>14230_SID\$09_INF\$01</b> or <b>14230_SID\$09_INF\$02</b> message was transmitted.
<u>ACTION- ECUSIM</u>	<p>If the DUT transmitted <b>14230_SID\$09_INF\$01</b> then after the <i>P2</i> delay, transmit the message <b>14230_ECU1_SID\$09_INF\$01</b> and wait for the DUT to transmit <b>14230_SID\$09_INF\$02</b>.</p> <p>If the DUT transmitted <b>14230_SID\$09_INF\$02</b> then after the <i>P2</i> delay, transmit the messages (with a <i>P2</i> delay between each message): <b>14230_ECU1_SID\$09_INF\$02_M1</b>, <b>14230_ECU1_SID\$09_INF\$02_M2</b>, <b>14230_ECU1_SID\$09_INF\$02_M3</b>, <b>14230_ECU1_SID\$09_INF\$02_M4</b>, <b>14230_ECU1_SID\$09_INF\$02_M5</b>.</p>
<u>EVALUATION</u>	Pass if the transmission is successful and the DUT indicates that VIN is 1GYDE63A7A0000000.

**Figure 152 - Verify an ISO 14230-4 SID \$09 VIN request**

## 9.3.21 Verify a SID \$09 VIN Request with NRC=\$78 (1 ECU)

<b>Test ID:</b>	\$30209004
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$09 INF \$02 when the ECU has an initial NRC = \$78.
<b>Rationale:</b>	This test will use a response pending (NRC \$78) from a single ECU.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the "ISO 14230-4 (2 ECUs) SID9 Ready State", which is detailed in 5.4.4.8.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$09 INF \$02 (VIN).
<u>EVALUATION</u>	Pass if the <b>14230_SID\$09_INF\$01</b> or <b>14230_SID\$09_INF\$02</b> message was transmitted.
<u>ACTION- ECUSIM</u>	If the DUT transmits the message <b>14230_SID\$09_INF\$01</b> , then after the delay <i>P2</i> , transmit the message <b>14230_ECU1_SID\$09_INF\$01</b> . Wait for the remaining message transactions in this ACTION to be completed.  If the DUT transmits the message <b>14230_SID\$09_INF\$02</b> , then after the delay <i>P2_MIN</i> and transmit the message <b>14230_ECU1_SID\$09_NRC78</b> . <u>EVALUATION</u> Pass if the <b>14230_ECU1_SID\$09_NRC78</b> message was transmitted
<u>ACTION- ECUSIM</u>	After the <i>P2</i> delay, transmit the following messages (with a <i>P2</i> delay between each message): <b>14230_ECU1_SID\$09_INF\$02_M1, 14230_ECU1_SID\$09_INF\$02_M2, 14230_ECU1_SID\$09_INF\$02_M3, 14230_ECU1_SID\$09_INF\$02_M4, 14230_ECU1_SID\$09_INF\$02_M5.</b>
<u>EVALUATION</u>	Pass if the transmission is successful and the DUT indicates that VIN is 1GYDE63A7A0000000.

**Figure 153 - Verify an ISO 14230-4 SID \$09 VIN request with NRC=\$78**

## 9.3.22 Verify a SID \$09 CALID Request (2 ECUs w/ Order Swap)

<b>Test ID:</b>	\$30209005
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$09 INF \$04.
<b>Rationale:</b>	This test will use positive responses from two different ECUs. The order of ECU responses will be different than that of the original connection order.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 14230-4 (2 ECUs) SID9 Ready State”, which is detailed in 5.4.4.8.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$09 INF \$04 (CALID).
<u>EVALUATION</u>	Pass if the <b>14230_SID\$09_INF\$03</b> or <b>14230_SID\$09_INF\$04</b> message was transmitted.
<u>ACTION- ECUSIM</u>	
	If the DUT transmitted <b>14230_SID\$09_INF\$03</b> then after the <i>P2</i> delay, transmit the message <b>14230_ECU1_SID\$09_INF\$03</b> and wait for the DUT to transmit <b>14230_SID\$09_INF\$04</b> .
	If the DUT transmitted <b>14230_SID\$09_INF\$04</b> then after the <i>P2</i> delay, transmit the following messages (with a <i>P2</i> delay between each message): <b>14230_ECU2_SID\$09_INF\$04_M1</b> , <b>14230_ECU1_SID\$09_INF\$04_M1</b> , <b>14230_ECU2_SID\$09_INF\$04_M2</b> , <b>14230_ECU1_SID\$09_INF\$04_M2</b> , <b>14230_ECU2_SID\$09_INF\$04_M3</b> , <b>14230_ECU1_SID\$09_INF\$04_M3</b> , <b>14230_ECU2_SID\$09_INF\$04_M4</b> , <b>14230_ECU1_SID\$09_INF\$04_M4</b> .
<u>EVALUATION</u>	Pass if the transmission is successful and the DUT indicates that the CALID for ECU #1 is 12589772 while CALID for ECU #2 is 24221419.

Figure 154 - Verify an ISO 14230-4 SID \$09 CALID request

## 9.3.23 Verify a SID \$09 CALID Request with 9 CALIDs from Each of 8 ECUs (8 ECUs w/ Order Swap)

<b>Test ID:</b>	\$30209006
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$09 INF \$04 when 8 ECUs respond with 9 CALIDs each.
<b>Rationale:</b>	This test will use positive responses from eight different ECUs. The order of ECU responses will be different than that of the original connection order with ECU3 through ECU8 indicating that no SID \$01 PIDs are supported.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 14230-4 (8 ECUs) SID9 Ready State”, which is detailed in 5.4.4.10.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$09 INF \$04 (CALID).
<u>EVALUATION</u>	Pass if the <b>14230_SID\$09_INF\$03</b> or <b>14230_SID\$09_INF\$04</b> message was transmitted.
<u>ACTION- ECUSIM</u>	
	If the DUT transmitted <b>14230_SID\$09_INF\$03</b> then after the <i>P2</i> delay, transmit the following messages (with a <i>P2</i> delay between each message): <b>14230_ECU5_SID\$09_INF\$03</b> , <b>14230_ECU2_SID\$09_INF\$03</b> , <b>14230_ECU6_SID\$09_INF\$03</b> , <b>14230_ECU3_SID\$09_INF\$03</b> ,

14230\_ECU7\_SID\$09\_INF\$03, 14230\_ECU4\_SID\$09\_INF\$03, 14230\_ECU1\_SID\$09\_INF\$03, 14230\_ECU8\_SID\$09\_INF\$03 and wait for the DUT to transmit 14230\_SID\$09\_INF\$04.

If the DUT transmitted 14230\_SID\$09\_INF\$04 then after the *P2* delay, transmit the following messages (with a *P2* delay between each message): 14230\_ECU5\_SID\$09\_INF\$04\_9\_M1 thru 14230\_ECU5\_SID\$09\_INF\$04\_9\_M35, 14230\_ECU2\_SID\$09\_INF\$04\_9\_M1 thru 14230\_ECU2\_SID\$09\_INF\$04\_9\_M35, 14230\_ECU6\_SID\$09\_INF\$04\_9\_M1 thru 14230\_ECU6\_SID\$09\_INF\$04\_9\_M35, 14230\_ECU3\_SID\$09\_INF\$04\_9\_M1 thru 14230\_ECU3\_SID\$09\_INF\$04\_9\_M35, 14230\_ECU7\_SID\$09\_INF\$04\_9\_M1 thru 14230\_ECU7\_SID\$09\_INF\$04\_9\_M35, 14230\_ECU4\_SID\$09\_INF\$04\_9\_M1 thru 14230\_ECU4\_SID\$09\_INF\$04\_9\_M35, 14230\_ECU1\_SID\$09\_INF\$04\_9\_M1 thru 14230\_ECU1\_SID\$09\_INF\$04\_9\_M35, 14230\_ECU8\_SID\$09\_INF\$04\_9\_M1 thru 14230\_ECU8\_SID\$09\_INF\$04\_9\_M35.

#### EVALUATION

Pass if the following is true:

All Transmissions are successful.

The DUT indicates that the CALID for ECU #1 are: "11589778", "21589777", "31589776", "41589775", "51589774", "61589773", "71589772", "81589771", "91589770".

The DUT indicates that the CALID for ECU #2 are: "12589778", "22589777", "32589776", "42589775", "52589774", "62589773", "72589772", "82589771", "92589770".

The DUT indicates that the CALID for ECU #3 are: "13589778", "23589777", "33589776", "43589775", "53589774", "63589773", "73589772", "83589771", "93589770".

The DUT indicates that the CALID for ECU #4 are: "14589778", "24589777", "34589776", "44589775", "54589774", "64589773", "74589772", "84589771", "94589770".

The DUT indicates that the CALID for ECU #5 are: "15589778", "25589777", "35589776", "45589775", "55589774", "65589773", "75589772", "85589771", "95589770".

The DUT indicates that the CALID for ECU #6 are: "16589778", "26589777", "36589776", "46589775", "56589774", "66589773", "76589772", "86589771", "96589770".

The DUT indicates that the CALID for ECU #7 are: "17589778", "27589777", "37589776", "47589775", "57589774", "67589773", "77589772", "87589771", "97589770".

The DUT indicates that the CALID for ECU #8 are: "18589778", "28589777", "38589776", "48589775", "58589774", "68589773", "78589772", "88589771", "98589770".

**Figure 155 - Verify an ISO 14230-4 SID \$09 CALID request with 9 CALIDs on an ECU**

## 9.3.24 Verify a SID \$09 CVN Request (2 ECU)

<b>Test ID:</b>	\$30209007
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$09 INF \$06.
<b>Rationale:</b>	This test will use positive responses from a two ECUs.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 14230-4 (2 ECUs) SID9 Ready State”, which is detailed in 5.4.4.8.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$09 INF \$06 (CVN).
<u>EVALUATION</u>	Pass if the <b>14230_SID\$09_INF\$05</b> or <b>14230_SID\$09_INF\$06</b> message was transmitted.
<u>ACTION- ECUSIM</u>	
	If the DUT transmitted <b>14230_SID\$09_INF\$05</b> then after the <i>P2</i> delay, transmit the following messages (with a <i>P2</i> delay between each message): <b>14230_ECU1_SID\$09_INF\$05</b> , <b>14230_ECU2_SID\$09_INF\$05</b> and wait for the DUT to transmit <b>14230_SID\$09_INF\$06</b> .
	If the DUT transmitted <b>14230_SID\$09_INF\$06</b> then after the <i>P2</i> delay, transmit the following messages (with a <i>P2</i> delay between each message): <b>14230_ECU1_SID\$09_INF\$06</b> and <b>14230_ECU2_SID\$09_INF\$06</b> .
<u>EVALUATION</u>	Pass if the transmission is successful and the DUT indicates that the CVN for ECU #1 is \$1791BC88 and ECU #2 is \$2791BC88.

**Figure 156 - Verify an ISO 14230-4 SID \$09 CVN request**

## 9.3.25 Verify a SID \$09 CVN Request with 9 CVNs (8 ECUs w/ Order Swap)

<b>Test ID:</b>	\$30209008
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$09 INF \$06 when 8 ECUs responds with 9 CVNs each.
<b>Rationale:</b>	This test will use positive responses from eight different ECUs. The order of ECU responses will be different than that of the original connection order with ECU3 through ECU8 indicating that no SID \$01 PIDs are supported.
<b>Set Up:</b>	The DUT and the ECUSIM shall be in the “ISO 14230-4 (8 ECUs) SID9 Ready State”, which is detailed in 5.4.4.10.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$09 INF \$06 (CVN).
<u>EVALUATION</u>	Pass if the <b>14230_SID\$09_INF\$05</b> or <b>14230_SID\$09_INF\$06</b> message was transmitted.
<u>ACTION- ECUSIM</u>	
	If the DUT transmitted <b>14230_SID\$09_INF\$05</b> then after the <i>P2</i> delay, transmit the following messages (with a <i>P2</i> delay between each message): <b>14230_ECU5_SID\$09_INF\$05</b> , <b>14230_ECU2_SID\$09_INF\$05</b> , <b>14230_ECU6_SID\$09_INF\$05</b> , <b>14230_ECU3_SID\$09_INF\$05</b> , <b>14230_ECU7_SID\$09_INF\$05</b> , <b>14230_ECU4_SID\$09_INF\$05</b> , <b>14230_ECU1_SID\$09_INF\$05</b> , <b>14230_ECU8_SID\$09_INF\$05</b> and wait for the DUT to transmit <b>14230_SID\$09_INF\$06</b> .

If the DUT transmitted **14230\_SID\$09\_INF\$06** then after the *P2* delay, transmit the following messages (with a *P2* delay between each message): **14230\_ECU8\_SID\$09\_INF\$06\_9\_M1** to **14230\_ECU8\_SID\$09\_INF\$06\_9\_M9**, **14230\_ECU1\_SID\$09\_INF\$06\_9\_M1** to **14230\_ECU1\_SID\$09\_INF\$06\_9\_M9**, **14230\_ECU7\_SID\$09\_INF\$06\_9\_M1** to **14230\_ECU7\_SID\$09\_INF\$06\_9\_M9**, **14230\_ECU2\_SID\$09\_INF\$06\_9\_M1** to **14230\_ECU2\_SID\$09\_INF\$06\_9\_M9**, **14230\_ECU6\_SID\$09\_INF\$06\_9\_M1** to **14230\_ECU6\_SID\$09\_INF\$06\_9\_M9**, **14230\_ECU3\_SID\$09\_INF\$06\_9\_M1** to **14230\_ECU3\_SID\$09\_INF\$06\_9\_M9**, **14230\_ECU5\_SID\$09\_INF\$06\_9\_M1** to **14230\_ECU5\_SID\$09\_INF\$06\_9\_M9**, **14230\_ECU4\_SID\$09\_INF\$06\_9\_M1** to **14230\_ECU4\_SID\$09\_INF\$06\_9\_M9**.

#### EVALUATION

Pass if the following is true:

Transmission is successful.

The DUT indicates that the CVN for ECU #1 is \$1791BC88, \$1891BC87, \$1991BC86, \$1A91BC85, \$1B91BC84, \$1C91BC83, \$1D91BC82, \$1E55AA81, \$1FFF0080.

The DUT indicates that the CVN for ECU #2 is \$2791BC88, \$2891BC87, \$2991BC86, \$2A91BC85, \$2B91BC84, \$2C91BC83, \$2D91BC82, \$2E55AA81, \$2FFF0080.

The DUT indicates that the CVN for ECU #3 is \$3791BC88, \$3891BC87, \$3991BC86, \$3A91BC85, \$3B91BC84, \$3C91BC83, \$3D91BC82, \$3E55AA81, \$3FFF0080.

The DUT indicates that the CVN for ECU #4 is \$4791BC88, \$4891BC87, \$4991BC86, \$4A91BC85, \$4B91BC84, \$4C91BC83, \$4D91BC82, \$4E55AA81, \$4FFF0080.

The DUT indicates that the CVN for ECU #5 is \$5791BC88, \$5891BC87, \$5991BC86, \$5A91BC85, \$5B91BC84, \$5C91BC83, \$5D91BC82, \$5E55AA81, \$5FFF0080.

The DUT indicates that the CVN for ECU #6 is \$6791BC88, \$6891BC87, \$6991BC86, \$6A91BC85, \$6B91BC84, \$6C91BC83, \$6D91BC82, \$6E55AA81, \$6FFF0080.

The DUT indicates that the CVN for ECU #7 is \$7791BC88, \$7891BC87, \$7991BC86, \$7A91BC85, \$7B91BC84, \$7C91BC83, \$7D91BC82, \$7E55AA81, \$7FFF0080.

The DUT indicates that the CVN for ECU #8 is \$8791BC88, \$8891BC87, \$8991BC86, \$8A91BC85, \$8B91BC84, \$8C91BC83, \$8D91BC82, \$8E55AA81, \$8FFF0080.

**Figure 157 - Verify an ISO 14230-4 SID \$09 CVN request with 9 CVNs on an ECU**

#### 9.3.26 Verify a SID \$09 IPT (INF\$08, 32 bytes) Request (2 ECUs w/ Order Swap)

<b>Test ID:</b>	\$30209009
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$09 INF \$08 (32 bytes).
<b>Rationale:</b>	This test will use positive responses from two ECUs. The order of ECU responses will be different than that of the original connection order.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the "ISO 14230-4 (2 ECUs) SID9 Ready State", which is detailed in 5.4.4.8.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$09 INF \$08 (IPT).
<u>EVALUATION</u>	Pass if the <b>14230_SID\$09_INF\$07</b> or <b>14230_SID\$09_INF\$08</b> message was transmitted.
<u>ACTION- ECUSIM</u>	<p>If the DUT transmitted <b>14230_SID\$09_INF\$07</b> then after the <i>P2</i> delay, transmit the following messages (with a <i>P2</i> delay between each message): <b>14230_ECU2_SID\$09_INF\$07_832</b>, <b>14230_ECU1_SID\$09_INF\$07_832</b> and wait for the DUT to transmit <b>14230_SID\$09_INF\$08</b>.</p> <p>If the DUT transmitted <b>14230_SID\$09_INF\$08</b> then after the <i>P2</i> delay, transmit the following messages (with a <i>P2</i> delay between each message): <b>14230_ECU2_SID\$09_INF\$08_32_M1</b></p>

thru 14230\_ECU2\_SID\$09\_INF\$08\_32\_M8, 14230\_ECU1\_SID\$09\_INF\$08\_32\_M1 thru  
14230\_ECU1\_SID\$09\_INF\$08\_32\_M8.

EVALUATION

Pass if the following is true:

Transmission is successful.

The DUT indicates that the IPT for ECU #1 is 32 bytes and has the following values:

OBDCOND = 100

IGNCNTR = 105

CATCOMP1 = 10

CATCOND1 = 15

CATCOMP2 = 00

CATCOND2 = 00

O2COMP1 = 30

O2COND1 = 35

O2COMP2 = 00

O2COND2 = 00

EGRCOMP = 50

EGRCOND = 55

AIRCOMP = 60

AIRCOND = 65

EVAPCOND = 70

EVAPCOND = 75

The DUT indicates that the IPT for ECU #2 is 32 bytes and has the following values:

OBDCOND = 100

IGNCNTR = 105

CATCOMP1 = 00

CATCOND1 = 00

CATCOMP2 = 20

CATCOND2 = 25

O2COMP1 = 00

O2COND1 = 00

O2COMP2 = 40

O2COND2 = 45

EGRCOMP = 00

EGRCOND = 00

AIRCOMP = 00

AIRCOND = 00

EVAPCOND = 00

EVAPCOND = 00

**Figure 158 - Verify an ISO 14230-4 SID \$09 IPT (INF\$08, 32 bytes) request (11-bit, 500K)**

## 9.3.27 Verify a SID \$09 IPT (INF\$08, 40 bytes) Request (2 ECUs w/ Order Swap)

<b>Test ID:</b>	\$3020900A
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$09 INF \$08 (40 bytes).
<b>Rationale:</b>	This test will use positive responses from two ECUs. The order of ECU responses will be different than that of the original connection order.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 14230-4 (2 ECUs) SID9 Ready State”, which is detailed in 5.4.4.8.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$09 INF \$08 (IPT).
<u>EVALUATION</u>	Pass if the <b>14230_SID\$09_INF\$07</b> or <b>14230_SID\$09_INF\$08</b> message was transmitted.
<u>ACTION- ECUSIM</u>	<p>If the DUT transmitted <b>14230_SID\$09_INF\$07</b> then after the <i>P2</i> delay, transmit the following messages (with a <i>P2</i> delay between each message): <b>14230_ECU2_SID\$09_INF\$07_840</b>, <b>14230_ECU1_SID\$09_INF\$07_840</b> and wait for the DUT to transmit <b>14230_SID\$09_INF\$08</b>.</p> <p>If the DUT transmitted <b>14230_SID\$09_INF\$08</b> then after the <i>P2</i> delay, transmit the following messages (with a <i>P2</i> delay between each message): <b>14230_ECU2_SID\$09_INF\$08_40_M1</b> thru <b>14230_ECU2_SID\$09_INF\$08_40_M10</b>, <b>14230_ECU1_SID\$09_INF\$08_40_M1</b> thru <b>14230_ECU1_SID\$09_INF\$08_40_M10</b>.</p>
<u>EVALUATION</u>	<p>Pass if the following is true:  Transmission is successful.  The DUT indicates that the IPT for ECU #1 is 40 bytes and has the following values:  OBDCOND = 100  IGNCNTR = 105  CATCOMP1 = 10  CATCOND1 = 15  CATCOMP2 = 00.  CATCOND2 = 00  O2COMP1 = 30  O2COND1 = 35  O2COMP2 = 00  O2COND2 = 00  EGRCOMP = 50  EGRCOND = 55  AIRCOMP = 60  AIRCOND = 65  EVAPCOND = 70  EVAPCOND = 75  SO2SCOMP1 = 90  SO2SCOND1 = 95  SO2SCOMP2 = 00  SO2SCOND2 = 00</p> <p>The DUT indicates that the IPT for ECU #2 is 40 bytes and has the following values:  OBDCOND = 100  IGNCNTR = 105  CATCOMP1 = 00  CATCOND1 = 00  CATCOMP2 = 20  CATCOND2 = 25</p>

O2COMP1 = 00 O2COND1 = 00 O2COMP2 = 40 O2COND2 = 45 EGRCOMP = 00 EGRCOND = 00 AIRCOMP = 00 AIRCOND = 00 EVAPCOND = 00 EVAPCOND = 00 SO2SCOMP1 = 00 SO2SCOND1 = 00 SO2SCOMP2 = 90 SO2SCOND2 = 95
--

**Figure 159 - Verify an ISO 14230-4 SID \$09 IPT (INF\$08, 40 bytes) request**

9.3.28 Verify a SID \$09 IPT (INF\$0B, 32 bytes) Request (2 ECUs w/ Order Swap)

<b>Test ID:</b>	\$3020900B
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$09 INF \$0B (32 bytes).
<b>Rationale:</b>	This test will use positive responses from two ECUs. The order of ECU responses will be different than that of the original connection order.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the "ISO 14230-4 (2 ECUs) SID9 Diesel Ready State", which is detailed in 5.4.4.9.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$09 INF \$0B (IPT).
<u>EVALUATION</u>	Pass if the <b>14230_SID\$09_INF\$07</b> or <b>14230_SID\$09_INF\$0B</b> message was transmitted.
<u>ACTION- ECUSIM</u>	<p>If the DUT transmitted <b>14230_SID\$09_INF\$07</b> then after the <i>P2</i> delay, transmit the following messages (with a <i>P2</i> delay between each message): <b>14230_ECU2_SID\$09_INF\$07_B32</b>, <b>14230_ECU1_SID\$09_INF\$07_B32</b> and wait for the DUT to transmit <b>14230_SID\$09_INF\$08</b>.</p> <p>If the DUT transmitted <b>14230_SID\$09_INF\$0B</b> then after the <i>P2</i> delay, transmit the following messages (with a <i>P2</i> delay between each message): <b>14230_ECU2_SID\$09_INF\$0B_32_M1</b> thru <b>14230_ECU2_SID\$09_INF\$0B_32_M8</b>, <b>14230_ECU1_SID\$09_INF\$0B_32_M1</b> thru <b>14230_ECU1_SID\$09_INF\$0B_32_M8</b>.</p>
<u>EVALUATION</u>	Pass if the following is true: Transmission is successful. The DUT indicates that the IPT for ECU #1 is 32 bytes and has the following values: OBDCOND = 100 IGNCNTR = 105 HCCATCOMP1 = 10 HCCATCOND1 = 15 NCATCOMP2 = 00 NCATCOND2 = 00 NADSCOMP1 = 00 NADSCOND1 = 00 PMCOMP2 = 00 PMCOND2 = 00 EGSCOMP = 50

<p>EGSCOND = 55  EGRCOMP = 60  EGRCOND = 65  BPCOND = 70  BPCOND = 75</p> <p>The DUT indicates that the IPT for ECU #2 is 32 bytes and has the following values:  OBDCOND = 100  IGNCNTR = 105  HCCATCOMP1 = 00  HCCATCOND1 = 00  NCATCOMP2 = 20  NCATCOND2 = 25  NADSCOMP1 = 00  NADSCOND1 = 00  PMCOMP2 = 40  PMCOND2 = 45  EGSCOMP = 00  EGSCOND = 00  EGRCOMP = 00  EGRCOND = 00  BPCOND = 00  BPCOND = 00</p>
--

**Figure 160 - Verify an ISO 14230-4 SID \$09 IPT (INF\$0B, 32 bytes) request**

9.3.29 Verify a SID \$09 IPT (INF\$0B, 36 bytes) Request (2 ECUs w/ Order Swap)

<b>Test ID:</b>	\$3020900C
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$09 INF \$0B (36 bytes).
<b>Rationale:</b>	This test will use positive responses from two ECUs. The order of ECU responses will be different than that of the original connection order.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 14230-4 (2 ECUs) SID9 Diesel Ready State”, which is detailed in 5.4.4.9.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$09 INF \$0B (IPT).
<u>EVALUATION</u>	Pass if the <b>14230_SID\$09_INF\$07</b> or <b>14230_SID\$09_INF\$0B</b> message was transmitted.
<u>ACTION- ECUSIM</u>	<p>If the DUT transmitted <b>14230_SID\$09_INF\$07</b> then after the <i>P2</i> delay, transmit the following messages (with a <i>P2</i> delay between each message): <b>14230_ECU2_SID\$09_INF\$07_B36</b>, <b>14230_ECU1_SID\$09_INF\$07_B36</b> and wait for the DUT to transmit <b>14230_SID\$09_INF\$08</b>.</p> <p>If the DUT transmitted <b>14230_SID\$09_INF\$0B</b> then after the <i>P2</i> delay, transmit the following messages (with a <i>P2</i> delay between each message): <b>14230_ECU2_SID\$09_INF\$0B_36_M1</b> thru <b>14230_ECU2_SID\$09_INF\$0B_36_M9</b>, <b>14230_ECU1_SID\$09_INF\$0B_36_M1</b> thru <b>14230_ECU1_SID\$09_INF\$0B_36_M9</b>.</p>
<u>EVALUATION</u>	<p>Pass if the following is true:  Transmission is successful.  The DUT indicates that the IPT for ECU #1 is 36 bytes and has the following values:  OBDCOND = 100  IGNCNTR = 105</p>

HCCATCOMP1 = 10  
 HCCATCOND1 = 15  
 NCATCOMP2 = 00  
 NCATCOND2 = 00  
 NADSCOMP1 = 00  
 NADSCOND1 = 00  
 PMCOMP2 = 00  
 PMCOND2 = 00  
 EGSCOMP = 50  
 EGSCOND = 55  
 EGRCOMP = 60  
 EGRCOND = 65  
 BPCOND = 70  
 BPCOND = 75  
 FUELCOND = 80  
 FUELCOND = 85

The DUT indicates that the IPT for ECU #2 is 36 bytes and has the following values:

OBDCOND = 100  
 IGNCNTR = 105  
 HCCATCOMP1 = 00  
 HCCATCOND1 = 00  
 NCATCOMP2 = 20  
 NCATCOND2 = 25  
 NADSCOMP1 = 00  
 NADSCOND1 = 00  
 PMCOMP2 = 40  
 PMCOND2 = 45  
 EGSCOMP = 00  
 EGSCOND = 00  
 EGRCOMP = 00  
 EGRCOND = 00  
 BPCOND = 00  
 BPCOND = 00  
 FUELCOND = 00  
 FUELCOND = 00

**Figure 161 - Verify an ISO 14230-4 SID \$09 IPT (INF\$0B, 36 bytes) request**

#### 9.4 ISO 9141-2 Tests

These network tests are specific to ISO 9141-2. Unless otherwise specified, all references to Electrical Connections, ECU Addresses, Messages, Message Timing, and pre-defined states will be as described in 5.4.4.7.

##### 9.4.1 Verify a SID \$01 PID Supported PIDs Requests (1 ECU)

<b>Test ID:</b>	\$30301000
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request a SID \$01 PID Supported PIDs.
<b>Rationale:</b>	This test will use positive responses from a single different ECU.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the "ISO 9141-2 (2 ECUs) Initialized State", which is detailed in 5.4.5.6.
<b>Procedure:</b>	
<b>ACTION- DUT</b>	Initiate the request for SID \$01 PID Supported PIDs.

<u>EVALUATION</u>	Pass if the <b>9141_SID\$01_PID\$00</b> or <b>9141_SID\$01_PID\$20</b> or <b>9141_SID\$01_PID\$40</b> message was transmitted.
<u>ACTION- ECUSIM</u>	<p>If the DUT transmitted <b>9141_SID\$01_PID\$00</b> then after the <i>P2</i> delay, transmit <b>9141_ECU1_SID\$01_PID\$00_20</b>, delay <i>P2</i> and transmit <b>9141_ECU2_SID\$01_PID\$00</b>. Wait for the remaining message transactions in this ACTION to be completed.</p> <p>If the DUT transmitted <b>9141_SID\$01_PID\$20</b> then after the <i>P2</i> delay, transmit <b>9141_ECU1_SID\$01_PID\$20</b>, and wait for the remaining message transactions in this ACTION to be completed.</p> <p>If the DUT transmitted <b>9141_SID\$01_PID\$40</b> then after the <i>P2</i> delay, transmit <b>9141_ECU1_SID\$01_PID\$40</b>, and wait for the remaining message transactions in this ACTION to be completed.</p>
<u>EVALUATION</u>	<p>Pass if all transmissions by the DUT and ECUSIM were successful and the DUT indicates that:</p> <p>ECU #1 supports PIDs \$01(Readiness Bits), \$0C (RPM), and \$41 (Monitor Status this drive cycle) are supported.</p> <p>ECU #2 supports PIDs \$01(Readiness Bits) and \$0C (RPM) are supported.</p>

**Figure 162 - Verify an ISO 9141-2 SID \$01 PID supported PID request**

9.4.2 Verify a SID \$01 PID \$01 Request for Spark Ignition (2 ECUs w/ Order Swap)

<b>Test ID:</b>	\$30301001
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request a SID \$01 PID, which the ECUSIM indicates is supported.
<b>Rationale:</b>	This test will use positive responses from two different ECUs. The order of ECU responses will be different than that of the original connection order. This test will also verify that the DUT can differentiate a spark ignition response from a compression ignition response.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the "ISO 9141-2 (2 ECUs) Initialized State", which is detailed in 5.4.5.6.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$01 PID \$01 (I/M Readiness Bits).
<u>EVALUATION</u>	Pass if the <b>9141_SID\$01_PID\$01</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2</i> delay, transmit the message <b>9141_ECU2_SID\$01_PID\$01_SPARK</b> , delay <i>P2</i> , then transmit the message <b>9141_ECU1_SID\$01_PID\$01_SPARK</b> .
<u>EVALUATION</u>	<p>Pass if the following is true:</p> <p>Transmission is successful.</p> <p>The DUT indicates that ECU #1 has the following values:</p> <p>MIL is OFF</p> <p>DCT COUNT is 0</p> <p>SPARK IGNITION MONITORS are SUPPORTED</p> <p>MISFIRE is SUPPORTED and READY</p> <p>FUEL SYSTEM MONITORING is SUPPORTED and READY</p> <p>COMPREHENSIVE COMPONENT MONITORING is SUPPORTED and READY</p> <p>CATALYST MONITORING is SUPPORTED and NOT READY</p> <p>HEATED CATALYST MONITORING is SUPPORTED and NOT READY</p>

EVAPORATIVE SYSTEM MONITORING is SUPPORTED and NOT READY  
 SECONDARY AIR SYSTEM MONITORING is SUPPORTED and NOT READY  
 OXYGEN SENSOR MONITORING is SUPPORTED and NOT READY  
 OXYGEN SENSOR HEATER MONITORING is SUPPORTED and NOT READY  
 EGR/VVT SYSTEM MONITORING is SUPPORTED and NOT READY

The DUT indicates that ECU #2 has the following values:

MIL is ON

DCT COUNT is 1

SPARK IGNITION MONITORS are SUPPORTED

COMPREHENSIVE COMPONENT MONITORING is SUPPORTED and NOT READY

**Figure 163 - Verify an ISO 9141-2 SID \$01 PID \$01 request for spark ignition**

9.4.3 Verify a SID \$01 PID \$01 Request for Compression Ignition (2 ECUs w/ Order Swap)

<b>Test ID:</b>	\$30301002
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request a SID \$01 PID, which the ECUSIM indicates is supported.
<b>Rationale:</b>	This test will use positive responses from two different ECUs. The order of ECU responses will be different than that of the original connection order. This test will also verify that the DUT can differentiate a spark ignition response from a compression ignition response.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the "ISO 9141-2 (2 ECUs) Initialized State", which is detailed in 5.4.5.6.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$01 PID \$01 (I/M Readiness Bits).
<u>EVALUATION</u>	Pass if the <b>9141_SID\$01_PID\$01</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the P2 delay, transmit the message <b>9141_ECU2_SID\$01_PID\$01_COMP</b> , delay P2, then transmit the message <b>9141_ECU1_SID\$01_PID\$01_COMP</b> .
<u>EVALUATION</u>	Pass if the following is true: Transmission is successful. The DUT indicates that ECU #1 has the following values: MIL is OFF DCT COUNT is 0 COMPRESSION IGNITION MONITORS are SUPPORTED MISFIRE is SUPPORTED and READY FUEL SYSTEM MONITORING is SUPPORTED and READY COMPREHENSIVE COMPONENT MONITORING is SUPPORTED and READY NMHC MONITORING is SUPPORTED and NOT READY NOX AFTERTREATMENT MONITORING is SUPPORTED and NOT READY BOOST PRESSURE SYSTEM MONITORING is SUPPORTED and NOT READY EXHAUST GAS SENSOR MONITORING is SUPPORTED and NOT READY PM FILTER MONITORING is SUPPORTED and NOT READY EGR/VVT SYSTEM MONITORING is SUPPORTED and NOT READY  The DUT indicates that ECU #2 has the following values: MIL is ON DCT COUNT is 1 COMPRESSION IGNITION MONITORS are SUPPORTED COMPREHENSIVE COMPONENT MONITORING is SUPPORTED and NOT READY

**Figure 164 - Verify an ISO 9141-2 SID \$01 PID \$01 request for compression ignition**

## 9.4.4 Verify a SID \$01 PID \$0C Request (2 ECUs w/ Order Swap)

<b>Test ID:</b>	\$30301003
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request a SID \$01 PID, which the ECUSIM indicates is supported.
<b>Rationale:</b>	This test will use positive responses from two different ECUs. The order of ECU responses will be different than that of the original connection order.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 9141-2 (2 ECUs) Initialized State”, which is detailed in 5.4.5.6.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$01 PID \$0C (RPM).
<u>EVALUATION</u>	Pass if the <b>9141_SID\$01_PID\$0C</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2</i> delay, transmit the message <b>9141_ECU2_SID\$01_PID\$0C_KOEO</b> , delay <i>P2</i> , then transmit the message <b>9141_ECU1_SID\$01_PID\$0C_KOEO</b> .
<u>EVALUATION</u>	Pass if transmission is successful and the DUT indicates that engine RPM is 0.

**Figure 165 - Verify an ISO 9141-2 SID \$01 PID \$0C request**

## 9.4.5 Verify a SID \$02 PID Freeze Frame DTC Request with No DTCs (2 ECUs w/ Order Swap)

<b>Test ID:</b>	\$30302000
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$02 PID \$02 (indicating that there is no Freeze Frame DTC), which the ECUSIM indicates is supported.
<b>Rationale:</b>	This test will use positive responses from two different ECUs. The order of ECU responses will be different than that of the original connection order.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 9141-2 (2 ECUs) Initialized State”, which is detailed in 5.4.5.6.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$02 PID \$02 (the Freeze Frame DTC).
<u>EVALUATION</u>	Pass if the <b>9141_SID\$02_PID\$02</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2</i> delay, transmit the message <b>9141_ECU2_SID\$02_PID\$02_NoDTC</b> , delay <i>P2</i> , then transmit the message <b>9141_ECU1_SID\$02_PID\$02_NoDTC</b> .
<u>EVALUATION</u>	Pass if transmission is successful and the DUT indicates that there is no Freeze Frame DTC in either ECU.

**Figure 166 - Verify an ISO 9141-2 SID \$02 PID freeze frame DTC request with no DTCs**

## 9.4.6 Verify a SID \$02 PID Freeze Frame DTC Request with DTCs (2 ECUs)

<b>Test ID:</b>	\$30302001
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$02 PID \$02 (indicating that there is a Freeze Frame DTC), which the ECUSIM indicates is supported.
<b>Rationale:</b>	This test will use positive responses from two different ECUs.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 9141-2 (2 ECUs) Initialized State”, which is detailed in 5.4.5.6.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$02 PID \$02 (the Freeze Frame DTC).
<u>EVALUATION</u>	Pass if the <b>9141_SID\$02_PID\$02</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2</i> delay, transmit the message <b>9141_ECU1_SID\$02_PID\$02_DTC</b> , delay <i>P2</i> , then transmit the message <b>9141_ECU2_SID\$02_PID\$02_NoDTC</b> .
<u>EVALUATION</u>	Pass if the following is true: The transmission is successful. The DUT indicates that ECU #1 has a Freeze Frame DTC of P0107 and ECU #2 has no Freeze Frame DTC.

**Figure 167 - Verify an ISO 9141-2 SID \$02 PID freeze frame DTC request with DTCs**

## 9.4.7 Verify a SID \$03 Confirmed DTC Request with No DTCs (2 ECUs w/ Order Swap)

<b>Test ID:</b>	\$30303000
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$03 and process a ‘no DTCs present’ response.
<b>Rationale:</b>	This test will use positive responses from two different ECUs. The order of ECU responses will be different than that of the original connection order.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 9141-2 (2 ECUs) Initialized State”, which is detailed in 5.4.5.6.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$03 (Confirmed DTCs).
<u>EVALUATION</u>	Pass if the <b>9141_SID\$03</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2</i> delay, transmit the message <b>9141_ECU2_SID\$03_NoDTC</b> , delay <i>P2</i> , then transmit the message <b>9141_ECU1_SID\$03_NoDTC</b> .
<u>EVALUATION</u>	Pass if transmission is successful and the DUT indicates that there are no confirmed DTCs in either ECU.

**Figure 168 - Verify an ISO 9141-2 SID \$03 confirmed DTC request with no DTCs**

## 9.4.8 Verify a SID \$03 Confirmed DTC Request with DTCs (2 ECUs)

<b>Test ID:</b>	\$30303001
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$03 and process a 'DTCs present' response.
<b>Rationale:</b>	This test will use positive responses from two different ECUs.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the "ISO 9141-2 (2 ECUs) Initialized State", which is detailed in 5.4.5.6.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$03 (Confirmed DTCs).
<u>EVALUATION</u>	Pass if the <b>9141_SID\$03</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2</i> delay, transmit the following messages (with a <i>P2</i> delay between each message): <b>9141_ECU1_SID\$03_DTC_M1, 9141_ECU1_SID\$03_DTC_M2, 9141_ECU2_SID\$03_NoDTC.</b>
<u>EVALUATION</u>	Pass if the following is true: The transmission is successful. The DUT indicates that ECU #1 has the following confirmed DTCs: P0107, P0110, P0115, and P0124. The DUT indicates that ECU #2 has no confirmed DTCs.

**Figure 169 - Verify an ISO 9141-2 SID \$03 confirmed DTC request with DTCs**

## 9.4.9 Verify a SID \$04 Clear Codes Request (2 ECUs w/ Order Swap)

<b>Test ID:</b>	\$30304000
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$04.
<b>Rationale:</b>	This test will use positive responses from two different ECUs. The order of ECU responses will be different than that of the original connection order.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the "ISO 9141-2 (2 ECUs) Initialized State", which is detailed in 5.4.5.6.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$04 (Clear DTCs).
<u>EVALUATION</u>	Pass if the <b>9141_SID\$04</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2</i> delay, transmit the message <b>9141_ECU2_SID\$04_DONE</b> , delay <i>P2</i> , then transmit the message <b>9141_ECU1_SID\$04_DONE</b> .
<u>EVALUATION</u>	Pass if the transmission is successful and the DUT indicates that DTCs have been cleared.

**Figure 170 - Verify an ISO 9141-2 SID \$04 clear codes request**

## 9.4.10 Test a SID \$04 Clear Codes Request with NRC=\$22 (2 ECUs)

<b>Test ID:</b>	\$30304001
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$04 when an ECU indicates that conditions are not correct.
<b>Rationale:</b>	This test will use negative responses (NRC \$22) from two different ECUs.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 9141-2 (2 ECUs) Initialized State”, which is detailed in 5.4.5.6.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$04 (Clear DTCs).
<u>EVALUATION</u>	Pass if the <b>9141_SID\$04</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2</i> delay, transmit the message <b>9141_ECU1_SID\$04_NRC22</b> , delay <i>P2</i> , then transmit the message <b>9141_ECU2_SID\$04_NRC22</b> .
<u>EVALUATION</u>	Pass if the transmission is successful and the DUT indicates that DTCs have NOT been cleared.

**Figure 171 - Test an ISO 9141-2 SID \$04 clear codes request with NRC=\$22**

## 9.4.11 Verify a SID \$04 Clear Codes Request with NRC=\$78 (2 ECUs)

<b>Test ID:</b>	\$30304002
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$04 when an ECU indicates that it is busy then eventually competes.
<b>Rationale:</b>	This test will use positive responses from the first ECU and a response pending (NRC \$78) from a second ECU.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 9141-2 (2 ECUs) Initialized State”, which is detailed in 5.4.5.6.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$04 (Clear DTCs).
<u>EVALUATION</u>	Pass if the <b>9141_SID\$04</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2</i> delay, transmit the message <b>9141_ECU1_SID\$04_NRC78</b> , delay <i>P2</i> , then transmit the message <b>9141_ECU2_SID\$04_DONE</b> .
<u>EVALUATION</u>	Pass if the transmission is successful.
<u>ACTION- ECUSIM</u>	After the <i>P2_78</i> delay, transmit the message <b>9141_ECU1_SID\$04_DONE</b>
<u>EVALUATION</u>	Pass if the transmission is successful and the DUT indicates that DTCs have been cleared.

**Figure 172 - Verify an ISO 9141-2 SID \$04 clear codes request with NRC=\$78**

## 9.4.12 Verify a SID \$05 TID \$01 Request (1 ECU)

<b>Test ID:</b>	\$30305000
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$05 TID \$01.
<b>Rationale:</b>	This test will use a positive response from a single ECU.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 9141-2 (2 ECUs) Initialized State”, which is detailed in 5.4.5.6.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$05 TID \$01.
<u>EVALUATION</u>	Pass if the <b>9141_SID\$05_TID\$01</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2</i> delay, transmit the message <b>9141_ECU1_SID\$05_MID\$01</b> .
<u>EVALUATION</u>	Pass if the transmission is successful and the DUT indicates that ECU #1 ECU #1 O2 Bank 1 Sensor 2 has a test limit of 450 mV.

**Figure 173 - Verify an ISO 9141-2 SID \$05 TID \$01 request**

## 9.4.13 Verify a SID \$06 MID Supported MID Request (1 ECU)

<b>Test ID:</b>	\$30306000
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$06 MID Supported MIDs.
<b>Rationale:</b>	This test will use a positive response from a single ECU.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 9141-2 (2 ECUs) Initialized State”, which is detailed in 5.4.5.6.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$06 MID \$00 (MID Supported MIDs).
<u>EVALUATION</u>	Pass if the <b>9141_SID\$06_MID\$00</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2</i> delay, transmit the message <b>9141_ECU1_SID\$06_MID\$00</b> .
<u>EVALUATION</u>	Pass if the transmission is successful and the DUT indicates that ECU #1 supports MIDs \$01, \$02, \$05, and \$06.

**Figure 174 - Verify an ISO 9141-2 SID \$06 MID supported MID request**

## 9.4.14 Verify a SID \$07 Pending DTC Request with No DTCs (2 ECUs w/ Order Swap)

<b>Test ID:</b>	\$30307000
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$07 and process a 'no DTCs present' response.
<b>Rationale:</b>	This test will use positive responses from two different ECUs. The order of ECU responses will be different than that of the original connection order.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the "ISO 9141-2 (2 ECUs) Initialized State", which is detailed in 5.4.5.6.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$07 (Pending DTCs).
<u>EVALUATION</u>	Pass if the <b>9141_SID\$07</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2</i> delay, transmit the message <b>9141_ECU2_SID\$07_NoDTC</b> , delay <i>P2</i> , then transmit the message <b>9141_ECU1_SID\$07_NoDTC</b> .
<u>EVALUATION</u>	Pass if transmission is successful and the DUT indicates that there are no pending DTCs in either ECU.

**Figure 175 - Verify an ISO 9141-2 SID \$07 pending DTC request with no DTCs**

## 9.4.15 Verify a SID \$07 Pending DTC Request with DTCs (2 ECUs)

<b>Test ID:</b>	\$30307001
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$07 and process a 'DTCs present' response.
<b>Rationale:</b>	This test will use positive responses from two different ECUs.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the "ISO 9141-2 (2 ECUs) Initialized State", which is detailed in 5.4.5.6.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$07 (Pending DTCs).
<u>EVALUATION</u>	Pass if the <b>9141_SID\$07</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2</i> delay, transmit the following messages (with a <i>P2</i> delay between each message): <b>9141_ECU1_SID\$07_DTC_M1</b> , <b>9141_ECU1_SID\$07_DTC_M2</b> , <b>9141_ECU2_SID\$07_NoDTC</b> .
<u>EVALUATION</u>	Pass if the following is true: The transmission is successful. The DUT indicates that ECU #1 has the following pending DTCs: P0107, P0110, P0115, and P0124. The DUT indicates that ECU #2 has no pending DTCs.

**Figure 176 - Verify an ISO 9141-2 SID \$07 pending DTC request with DTCs**

## 9.4.16 Verify a SID \$08 TID Supported TID Request (1 ECU)

<b>Test ID:</b>	\$30308000
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$08 TID Supported TIDs.
<b>Rationale:</b>	This test will use a positive response from a single ECU.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 9141-2 (2 ECUs) Initialized State”, which is detailed in 5.4.5.6.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$08 TID \$00 (TID Supported TIDs).
<u>EVALUATION</u>	Pass if the <b>9141_SID\$08_PID\$00</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2</i> delay, transmit the message <b>9141_ECU1_SID\$08_PID\$00</b> .
<u>EVALUATION</u>	Pass if the transmission is successful and the DUT indicates that ECU #1 supports TID \$01 (the leak test).

**Figure 177 - Verify an ISO 9141-2 SID \$08 TID supported TID request**

## 9.4.17 Verify a SID \$09 INF Supported INF Request (2 ECUs w/ Order Swap)

<b>Test ID:</b>	\$30309000
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$09 INF Supported INFs.
<b>Rationale:</b>	This test will use positive responses from two different ECUs. The order of ECU responses will be different than that of the original connection order.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 9141-2 (2 ECUs) Initialized State”, which is detailed in 5.4.5.6.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$09 INF \$00 (INF Supported INFs).
<u>EVALUATION</u>	Pass if the <b>9141_SID\$09_PID\$00</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2</i> delay, transmit the message <b>9141_ECU2_SID\$09_INF\$00</b> , delay <i>P2</i> , then transmit the message <b>9141_ECU1_SID\$09_INF\$00</b> .
<u>EVALUATION</u>	Pass if the transmission is successful and the DUT indicates that: ECU #1 supports INF \$01 (MC_VIN), INF \$02 (VIN), INF \$03 (MC_CALID), INF \$04 (CALID), INF \$05 (MC_CVN), INF \$06 (CVN), INF \$07 (MC_IPT), INF \$08 (IPT) ECU #2 supports INF \$03 (MC_CALID), INF \$04 (CALID), INF \$05 (MC_CVN), INF \$06 (CVN), INF \$07 (MC_IPT), INF \$08 (IPT)

**Figure 178 - Verify an ISO 9141-2 SID \$09 INF supported INF request**

## 9.4.18 Verify a SID \$09 INF Supported INF Request Diesel (2 ECUs w/ Order Swap)

<b>Test ID:</b>	\$30309001
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$09 INF Supported INFs.
<b>Rationale:</b>	This test will use positive responses from two different ECUs. The order of ECU responses will be different than that of the original connection order.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 9141-2 (2 ECUs) Initialized State”, which is detailed in 5.4.5.6.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$09 INF \$00 (INF Supported INFs).
<u>EVALUATION</u>	Pass if the <b>9141_SID\$09_PID\$00</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2</i> delay, transmit the message <b>9141_ECU2_SID\$09_INF\$00_DIESEL</b> , delay <i>P2</i> , then transmit the message <b>9141_ECU1_SID\$09_INF\$00_DIESEL</b> .
<u>EVALUATION</u>	Pass if the transmission is successful and the DUT indicates that: ECU #1 supports INF \$01 (MC_VIN), INF \$02 (VIN), INF \$03 (MC_CALID), INF \$04 (CALID), INF \$05 (MC_CVN), INF \$06 (CVN), INF \$07 (MC_IPT), INF \$0B (IPT) ECU #2 supports INF \$03 (MC_CALID), INF \$04 (CALID), INF \$05 (MC_CVN), INF \$06 (CVN), INF \$07 (MC_IPT), INF \$0B (IPT)

**Figure 179 - Verify an ISO 9141-2 SID \$09 INF supported INF request**

## 9.4.19 Verify a SID \$09 INF Supported INF Request (8 ECUs w/ Order Swap)

<b>Test ID:</b>	\$30309002
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$09 INF Supported INFs.
<b>Rationale:</b>	This test will use positive responses from eight different ECUs. The order of ECU responses will be different than that of the original connection order.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 9141-2 (2 ECUs) Initialized State”, which is detailed in 5.4.5.6.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$09 INF \$00 (INF Supported INFs).
<u>EVALUATION</u>	Pass if the <b>9141_SID\$09_PID\$00</b> message was transmitted.
<u>ACTION- ECUSIM</u>	After the <i>P2</i> delay, transmit the following messages (with a <i>P2</i> delay between each message): <b>9141_ECU2_SID\$09_INF\$00, 9141_ECU1_SID\$09_INF\$00, 9141_ECU4_SID\$09_INF\$00, 9141_ECU3_SID\$09_INF\$00, 9141_ECU6_SID\$09_INF\$00, 9141_ECU5_SID\$09_INF\$00, 9141_ECU8_SID\$09_INF\$00, 9141_ECU7_SID\$09_INF\$00.</b>
<u>EVALUATION</u>	Pass if the transmission is successful and the DUT indicates that: ECU #1 supports INF \$01 (MC_VIN), INF \$02 (VIN), INF \$03 (MC_CALID), INF \$04 (CALID), INF \$05 (MC_CVN), INF \$06 (CVN), INF \$07 (MC_IPT), INF \$08 (IPT)

<p>ECU #2 supports INF \$03 (MC_CALID), INF \$04 (CALID), INF \$05 (MC_CVN), INF \$06 (CVN), INF \$07 (MC_IPT), INF \$08 (IPT)</p> <p>ECU #3 supports INF \$03 (MC_CALID), INF \$04 (CALID), INF \$05 (MC_CVN), INF \$06 (CVN)</p> <p>ECU #4 supports INF \$03 (MC_CALID), INF \$04 (CALID), INF \$05 (MC_CVN), INF \$06 (CVN)</p> <p>ECU #5 supports INF \$03 (MC_CALID), INF \$04 (CALID), INF \$05 (MC_CVN), INF \$06 (CVN)</p> <p>ECU #6 supports INF \$03 (MC_CALID), INF \$04 (CALID), INF \$05 (MC_CVN), INF \$06 (CVN)</p> <p>ECU #7 supports INF \$03 (MC_CALID), INF \$04 (CALID), INF \$05 (MC_CVN), INF \$06 (CVN)</p> <p>ECU #8 supports INF \$03 (MC_CALID), INF \$04 (CALID), INF \$05 (MC_CVN), INF \$06 (CVN)</p>
--

**Figure 180 - Verify an ISO 9141-2 SID \$09 INF supported INF request**

9.4.20 Verify a SID \$09 VIN Request (1 ECU)

<b>Test ID:</b>	\$30309003
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$09 INF \$02.
<b>Rationale:</b>	This test will use a positive response from a single ECU.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the "ISO 9141-2 (2 ECUs) SID9 Ready State", which is detailed in 5.4.5.8.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$09 INF \$02 (VIN).
<u>EVALUATION</u>	Pass if the <b>9141_SID\$09_INF\$01</b> or <b>9141_SID\$09_INF\$02</b> message was transmitted.
<u>ACTION- ECUSIM</u>	<p>If the DUT transmitted <b>9141_SID\$09_INF\$01</b> then after the <i>P2</i> delay, transmit the message <b>9141_ECU1_SID\$09_INF\$01</b> and wait for the DUT to transmit <b>9141_SID\$09_INF\$02</b>.</p> <p>If the DUT transmitted <b>9141_SID\$09_INF\$02</b> then after the <i>P2</i> delay, transmit the messages (with a <i>P2</i> delay between each message): <b>9141_ECU1_SID\$09_INF\$02_M1</b>, <b>9141_ECU1_SID\$09_INF\$02_M2</b>, <b>9141_ECU1_SID\$09_INF\$02_M3</b>, <b>9141_ECU1_SID\$09_INF\$02_M4</b>, <b>9141_ECU1_SID\$09_INF\$02_M5</b>.</p>
<u>EVALUATION</u>	Pass if the transmission is successful and the DUT indicates that VIN is 1GYDE63A7A0000000.

**Figure 181 - Verify an ISO 9141-2 SID \$09 VIN request**

## 9.4.21 Verify a SID \$09 VIN Request with NRC=\$78 (1 ECU)

<b>Test ID:</b>	\$30309004
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$09 INF \$02 when the ECU has an initial NRC = \$78.
<b>Rationale:</b>	This test will use a response pending (NRC \$78) from a single ECU.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the "ISO 9141-2 (2 ECUs) SID9 Ready State", which is detailed in 5.4.5.8.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$09 INF \$02 (VIN).
<u>EVALUATION</u>	Pass if the <b>9141_SID\$09_INF\$01</b> or <b>9141_SID\$09_INF\$02</b> message was transmitted.
<u>ACTION- ECUSIM</u>	If the DUT transmits the message <b>9141_SID\$09_INF\$01</b> , then after the delay <i>P2</i> , transmit the message <b>9141_ECU1_SID\$09_INF\$01</b> . Wait for the remaining message transactions in this ACTION to be completed.  If the DUT transmits the message <b>9141_SID\$09_INF\$02</b> , then after the delay <i>P2_MIN</i> and transmit the message <b>9141_ECU1_SID\$09_NRC78</b> .
<u>EVALUATION</u>	Pass if the <b>9141_ECU1_SID\$09_NRC78</b> message was transmitted
<u>ACTION- ECUSIM</u>	After the <i>P2</i> delay, transmit the following messages (with a <i>P2</i> delay between each message): <b>9141_ECU1_SID\$09_INF\$02_M1</b> , <b>9141_ECU1_SID\$09_INF\$02_M2</b> , <b>9141_ECU1_SID\$09_INF\$02_M3</b> , <b>9141_ECU1_SID\$09_INF\$02_M4</b> , <b>9141_ECU1_SID\$09_INF\$02_M5</b> .
<u>EVALUATION</u>	Pass if the transmission is successful and the DUT indicates that VIN is 1GYDE63A7A0000000.

**Figure 182 - Verify an ISO 9141-2 SID \$09 VIN request with NRC=\$78**

## 9.4.22 Verify a SID \$09 CALID Request (2 ECUs w/ Order Swap)

<b>Test ID:</b>	\$30309005
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$09 INF \$04.
<b>Rationale:</b>	This test will use positive responses from two different ECUs. The order of ECU responses will be different than that of the original connection order.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 9141-2 (2 ECUs) SID9 Ready State”, which is detailed in 5.4.5.8.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$09 INF \$04 (CALID).
<u>EVALUATION</u>	Pass if the <b>9141_SID\$09_INF\$03</b> or <b>9141_SID\$09_INF\$04</b> message was transmitted.
<u>ACTION- ECUSIM</u>	
	If the DUT transmitted <b>9141_SID\$09_INF\$03</b> then after the <i>P2</i> delay, transmit the message <b>9141_ECU1_SID\$09_INF\$03</b> and wait for the DUT to transmit <b>9141_SID\$09_INF\$04</b> .
	If the DUT transmitted <b>9141_SID\$09_INF\$04</b> then after the <i>P2</i> delay, transmit the following messages (with a <i>P2</i> delay between each message): <b>9141_ECU2_SID\$09_INF\$04_M1, 9141_ECU1_SID\$09_INF\$04_M1, 9141_ECU2_SID\$09_INF\$04_M2, 9141_ECU1_SID\$09_INF\$04_M2, 9141_ECU2_SID\$09_INF\$04_M3, 9141_ECU1_SID\$09_INF\$04_M3, 9141_ECU2_SID\$09_INF\$04_M4, 9141_ECU1_SID\$09_INF\$04_M4.</b>
<u>EVALUATION</u>	Pass if the transmission is successful and the DUT indicates that the CALID for ECU #1 is 12589772 while CALID for ECU #2 is 24221419.

Figure 183 - Verify an ISO 9141-2 SID \$09 CALID request

## 9.4.23 Verify a SID \$09 CALID Request with 9 CALIDs (8 ECUs w/ Order Swap)

<b>Test ID:</b>	\$30309006
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$09 INF \$04 when 8 ECUs respond with 9 CALIDs each.
<b>Rationale:</b>	This test will use positive responses from eight different ECUs. The order of ECU responses will be different than that of the original connection order with ECU3 through ECU8 indicating that no SID \$01 PIDs are supported.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 9141-2 (8 ECUs) SID9 Ready State”, which is detailed in 5.4.5.10.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$09 INF \$04 (CALID).
<u>EVALUATION</u>	Pass if the <b>9141_SID\$09_INF\$03</b> or <b>9141_SID\$09_INF\$04</b> message was transmitted.
<u>ACTION- ECUSIM</u>	
	If the DUT transmitted <b>9141_SID\$09_INF\$03</b> then after the <i>P2</i> delay, transmit the following messages (with a <i>P2</i> delay between each message): <b>9141_ECU5_SID\$09_INF\$03, 9141_ECU2_SID\$09_INF\$03, 9141_ECU6_SID\$09_INF\$03, 9141_ECU3_SID\$09_INF\$03, 9141_ECU7_SID\$09_INF\$03, 9141_ECU4_SID\$09_INF\$03,</b>

**9141\_ECU1\_SID\$09\_INF\$03, 9141\_ECU8\_SID\$09\_INF\$03** and wait for the DUT to transmit **9141\_SID\$09\_INF\$04**.

If the DUT transmitted **9141\_SID\$09\_INF\$04** then after the *P2* delay, transmit the following messages (with a *P2* delay between each message): **9141\_ECU5\_SID\$09\_INF\$04\_9\_M1** thru **9141\_ECU5\_SID\$09\_INF\$04\_9\_M35**, **9141\_ECU2\_SID\$09\_INF\$04\_9\_M1** thru **9141\_ECU2\_SID\$09\_INF\$04\_9\_M35**, **9141\_ECU6\_SID\$09\_INF\$04\_9\_M1** thru **9141\_ECU6\_SID\$09\_INF\$04\_9\_M35**, **9141\_ECU3\_SID\$09\_INF\$04\_9\_M1** thru **9141\_ECU3\_SID\$09\_INF\$04\_9\_M35**, **9141\_ECU7\_SID\$09\_INF\$04\_9\_M1** thru **9141\_ECU7\_SID\$09\_INF\$04\_9\_M35**, **9141\_ECU4\_SID\$09\_INF\$04\_9\_M1** thru **9141\_ECU4\_SID\$09\_INF\$04\_9\_M35**, **9141\_ECU1\_SID\$09\_INF\$04\_9\_M1** thru **9141\_ECU1\_SID\$09\_INF\$04\_9\_M35**, **9141\_ECU8\_SID\$09\_INF\$04\_9\_M1** thru **9141\_ECU8\_SID\$09\_INF\$04\_9\_M35**.

#### EVALUATION

Pass if the following is true:

All Transmissions are successful.

The DUT indicates that the CALID for ECU #1 are: "11589778", "21589777", "31589776", "41589775", "51589774", "61589773", "71589772", "81589771", "91589770".

The DUT indicates that the CALID for ECU #2 are: "12589778", "22589777", "32589776", "42589775", "52589774", "62589773", "72589772", "82589771", "92589770".

The DUT indicates that the CALID for ECU #3 are: "13589778", "23589777", "33589776", "43589775", "53589774", "63589773", "73589772", "83589771", "93589770".

The DUT indicates that the CALID for ECU #4 are: "14589778", "24589777", "34589776", "44589775", "54589774", "64589773", "74589772", "84589771", "94589770".

The DUT indicates that the CALID for ECU #5 are: "15589778", "25589777", "35589776", "45589775", "55589774", "65589773", "75589772", "85589771", "95589770".

The DUT indicates that the CALID for ECU #6 are: "16589778", "26589777", "36589776", "46589775", "56589774", "66589773", "76589772", "86589771", "96589770".

The DUT indicates that the CALID for ECU #7 are: "17589778", "27589777", "37589776", "47589775", "57589774", "67589773", "77589772", "87589771", "97589770".

The DUT indicates that the CALID for ECU #8 are: "18589778", "28589777", "38589776", "48589775", "58589774", "68589773", "78589772", "88589771", "98589770".

**Figure 184 - Verify an ISO 9141-2 SID \$09 CALID request with 9 CALIDs on an ECU**

## 9.4.24 Verify a SID \$09 CVN Request (2 ECU)

<b>Test ID:</b>	\$30309007
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$09 INF \$06.
<b>Rationale:</b>	This test will use positive responses from a two ECUs.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 9141-2 (2 ECUs) SID9 Ready State”, which is detailed in 5.4.5.8.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$09 INF \$06 (CVN).
<u>EVALUATION</u>	Pass if the <b>9141_SID\$09_INF\$05</b> or <b>9141_SID\$09_INF\$06</b> message was transmitted.
<u>ACTION- ECUSIM</u>	
If the DUT transmitted <b>9141_SID\$09_INF\$05</b> then after the <i>P2</i> delay, transmit the following messages (with a <i>P2</i> delay between each message): <b>9141_ECU1_SID\$09_INF\$05</b> , <b>9141_ECU2_SID\$09_INF\$05</b> and wait for the DUT to transmit <b>9141_SID\$09_INF\$06</b> .	
If the DUT transmitted <b>9141_SID\$09_INF\$06</b> then after the <i>P2</i> delay, transmit the following messages (with a <i>P2</i> delay between each message): <b>9141_ECU1_SID\$09_INF\$06</b> and <b>9141_ECU2_SID\$09_INF\$06</b> .	
<u>EVALUATION</u>	Pass if the transmission is successful and the DUT indicates that the CVN for ECU #1 is \$1791BC88 and ECU #2 is \$2791BC88.

**Figure 185 - Verify an ISO 9141-2 SID \$09 CVN request**

## 9.4.25 Verify a SID \$09 CVN Request with 9 CVNs (8 ECUs w/ Order Swap)

<b>Test ID:</b>	\$30309008
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$09 INF \$06 when 8 ECUs responds with 9 CVNs each.
<b>Rationale:</b>	This test will use positive responses from eight different ECUs. The order of ECU responses will be different than that of the original connection order with ECU3 through ECU8 indicating that no SID \$01 PIDs are supported.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 9141-2 (8 ECUs) SID9 Ready State”, which is detailed in 5.4.5.10.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$09 INF \$06 (CVN).
<u>EVALUATION</u>	Pass if the <b>9141_SID\$09_INF\$05</b> or <b>9141_SID\$09_INF\$06</b> message was transmitted.
<u>ACTION- ECUSIM</u>	
If the DUT transmitted <b>9141_SID\$09_INF\$05</b> then after the <i>P2</i> delay, transmit the following messages (with a <i>P2</i> delay between each message): <b>9141_ECU5_SID\$09_INF\$05, 9141_ECU2_SID\$09_INF\$05, 9141_ECU6_SID\$09_INF\$05, 9141_ECU3_SID\$09_INF\$05, 9141_ECU7_SID\$09_INF\$05, 9141_ECU4_SID\$09_INF\$05, 9141_ECU1_SID\$09_INF\$05, 9141_ECU8_SID\$09_INF\$05</b> and wait for the DUT to transmit <b>9141_SID\$09_INF\$06</b> .	

If the DUT transmitted **9141\_SID\$09\_INF\$06** then after the *P2* delay, transmit the following messages (with a *P2* delay between each message): **9141\_ECU8\_SID\$09\_INF\$06\_9\_M1** to **9141\_ECU8\_SID\$09\_INF\$06\_9\_M9**, **9141\_ECU1\_SID\$09\_INF\$06\_9\_M1** to **9141\_ECU1\_SID\$09\_INF\$06\_9\_M9**, **9141\_ECU7\_SID\$09\_INF\$06\_9\_M1** to **9141\_ECU7\_SID\$09\_INF\$06\_9\_M9**, **9141\_ECU2\_SID\$09\_INF\$06\_9\_M1** to **9141\_ECU2\_SID\$09\_INF\$06\_9\_M9**, **9141\_ECU6\_SID\$09\_INF\$06\_9\_M1** to **9141\_ECU6\_SID\$09\_INF\$06\_9\_M9**, **9141\_ECU3\_SID\$09\_INF\$06\_9\_M1** to **9141\_ECU3\_SID\$09\_INF\$06\_9\_M9**, **9141\_ECU5\_SID\$09\_INF\$06\_9\_M1** to **9141\_ECU5\_SID\$09\_INF\$06\_9\_M9**, **9141\_ECU4\_SID\$09\_INF\$06\_9\_M1** to **9141\_ECU4\_SID\$09\_INF\$06\_9\_M9**.

#### EVALUATION

Pass if the following is true:

Transmission is successful.

The DUT indicates that the CVN for ECU #1 is \$1791BC88, \$1891BC87, \$1991BC86, \$1A91BC85, \$1B91BC84, \$1C91BC83, \$1D91BC82, \$1E55AA81, \$1FFF0080.

The DUT indicates that the CVN for ECU #2 is \$2791BC88, \$2891BC87, \$2991BC86, \$2A91BC85, \$2B91BC84, \$2C91BC83, \$2D91BC82, \$2E55AA81, \$2FFF0080.

The DUT indicates that the CVN for ECU #3 is \$3791BC88, \$3891BC87, \$3991BC86, \$3A91BC85, \$3B91BC84, \$3C91BC83, \$3D91BC82, \$3E55AA81, \$3FFF0080.

The DUT indicates that the CVN for ECU #4 is \$4791BC88, \$4891BC87, \$4991BC86, \$4A91BC85, \$4B91BC84, \$4C91BC83, \$4D91BC82, \$4E55AA81, \$4FFF0080.

The DUT indicates that the CVN for ECU #5 is \$5791BC88, \$5891BC87, \$5991BC86, \$5A91BC85, \$5B91BC84, \$5C91BC83, \$5D91BC82, \$5E55AA81, \$5FFF0080.

The DUT indicates that the CVN for ECU #6 is \$6791BC88, \$6891BC87, \$6991BC86, \$6A91BC85, \$6B91BC84, \$6C91BC83, \$6D91BC82, \$6E55AA81, \$6FFF0080.

The DUT indicates that the CVN for ECU #7 is \$7791BC88, \$7891BC87, \$7991BC86, \$7A91BC85, \$7B91BC84, \$7C91BC83, \$7D91BC82, \$7E55AA81, \$7FFF0080.

The DUT indicates that the CVN for ECU #8 is \$8791BC88, \$8891BC87, \$8991BC86, \$8A91BC85, \$8B91BC84, \$8C91BC83, \$8D91BC82, \$8E55AA81, \$8FFF0080.

**Figure 186 - Verify an ISO 9141-2 SID \$09 CVN request with 9 CVNs on an ECU**

#### 9.4.26 Verify a SID \$09 IPT (INF\$08, 32 bytes) Request (2 ECUs w/ Order Swap)

<b>Test ID:</b>	\$30309009
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$09 INF \$08 (32 bytes).
<b>Rationale:</b>	This test will use positive responses from two ECUs. The order of ECU responses will be different than that of the original connection order.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the "ISO 9141-2 (2 ECUs) SID( Ready State", which is detailed in 5.4.5.8.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$09 INF \$08 (IPT).
<u>EVALUATION</u>	Pass if the <b>9141_SID\$09_INF\$07</b> or <b>9141_SID\$09_INF\$08</b> message was transmitted.
<u>ACTION- ECUSIM</u>	<p>If the DUT transmitted <b>9141_SID\$09_INF\$07</b> then after the <i>P2</i> delay, transmit the following messages (with a <i>P2</i> delay between each message): <b>9141_ECU2_SID\$09_INF\$07_832</b>, <b>9141_ECU1_SID\$09_INF\$07_832</b> and wait for the DUT to transmit <b>9141_SID\$09_INF\$08</b>.</p> <p>If the DUT transmitted <b>9141_SID\$09_INF\$08</b> then after the <i>P2</i> delay, transmit the following messages (with a <i>P2</i> delay between each message): <b>9141_ECU2_SID\$09_INF\$08_32_M1</b></p>

thru 9141\_ECU2\_SID\$09\_INF\$08\_32\_M8, 9141\_ECU1\_SID\$09\_INF\$08\_32\_M1 thru 9141\_ECU1\_SID\$09\_INF\$08\_32\_M8.

EVALUATION

Pass if the following is true:

Transmission is successful.

The DUT indicates that the IPT for ECU #1 is 32 bytes and has the following values:

OBDCOND = 100

IGNCNTR = 105

CATCOMP1 = 10

CATCOND1 = 15

CATCOMP2 = 00

CATCOND2 = 00

O2COMP1 = 30

O2COND1 = 35

O2COMP2 = 00

O2COND2 = 00

EGRCOMP = 50

EGRCOND = 55

AIRCOMP = 60

AIRCOND = 65

EVAPCOND = 70

EVAPCOND = 75

The DUT indicates that the IPT for ECU #2 is 32 bytes and has the following values:

OBDCOND = 100

IGNCNTR = 105

CATCOMP1 = 00

CATCOND1 = 00

CATCOMP2 = 20

CATCOND2 = 25

O2COMP1 = 00

O2COND1 = 00

O2COMP2 = 40

O2COND2 = 45

EGRCOMP = 00

EGRCOND = 00

AIRCOMP = 00

AIRCOND = 00

EVAPCOND = 00

EVAPCOND = 00

**Figure 187 - Verify an ISO 9141-2 SID \$09 IPT (INF\$08, 32 bytes) request (11-bit, 500K)**

## 9.4.27 Verify a SID \$09 IPT (INF\$08, 40 bytes) Request (2 ECUs w/ Order Swap)

<b>Test ID:</b>	\$3030900A
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$09 INF \$08 (40 bytes).
<b>Rationale:</b>	This test will use positive responses from two ECUs. The order of ECU responses will be different than that of the original connection order.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the “ISO 9141-2 (2 ECUs) SID9 Ready State”, which is detailed in 5.4.5.8.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$09 INF \$08 (IPT).
<u>EVALUATION</u>	Pass if the <b>9141_SID\$09_INF\$07</b> or <b>9141_SID\$09_INF\$08</b> message was transmitted.
<u>ACTION- ECUSIM</u>	<p>If the DUT transmitted <b>9141_SID\$09_INF\$07</b> then after the <i>P2</i> delay, transmit the following messages (with a <i>P2</i> delay between each message): <b>9141_ECU2_SID\$09_INF\$07_840</b>, <b>9141_ECU1_SID\$09_INF\$07_840</b> and wait for the DUT to transmit <b>9141_SID\$09_INF\$08</b>.</p> <p>If the DUT transmitted <b>9141_SID\$09_INF\$08</b> then after the <i>P2</i> delay, transmit the following messages (with a <i>P2</i> delay between each message): <b>9141_ECU2_SID\$09_INF\$08_40_M1</b> thru <b>9141_ECU2_SID\$09_INF\$08_40_M10</b>, <b>9141_ECU1_SID\$09_INF\$08_40_M1</b> thru <b>9141_ECU1_SID\$09_INF\$08_40_M10</b>.</p>
<u>EVALUATION</u>	<p>Pass if the following is true:  Transmission is successful.  The DUT indicates that the IPT for ECU #1 is 40 bytes and has the following values:  OBDCOND = 100  IGNCNTR = 105  CATCOMP1 = 10  CATCOND1 = 15  CATCOMP2 = 00.  CATCOND2 = 00  O2COMP1 = 30  O2COND1 = 35  O2COMP2 = 00  O2COND2 = 00  EGRCOMP = 50  EGRCOND = 55  AIRCOMP = 60  AIRCOND = 65  EVAPCOND = 70  EVAPCOND = 75  SO2SCOMP1 = 90  SO2SCOND1 = 95  SO2SCOMP2 = 00  SO2SCOND2 = 00</p> <p>The DUT indicates that the IPT for ECU #2 is 40 bytes and has the following values:  OBDCOND = 100  IGNCNTR = 105  CATCOMP1 = 00  CATCOND1 = 00  CATCOMP2 = 20  CATCOND2 = 25</p>

O2COMP1 = 00
O2COND1 = 00
O2COMP2 = 40
O2COND2 = 45
EGRCOMP = 00
EGRCOND = 00
AIRCOMP = 00
AIRCOND = 00
EVAPCOND = 00
EVAPCOND = 00
SO2SCOMP1 = 00
SO2SCOND1 = 00
SO2SCOMP2 = 90
SO2SCOND2 = 95

**Figure 188 - Verify an ISO 9141-2 SID \$09 IPT (INF\$08, 40 bytes) request**

9.4.28 Verify a SID \$09 IPT (INF\$0B, 32 bytes) Request (2 ECUs w/ Order Swap)

<b>Test ID:</b>	\$3030900B
<b>Purpose:</b>	The purpose of this test is to verify that the DUT can correctly request SID \$09 INF \$0B (32 bytes).
<b>Rationale:</b>	This test will use positive responses from two ECUs. The order of ECU responses will be different than that of the original connection order.
<b>Set Up:</b>	The DUT and ECUSIM shall be in the "ISO 9141-2 (2 ECUs) SID9 Diesel Ready State", which is detailed in 5.4.5.9.
<b>Procedure:</b>	
<u>ACTION- DUT</u>	Initiate the request for SID \$09 INF \$0B (IPT).
<u>EVALUATION</u>	Pass if the <b>9141_SID\$09_INF\$07</b> or <b>9141_SID\$09_INF\$0B</b> message was transmitted.
<u>ACTION- ECUSIM</u>	<p>If the DUT transmitted <b>9141_SID\$09_INF\$07</b> then after the <i>P2</i> delay, transmit the following messages (with a <i>P2</i> delay between each message): <b>9141_ECU2_SID\$09_INF\$07_B32</b>, <b>9141_ECU1_SID\$09_INF\$07_B32</b> and wait for the DUT to transmit <b>9141_SID\$09_INF\$08</b>.</p> <p>If the DUT transmitted <b>9141_SID\$09_INF\$0B</b> then After the <i>P2</i> delay, transmit the following messages (with a <i>P2</i> delay between each message): <b>9141_ECU2_SID\$09_INF\$0B_32_M1</b> thru <b>9141_ECU2_SID\$09_INF\$0B_32_M8</b>, <b>9141_ECU1_SID\$09_INF\$0B_32_M1</b> thru <b>9141_ECU1_SID\$09_INF\$0B_32_M8</b>.</p>
<u>EVALUATION</u>	<p>Pass if the following is true:  Transmission is successful.  The DUT indicates that the IPT for ECU #1 is 32 bytes and has the following values:  OBDCOND = 100  IGNCNTR = 105  HCCATCOMP1 = 10  HCCATCOND1 = 15  NCATCOMP2 = 00  NCATCOND2 = 00  NADSCOMP1 = 00  NADSCOND1 = 00  PMCOMP2 = 00  PMCOND2 = 00  EGSCOMP = 50</p>