INTERNATIONAL STANDARD ISO/IEC 14496-3:2009/Amd.4:2013
TECHNICAL CORRIGENDUM 1

Published 2015-04-01

INTERNATIONAL ORGANIZATION FOR STANDARDIZATION • МЕЖДУНАРОДНАЯ ОРГАНИЗАЦИЯ ПО СТАНДАРТИЗАЦИИ • ORGANISATION INTERNATIONALE DE NORMALISATION
INTERNATIONAL ELECTROTECHNICAL COMMISSION • МЕЖДУНАРОДНАЯ ЭЛЕКТРОТЕХНИЧЕСКАЯ КОМИССИЯ • COMMISSION ÉLECTROTECHNIQUE INTERNATIONALE

Information technology — Coding of audio-visual objects

Part 3:
Audio

AMENDMENT 4: New levels for AAC profiles

TECHNICAL CORRIGENDUM 1

Technologies de l'information — Codage des objets audiovisuels

Partie 3: Codage audio

AMENDEMENT 4: Nouveaux niveaux pour profils AAC

RECTIFICATIF TECHNIQUE 1

Technical Corrigendum 1 to ISO/IEC 14496-3:2014 was prepared by Joint Technical Committee ISO/IEC JTC 1, *Information technology*, Subcommittee SC 29, *Coding of audio, picture, multimedia and hypermedia*.

STANDARDSISO.COM : Click to view the full PDF of ISO/IEC 14496-3:2009/Amd 4:2013/Cor 1:2015

1 Changes to the text of ISO/IEC 14496-3:2009/Amd 4:2013

In 4.5.2.14.1.1 replace

"Downmix loudness compensation gain sign information. One bit indicating the sign of the global downmix gain factor for a 2-ch stereo downmix "

ICS 35.040 **Ref. No. ISO/IEC 14496-3:2009/Amd.4:2013/Cor.1:2015(E)**

© ISO/IEC 2015 – All rights reserved

Published in Switzerland

With

"Downmix loudness compensation gain sign information. One bit indicating the sign of the global downmix gain factor for a 2-ch stereo downmix. (0 if positive, 1 if negative)".

and replace:

dmix_a_idx indicates an index for the generation of a 5-channel downmix as shown in Tables XX and XX

dmix_b_idx indicates an index for the generation of a 5-channel downmix as shown in Tables XX and XX

by:

dmix_a_idx indicates an index for the generation of a 5-channel downmix as shown in Tables AMD4.8 and AMD4.9

dmix_b_idx indicates an index for the generation of a 5-channel downmix as shown in Tables AMD4.8 and AMD4.9

In 4.5.2.14.1.2 replace

"**4.5.2.14.1.2**"
With

"**4.5.2.14.1.2 Integration in bitstream**".

In 4.5.2.14.2.2 replace
"

Channel Configuration	dmix_a_idx	dmix_b_idx
7.1 Back, 6.1	d1	d2
7.1 Front	e1	e2
7.1 Top	f1	f2

"
with

"

Channel Configuration	Multiplication factor of dmix_a_idx	Multiplication factor of dmix_b_idx
7.1 Back, 6.1	d1	d2
7.1 Front	e1	e2
7.1 Top	f1	f2

"
In 4.5.2.14.2.2.1 replace

"Rs' = Rs × d1 + Rsr' × d2"

With

" Rs' = Rs × d1 + Rsr × d2".

STANDARDSISO.COM : Click to view the full PDF of ISO/IEC 14496-3:2009/Amd 4:2013/Cor 1:2015

© ISO/IEC 2015 – All rights reserved

In 4.5.2.14.2.2.3 replace

"C, L, R, Ls, Rs, Lvh, Rvh, LFE are the source signals and C', L', R', Ls', Rs', LFE' are the derived 5.1 channel signals."

With

"C, L, R, Ls, Rs, Lv, Rv, LFE are the source signals and C', L', R', Ls', Rs', LFE' are the derived 5.1 channel signals.".

In 4.5.2.14.2.3 replace

"**dmx_gain_5** indicates the correction for 7-channel to 5-channel downmix and **dmx_gain_2** for the 5-channel to 2-channel downmix."

With

" **dmx_gain_5** indicates the correction factor for 7-channel to 5-channel downmix and **dmx_gain_2** for the 5-channel to 2-channel downmix.".

And replace

"In case of downmixing from 7 to 2 channels the gains shall be applied in combination (**dmx_gain_5** + **dmx_gain_2**)."

With

"In case of downmixing from 7 to 2 channels the gains shall be applied in combination (**dmx_gain_5** × **dmx_gain_2**)."

In 4.5.2.14.2.3.1 replace

"dmx_gain_5 = 10 × (dmx_gain_5_idx/80), if dmx_gain_5_sign == 0

dmx_gain_5 = 10 × (-dmx_gain_5_idx/80), if dmx_gain_5_sign == 1"

with

"dmx_gain_5 = 10 ^ (dmx_gain_5_idx/80), if dmx_gain_5_sign == 0

dmx_gain_5 = 10 ^ (-dmx_gain_5_idx/80), if dmx_gain_5_sign == 1"

In 4.5.2.14.2.3.2 replace

"dmx_gain_2 = 10 × (dmx_gain_2_idx/80), if dmx_gain_2_sign == 0

dmx_gain_2 = 10 × (-dmx_gain_2_idx/80), if dmx_gain_2_sign == 1"

with

"dmx_gain_2 = 10 ^ (dmx_gain_2_idx/80), if dmx_gain_2_sign == 0

dmx_gain_2 = 10 ^ (-dmx_gain_2_idx/80), if dmx_gain_2_sign == 1"

In 4.5.2.14.2.2.3 replace
"

STANDARDSISO.COM : Click to view the full PDF of ISO/IEC 14496-3:2009/Amd 4:2013/Cor 1:2015

© ISO/IEC 2015 – All rights reserved