INTERNATIONAL STANDARD **ISO** 2593

Connector pin allocations for use with high-speed data terminal equipment

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FOREWORD

ISO (the International Organization for Standardization) is a worldwide federation of national standards institutes (ISO Member Bodies). The work of developing International Standards is carried out through ISO Technical Committees. Every Member Body interested in a subject for which a Technical Committee has been set up has the right to be represented on that Committee. International organizations, governmental and non-governmental, in liaison with ISO, also take part in the work.

Draft International Standards adopted by the Technical Committees are circulated to the Member Bodies for approval before their acceptance as International Standards by the ISO Council.

International Standard ISO 2593 was drawn up by Technical Committee ISO/TC 97, *Computers and information processing.*

It was approved in July 1972 by the Member Bodies of the following countries :

Australia	Italy	Spain
Belgium	Japan	Sweden
Canada	Netherlands	Switzerland
Czechoslovakia	New Zealand	Thailand
Denmark	Poland	United Kingdom
Egypt, Arab Rep. of	Portugal	U.S.A.
France	Romania	
Germany	South Africa, Rep. of	

The Member Body of the following country expressed disapproval of the document on technical grounds :

U.S.S.R.

Connector pin allocations for use with high-speed data terminal equipment

0 INTRODUCTION

This International Standard was prepared as a complement to the CCITT Recommendation V 35 and as a guide to other data communication equipment, and to data terminal equipment which operates at a data signalling rate greater than approximately 20 000 bits per second.

1 SCOPE AND FIELD OF APPLICATION

This International Standard provides a correspondence between the interface circuit numbers used in CCITT Recommendation V 35, and the pin numbers of the connector used on the data communication equipment and the data terminal equipment.

NOTE — This International Standard defines the connector by a military standard as no international standard exists at this time.

2 REFERENCES

CCITT Recommendation V 35, *Modem for 48 kilobits per second.*

MIL Specification MIL-C-28748, *Connector, electrical rectangular, crimp type, removable contact, for rack and panel and other application.*

NOTE — Available from Navy Publications and Form Center, 5801 Tabor Ave, PHILA, PA 19120, U.S.A.

3 CONNECTOR PIN ALLOCATIONS

The data terminal equipment shall be terminated on a 34 pin connector conforming to MIL Specification No. MIL-C-28748.

The pin allocation shall be as follows :

Pin	Function	CCITT circuit No.	Direction
A	Protective ground or earth	101	common
B	Common return	102	common
C	Request to send	105	from DTE
D	Ready for sending	106	to DTE
E	Data set ready	107	to DTE
F	Data channel received line signal detector	109	to DTE
H	Connect data set to line	108/1	from DTE
	Data terminal ready	108/2	from DTE
J	Calling indicator	125	to DTE
K	F_1	—	—
L	F_2	—	—
M	F_1	—	—
N	F_2	—	—
R	Received data A-wire	104	to DTE
T	Received data B-wire	104	to DTE
V	Receiver signal element timing A-wire	115	to DTE
X	Receiver signal element timing B-wire	115	to DTE
Y	Transmitter signal element timing A-wire	114	to DTE
AA	Transmitter signal element timing B-wire	114	to DTE
P	Transmitted data A-wire	103	from DTE
S	Transmitted data B-wire	103	from DTE
U	Transmitter signal element timing A-wire	113	from DTE
Z	F_3	—	—
W	Transmitter signal element timing B-wire	113	from DTE
BB	F_3	—	—
CC	F_4	—	—
DD	F_5	—	—
EE	F_4	—	—
FF	F_5	—	—
HH	N_1	—	—
JJ	N_2	—	—
KK	N_1	—	—
LL	N_2	—	—
MM	F	—	—
NN	F	—	—

N = Pin number permanently reserved for national use.

F = Pin number reserved for future International Standard and should not be used for national use.

Subscripts indicate pins which may be associated to form pairs; for example, F_1 on pins K and M form a pair.

NOTES

1   Pins HH, JJ and KK are used in the United Kingdom for transmiter clock control, alternate use transmit and alternate use receive respectively.

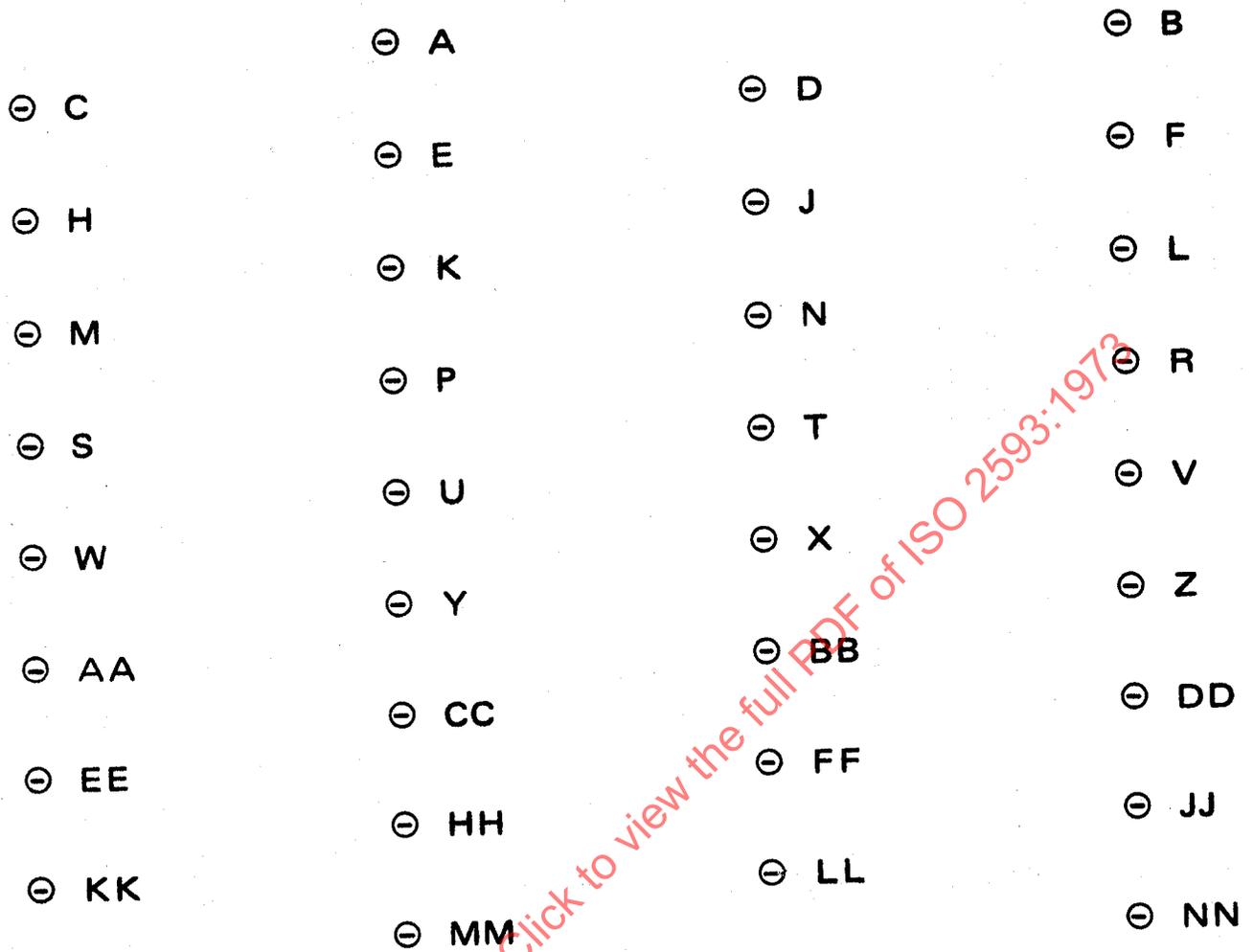2   The Figure opposite illustrates the physical layout of the connector pins.

⊖ A

⊖ B

⊖ C

⊖ D

⊖ E

⊖ F

⊖ H

⊖ J

⊖ K

⊖ L

⊖ M

⊖ N

⊖ P

⊖ R

⊖ S

⊖ T

⊖ U

⊖ W

⊖ X

⊖ V

⊖ Y

⊖ Z

⊖ AA

⊖ BB

⊖ DD

⊖ EE

⊖ CC

⊖ KK

⊖ FF

⊖ JJ

⊖ HH

⊖ LL

⊖ MM

⊖ NN

FIGURE – 34 Pin connector layout

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E R R A T U M

Page 2

In the table of pin allocations, correct the function of pin B to read "Signal ground or common return".