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**Road vehicles — Media Oriented  
Systems Transport (MOST) —**

**Part 3:  
Application layer conformance test  
plan**

*Véhicules routiers — Système de transport axé sur les médias —  
Partie 3: Plan d'essais de conformité de la couche d'application*

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## Foreword

ISO (the International Organization for Standardization) is a worldwide federation of national standards bodies (ISO member bodies). The work of preparing International Standards is normally carried out through ISO technical committees. Each member body interested in a subject for which a technical committee has been established has the right to be represented on that committee. International organizations, governmental and non-governmental, in liaison with ISO, also take part in the work. ISO collaborates closely with the International Electrotechnical Commission (IEC) on all matters of electrotechnical standardization.

The procedures used to develop this document and those intended for its further maintenance are described in the ISO/IEC Directives, Part 1. In particular, the different approval criteria needed for the different types of ISO documents should be noted. This document was drafted in accordance with the editorial rules of the ISO/IEC Directives, Part 2 (see [www.iso.org/directives](http://www.iso.org/directives)).

Attention is drawn to the possibility that some of the elements of this document may be the subject of patent rights. ISO shall not be held responsible for identifying any or all such patent rights. Details of any patent rights identified during the development of the document will be in the Introduction and/or on the ISO list of patent declarations received (see [www.iso.org/patents](http://www.iso.org/patents)).

Any trade name used in this document is information given for the convenience of users and does not constitute an endorsement.

For an explanation of the voluntary nature of standards, the meaning of ISO specific terms and expressions related to conformity assessment, as well as information about ISO's adherence to the World Trade Organization (WTO) principles in the Technical Barriers to Trade (TBT), see [www.iso.org/iso/foreword.html](http://www.iso.org/iso/foreword.html).

This document was prepared by Technical Committee ISO/TC 22, *Road vehicles*, Subcommittee SC 31, *Data communication*.

A list of all parts in the ISO 21806 series can be found on the ISO website.

Any feedback or questions on this document should be directed to the user's national standards body. A complete listing of these bodies can be found at [www.iso.org/members.html](http://www.iso.org/members.html).

## Introduction

The Media Oriented Systems Transport (MOST) communication technology was initially developed at the end of the 1990s in order to support complex audio applications in cars. The MOST Cooperation was founded in 1998 with the goal to develop and enable the technology for the automotive industry. Today, MOST<sup>1)</sup> enables the transport of high quality of service (QoS) audio and video together with packet data and real-time control to support modern automotive multimedia and similar applications. MOST is a function-oriented communication technology to network a variety of multimedia devices comprising one or more MOST nodes.

Figure 1 shows a MOST network example.

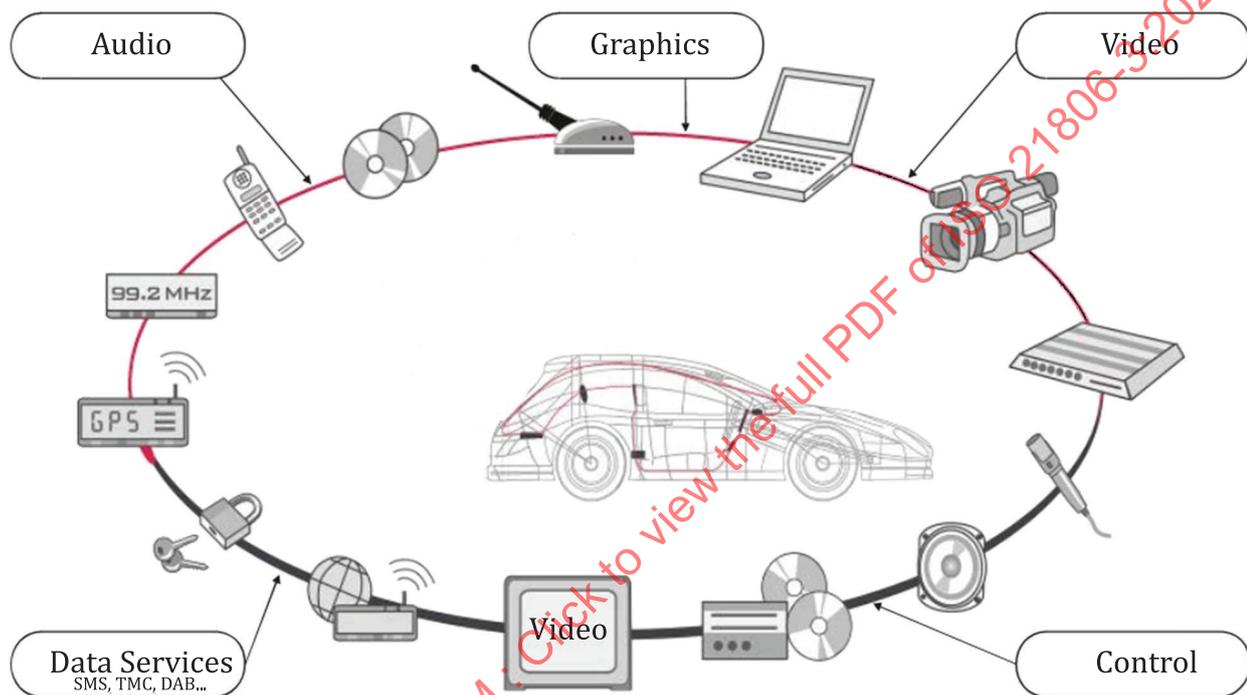


Figure 1 — MOST network example

The MOST communication technology provides:

- synchronous and isochronous streaming,
- small overhead for administrative communication control,
- a functional and hierarchical system model,
- API standardization through a function block (FBlock) framework,
- free partitioning of functionality to real devices,
- service discovery and notification, and
- flexibly scalable automotive-ready Ethernet communication according to ISO/IEC/IEEE 8802-3<sup>[2]</sup>.

MOST is a synchronous time-division-multiplexing (TDM) network that transports different data types on separate channels at low latency. MOST supports different bit rates and physical layers. The network clock is provided with a continuous data signal.

1) MOST<sup>®</sup> is the registered trademark of Microchip Technology Inc. This information is given for the convenience of users of this document and does not constitute an endorsement by ISO.

Within the synchronous base data signal, the content of multiple streaming connections and control data is transported. For streaming data connections, bandwidth is reserved to avoid interruptions, collisions, or delays in the transport of the data stream.

MOST specifies mechanisms for sending anisochronous, packet-based data in addition to control data and streaming data. The transmission of packet-based data is separated from the transmission of control data and streaming data. None of them interfere with each other.

A MOST network consists of devices that are connected to one common control channel and packet channel.

In summary, MOST is a network that has mechanisms to transport the various signals and data streams that occur in multimedia and infotainment systems.

The ISO standards maintenance portal (<https://standards.iso.org/iso/>) provides references to MOST specifications implemented in today's road vehicles because easy access via hyperlinks to these specifications is necessary. It references documents that are normative or informative for the MOST versions 4V0, 3V1, 3V0, and 2V5.

The ISO 21806 series has been established in order to specify requirements and recommendations for implementing the MOST communication technology into multimedia devices and to provide conformance test plans for implementing related test tools and test procedures.

To achieve this, the ISO 21806 series is based on the open systems interconnection (OSI) basic reference model in accordance with ISO/IEC 7498-1<sup>[1]</sup> and ISO/IEC 10731<sup>[3]</sup>, which structures communication systems into seven layers as shown in [Figure 2](#). Stream transmission applications use a direct stream data interface (transparent) to the data link layer.

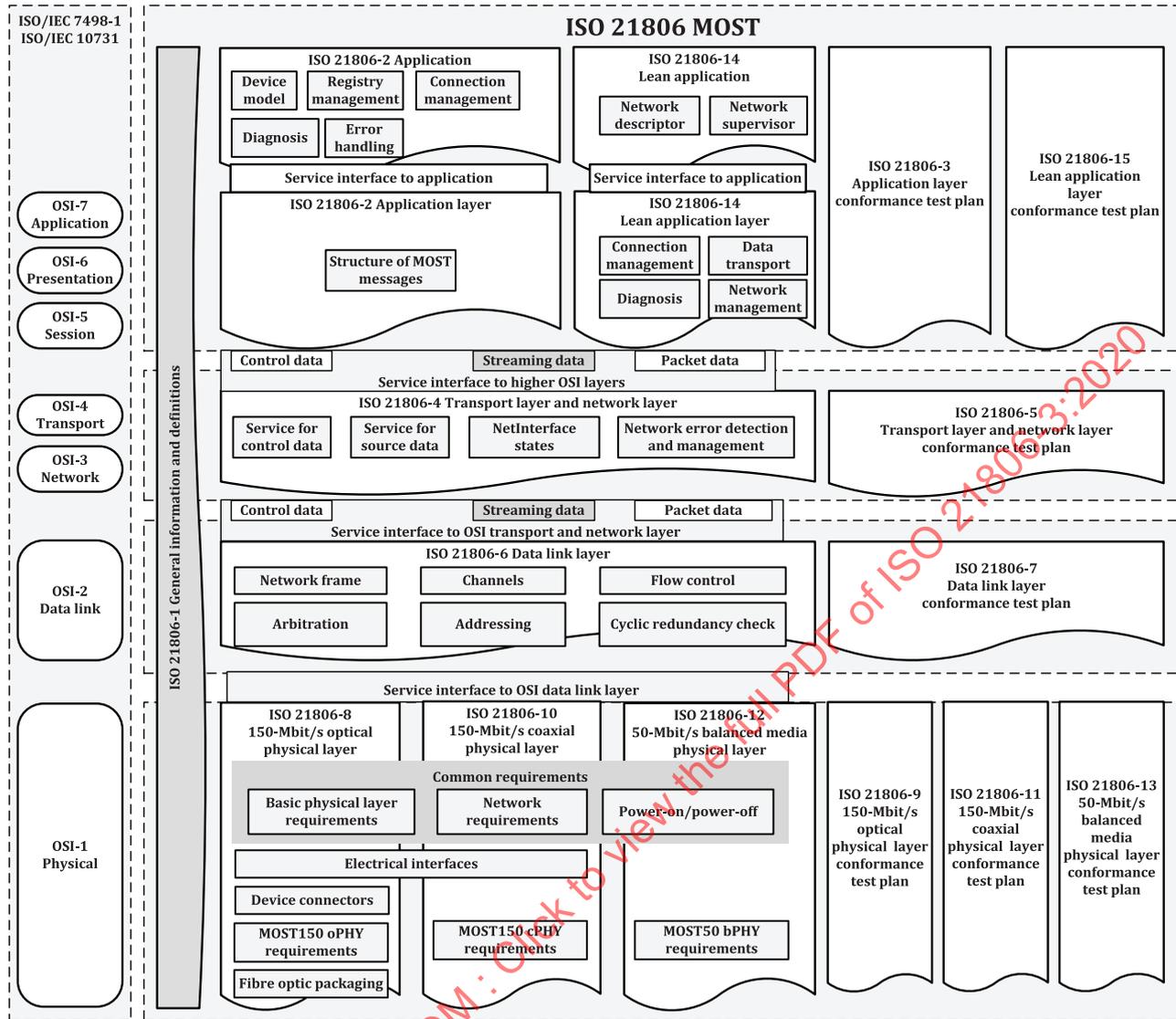


Figure 2 — The ISO 21806 series reference according to the OSI model

The International Organization for Standardization (ISO) draws attention to the fact that it is claimed that compliance with this document may involve the use of a patent.

ISO takes no position concerning the evidence, validity and scope of this patent right.

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# Road vehicles — Media Oriented Systems Transport (MOST) —

## Part 3: Application layer conformance test plan

### 1 Scope

This document specifies the conformance test plan (CTP) for the application layer for MOST, a synchronous time-division-multiplexing network, as specified in ISO 21806-2.

This document specifies conformance test cases (CTCs) in the following categories:

- device model;
- data and basic data types;
- registry management;
- connection management;
- error management;
- diagnosis.

Interoperability testing is not in the scope of this document.

### 2 Normative references

The following documents are referred to in the text in such a way that some or all of their content constitutes requirements of this document. For dated references, only the edition cited applies. For undated references, the latest edition of the referenced document (including any amendments) applies.

ISO/IEC 9646-1:1994, *Information technology — Open Systems Interconnection — Conformance testing methodology and framework — Part 1: General concepts*

ISO 21806-1:2020, *Road vehicles — Media Oriented Systems Transport (MOST) — Part 1: General information and definitions*

ISO 21806-2:2020, *Road vehicles — Media Oriented Systems Transport (MOST) — Part 2: Application layer*

ISO 21806-4:2020, *Road vehicles — Media Oriented Systems Transport (MOST) — Part 4: Transport layer and network layer*

### 3 Terms and definitions

For the purposes of this document, the terms and definitions given in ISO 21806-1, ISO 21806-2, ISO 21806-4, ISO/IEC 9646-1, and the following apply.

ISO and IEC maintain terminological databases for use in standardisation at the following addresses:

- ISO Online browsing platform: available at <https://www.iso.org/obp>
- IEC Electropedia: available at <http://www.electropedia.org/>

**3.1  
REPEAT**

pseudo code command for an iteration

**3.2  
REPEAT END**

pseudo code command for ending an iteration

## 4 Symbols and abbreviated terms

### 4.1 Symbols

--- empty cell/undefined

### 4.2 Abbreviated terms

|     |                              |
|-----|------------------------------|
| CTC | conformance test case        |
| CTP | conformance test plan        |
| CR  | central registry             |
| DR  | decentral registry           |
| IUT | implementation under test    |
| LT  | lower tester                 |
| MPI | maximum position information |
| MSC | Message Sequence Chart       |
| NCE | network change event         |
| OSI | Open Systems Interconnection |
| UT  | upper tester                 |

## 5 Conventions

This document is based on OSI service conventions as specified in ISO/IEC 10731<sup>[3]</sup> and ISO/IEC 9646-1 for conformance test system set-up.

## 6 CTP overview

### 6.1 Test set-up

All CTCs are based on the same test set-up with an upper tester (UT) and a lower tester (LT). The LT contains the lower tester pre-IUT (LT pre-IUT) and the lower tester post-IUT (LT post-IUT).

[Figure 3](#) specifies the test set-up.

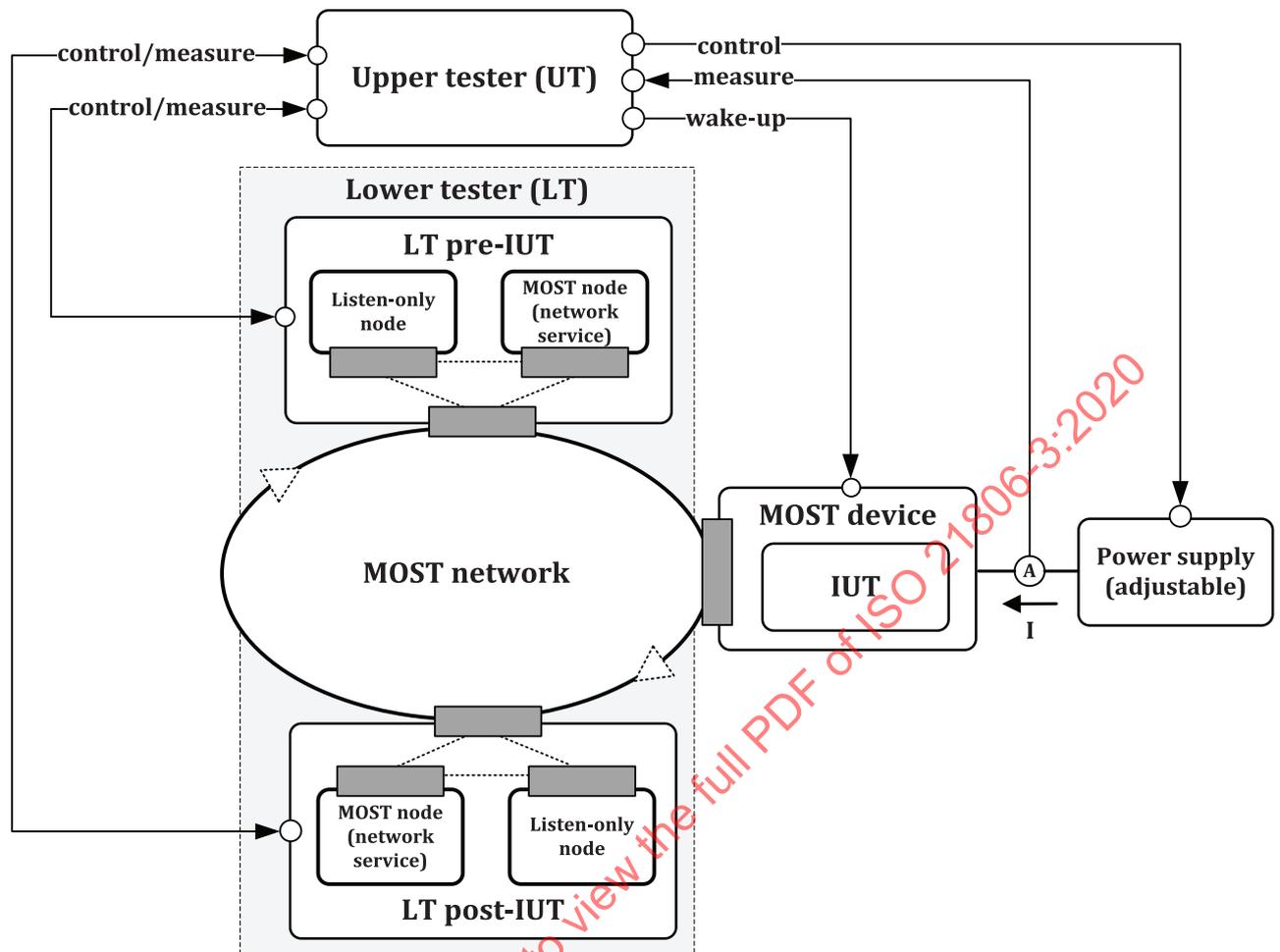


Figure 3 — Test set-up

The LT pre-IUT and the LT post-IUT implement the application layer services and the lower layer services of a MOST node in accordance with the ISO 21806 series. They also contain a listen-only node in front of the MOST node to log the whole communication. The MOST node is able to operate as TimingMaster or TimingSlave; alternatively, it can be physically disconnected from the MOST network. If it is disconnected, the associated LT pre-IUT or LT post-IUT serves as listen-only node.

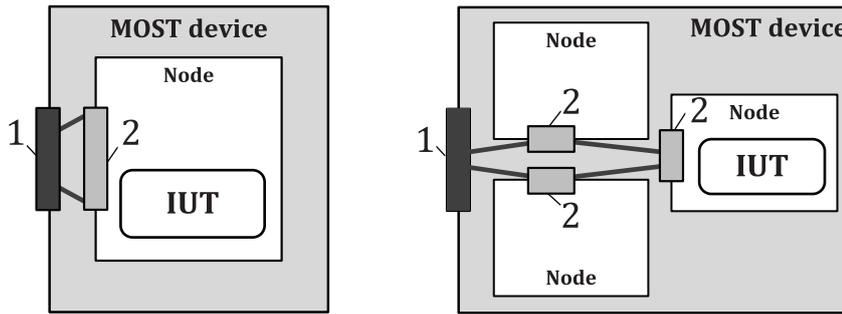
Every CTC specifies the roles of the LT pre-IUT and the LT post-IUT.

During testing of the MOST device that implements the IUT, avoid over-temperature by following the manufacturer recommendations regarding cooling.

The power supply of the MOST device that contains the IUT is adjustable and the power consumption can be monitored by the UT. This is necessary to determine whether a node has entered `s_NetInterface_Sleep`.

A MOST device contains one or more nodes, which are connected to an external MOST physical interface. One of the nodes contains the implementation under test (IUT). All tests and timings, specified by the CTP, are related to the external MOST physical interface.

Figure 4 shows a MOST device with one node and a MOST device with three internal nodes.



- 1 external MOST physical interface
- 2 internal MOST physical interface

Figure 4 — MOST device with one node and MOST device with three nodes

## 6.2 Conformance test plan organisation

CTCs are independent of one another. Each CTC checks the behaviour of the IUT for requirements stated in ISO 21806-2. Within CTCs, which require variations of individual parameters, each specified value of the parameter is iterated.

The measurement uncertainty for each CTC shall be in accordance with [Annex A](#).

## 7 CTP general information

### 7.1 CTC remarks

#### 7.1.1 Timer naming

For conformance testing of the IUT, the UT and LT need minimum and maximum timers. The names of the timers used by this document are based on ISO 21806-2 and ISO 21806-4. To obtain the timer name, for minimum and maximum, “\_min” and “\_max” are appended, respectively. [Table 1](#) shows a timer naming definition example for  $t_{Config}$ .

Table 1 — Timer naming example

| Name         | Minimum value name | Typical value name | Maximum value name | Unit | Purpose  |
|--------------|--------------------|--------------------|--------------------|------|--|
| $t_{Config}$ | $t_{Config\_min}$  | $t_{Config}$       | $t_{Config\_max}$  | ms   | Time before <code>ev_Init_Error_Shutdown</code> or delay for RBD result. |

#### 7.1.2 Deadlock prevention

This document specifies the timeouts  $t_{DeadLockShort}$ ,  $t_{DeadLockMid}$ , and  $t_{DeadLockLong}$  to prevent deadlock situations during conformance testing. These are the default values:

- $t_{DeadLockShort}$ : 1 s;
- $t_{DeadLockMid}$ : 20 s;
- $t_{DeadLockLong}$ : 5 min.

These timeouts are only relevant for conformance testing and may be extended.

### 7.1.3 Un-initialised logical node address

The variable `uninitialised_node_address` is defined as the address of an un-initialised node, which is specified in ISO 21806-2.

### 7.1.4 Addresses of MOST nodes in the LT

The address of a MOST node in the LT is the default logical node address corresponding to the node position.

If this address is in conflict with the address of a node that contains the IUT (e.g. if a supplier uses static addresses in the dynamic address range), the affected MOST node in the LT shall use a valid free address.

### 7.1.5 Device manufacturer information list

This list contains all information that is provided by the device manufacturer for conformance testing. It also includes remarks and references to corresponding CTCs.

[Table 2](#) shows the device manufacturer information list, which does not include information stored in FBlock EnhancedTestability.

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Table 2 — Device manufacturer information list

| Category                    | Item/property  | Description   | Reference to CTC  |
|-----------------------------|--|---|---|
| MOST network configuration  | IUT in the TimingMaster  | Determines whether the IUT is part of the TimingMaster.   | All CTCs  |
|                             | IUT in the NetworkMaster   | Determines whether the IUT is part of the NetworkMaster.  | All CTCs  |
|                             | IUT in the PowerMaster   | Determines whether the IUT is part of the PowerMaster.  | All CTCs  |
|                             | IUT in the connection manager  | ---   | CTC_3.1-3, CTC_3.1-4, CTC_3.1-5, CTC_3.1-6, CTC_3.2-3, CTC_3.2-4, CTC_3.2-5, CTC_3.2-6, CTC_3.2-7, CTC_3.2-8, CTC_3.2-9, CTC_3.2-14 |
|                             | Multi-node device  | If the IUT is part of a MOST device that contains more than one node, the following information is provided: <ul style="list-style-type: none"> <li>— number of nodes in the MOST device;</li> <li>— topology of the MOST device (position of PowerMaster and TimingMaster/ NetworkMaster);</li> <li>— position of the node that contains the IUT.</li> </ul> | All CTCs  |
|                             | IUT sample frequency   | If the IUT is not part of the TimingMaster, the LT provides the correct network frame rate (44,1 kHz or 48,0 kHz).  | All CTCs  |
|                             | Required value of boundary descriptor (if the TimingMaster is in the LT) | Value of the boundary descriptor. Unless otherwise stated, all CTCs are performed with this value of the boundary descriptor.   | All CTCs  |
|                             | $m_{i\_MaxInvalidReg}$   | The maximum number of permitted conflicting node address registrations by a NetworkSlave.   | CTC_2.6.2-3a  |
|                             | $m_{i\_MaxSetNewInstID}$   | When an invalid InstID registration occurs, the NetworkMaster sends a request to the NetworkSlave for setting a new InstID.   | CTC_2.6.2-6   |
|                             | $t_{Config\_max}$  | Time before ev_Init_Error_Shutdown or delay for RBD result.   | CTC_2.1.1-6b  |
| $t_{ConfigurationAnnounce}$ | Limit for the NetworkMaster to set the central registry state.           | CTC_2.6.2-4a, CTC_2.6.2-5   |   |
| $t_{WaitForAnswer\_min}$    | Time the NetworkMaster waits for all NetworkSlaves to respond.           | CTC_2.6.2-1, CTC_2.6.2-3b, CTC_2.6.2-5  |   |
| $t_{WaitForAnswer\_max}$    |  |   |   |

Table 2 (continued)

| Category         | Item/property  | Description  | Reference to CTC                            |
|------------------|--|--|---|
| Power management | Node that contains the IUT supports <code>s_NetInterface_Sleep</code>  | Determines whether the node that contains the IUT supports <code>s_NetInterface_Sleep</code> :<br><br>— yes: the MOST device that contains the IUT reduces its power consumption below threshold before timeout expires;<br><br>— no: the reduction of power consumption is not detectable.  | CTC_2.3.2-3                                 |
|                  | <code>s_NetInterface_Sleep</code> :<br>$I_{NetInterfaceSleep\_Threshold}$  | Threshold of current for <code>s_NetInterface_Sleep</code> detection   | See 7.1.6.                                  |
|                  | <code>s_NetInterface_Sleep</code> :<br>$t_{PwrSwitchOffDelay\_min}$<br>$t_{PwrSwitchOffDelay\_max}$  | $t_{PwrSwitchOffDelay\_min}$<br>Specific timeout for <code>s_NetInterface_Sleep</code> ; after the end of network activity, the node that contains the IUT does not enter <code>s_NetInterface_Sleep</code> (reduced power consumption) before $t_{PwrSwitchOffDelay\_min}$ expires.<br><br>$t_{PwrSwitchOffDelay\_max}$<br>MOST device specific timeout for <code>s_NetInterface_Sleep</code> ; after the end of network activity, the node that contains the IUT enters <code>s_NetInterface_Sleep</code> (reduced power consumption) before $t_{PwrSwitchOffDelay\_max}$ expires. | CTC_2.3.2-3,<br>CTC_2.6.4-1                 |
|                  | Wake-up preconditions  | Preconditions for the node that contains the IUT for wake-up.<br><br>Supplemented by information whether the node that contains the IUT needs additional conditions during operation (e.g. ignition ON) to stay in <code>s_NetInterface_Normal_Operation</code> .  | See 7.1.6.                                  |
|                  | Node that contains the IUT is capable of waking via network startup (i.e. switching on its MOST output)  | ---  | CTC_2.4.1-2                                 |
|                  | Delay between connection to power (of the MOST device that contains the IUT) and the ability of the node that contains the IUT to detect wake-up events  | Potentially, the UT (see Figure 3) waits for a short period of time between connecting the MOST device that contains the IUT to power and switching on the MOST output to wake up the node that contains the IUT. Otherwise, the node that contains the IUT does not detect a wake-up event.   | All CTCs                                    |
|                  | <code>s_NetInterface_Normal_Operation</code> :<br>Delay until all FBlocks of the node that contains the IUT are available after Configuration. Status (OK) (equivalent to $t_{WaitForApplication}$ ) | This delay covers:<br><br>— NetworkMaster: period of time the node that contains the IUT needs to add own FBlocks to its central registry after Configuration. Status (OK);<br><br>— NetworkSlave: delay between <code>ev_Init_Ready</code> and availability of application.   | See 7.1.6.<br>CTC_2.6.2-3b                  |
|                  | $t_{WaitBeforeScan}$   | Specific limit for the NetworkMaster to start an FBlock scan.  | CTC_2.4.1-9,<br>CTC_2.6.2-5,<br>CTC_2.6.4-8 |

Table 2 (continued)

| Category                                   | Item/property  | Description   | Reference to CTC  |
|--|--|---|---|
| <b>Addressing</b>                          | Node that contains the IUT uses static node address in dynamic address range   | If the node that contains the IUT uses a static logical node address that is in the specified dynamic address range, the address is provided.                 | See 7.1.6.<br>CTC_2.6.4-4   |
|  | Free address   | Logical node address that may be used by a MOST node in the LT during testing.  | CTC_2.6.4-3   |
|  | Free FBlock range  | FBlocks that are not used by the node that contains the IUT and which may be used by the UT.  | CTC_2.6.2-4b,<br>CTC_2.6.2-4c   |
|  | Group address of the node that contains the IUT  | ---   | CTC_2.7-1   |
| <b>General communication</b>               | $t_{Property}$   | Limit for responding to a command that reads a property.  | CTC_2.1.0-1,<br>CTC_2.1.0-2,<br>CTC_2.8.3-2,<br>CTC_2.8.4-3,<br>CTC_3.0-1,<br>CTC_3.1-1,<br>CTC_3.2-1 |
|  | $t_{NotificationProperty}$   | Limit for reacting to a Notification_Set message.   | CTC_2.8.3-1a,<br>CTC_2.8.3-1b,<br>CTC_2.8.3-7   |
| <b>Physical parameter (voltage levels)</b> | $U_{IUT\_Operating}$   | At this voltage level, the MOST device that contains the IUT operates normally.<br><br>Unless otherwise stated, all CTCs are performed at this voltage level. | All CTCs  |
| <b>Messaging</b>                           | Node that contains the IUT supports segmented messages   | The node that contains the IUT is able to send and receive segmented messages.  | CTC_2.8.4-2,<br>CTC_2.8.4-3,<br>CTC_2.8.4-7,<br>CTC_2.8.4-8   |
| <b>Sink/Source</b>                         | List of FBlocks, containing sink and/or source functionality   | The list contains all FBlocks reported by NetBlock.FBlockIDs.Status.  | CTC_3.0-1   |
|  | MOST devices with sinks:<br><br>List of all supported sink numbers with ContentType, ContentDescription (data type of the parameter) and TransmissionClass     | ---   | CTC_3.1-1,<br>CTC_3.1-3,<br>CTC_3.1-4,<br>CTC_3.1-5,<br>CTC_3.1-6                                     |
|  | MOST devices with sources:<br><br>List of all supported source numbers with ContentType, ContentDescription (data type of the parameter) and TransmissionClass | ---   | CTC_3.2-1,<br>CTC_3.2-3,<br>CTC_3.2-4,<br>CTC_3.2-5,<br>CTC_3.2-6,<br>CTC_3.2-7,<br>CTC_3.2-14        |
|  | MOST devices with sources:<br><br>BlockWidth and ConnectionLabel   | ---   | CTC_3.2-3   |
|  | Node that contains the IUT supports SourceActivity   | ---   | CTC_3.2-14  |
|  |  |   |   |

7.1.6 States of the node that contains the IUT

Table 3 specifies how the NetInterface state s\_NetInterface\_Normal\_Operation is effectuated and detected in the node that contains the IUT.

**Table 3 — Effectuate and detect  $s_{\text{NetInterface\_Normal\_Operation}}$**

| Effectuate state   | Detect state   |
|--|--|
| <p>a) The IUT is contained in a NetworkSlave:</p> <ul style="list-style-type: none"> <li>— the UT shall start the network;</li> <li>— wait for the node that contains the IUT to open its bypass (MPI is nominal<sup>a</sup>);</li> <li>— send <code>NetworkMaster.Configuration.Status(NotOK)</code>;</li> <li>— perform an FBlock scan (including retries if the address of the node that contains the IUT is invalid);</li> <li>— send <code>NetworkMaster.Configuration.Status(OK)</code>;</li> <li>— wait for <math>t_{\text{WaitForApplication}}</math>.</li> </ul>                                    | <p>a) The IUT is contained in a NetworkSlave: the node that contains the IUT responds to <code>NetBlock.FBlockIDs.Get</code>.</p>    |
| <p>b) The IUT is contained in the NetworkMaster:</p> <ul style="list-style-type: none"> <li>— the UT shall behave like a NetworkSlave; it shall process and respond to all requests from the node that contains the IUT so that the node can enter central registry state OK;</li> <li>— the UT shall respond to an FBlock scan by the node that contains the IUT; additionally, the UT shall wait for the node to open its bypass (MPI is nominal<sup>a</sup>) if the node is part of a multi-node device;</li> <li>— finally, the UT shall wait for <math>t_{\text{WaitForApplication}}</math>.</li> </ul> | <p>b) The IUT is contained in the NetworkMaster: the node that contains the IUT responds to <code>NetBlock.FBlockIDs.Get</code>.</p> |
| <p><sup>a</sup> The nominal MPI is the total number of nodes in the set-up, based on the IUT manufacturer information and the test equipment.</p>  |  |

Table 4 specifies how the NetInterface state  $s_{\text{NetInterface\_Sleep}}$  is effectuated and detected in the node that contains the IUT.

**Table 4 — Effectuate and detect  $s_{\text{NetInterface\_Sleep}}$  state**

| Effectuate state              | Detect state  |
|-------------------------------|---|
| <p>Switch off MOST output</p> | <p>a) When <math>s_{\text{NetInterface\_Sleep}}</math> is detectable the following applies:</p> <ul style="list-style-type: none"> <li>— when monitoring power consumption, the current reaches or drops below <math>I_{\text{NetInterfaceSleep\_Threshold}}</math>;</li> <li>— if the LT detects network activity when <math>t_{\text{PwrSwitchOffDelay\_max}}</math> expires, the test shall be stopped; the timer shall be started as soon as the node that contains the IUT switches off the MOST output;</li> <li>— if the current does not reach or drop below <math>I_{\text{NetInterfaceSleep\_Threshold}}</math> within timeout <math>t_{\text{PwrSwitchOffDelay\_max}}</math>, the test shall be stopped.</li> </ul> <p>b) When <math>s_{\text{NetInterface\_Sleep}}</math> is not detectable the following applies:</p> <ul style="list-style-type: none"> <li>— when the timeout <math>t_{\text{PwrSwitchOffDelay\_max}}</math> expires and if the LT does not detect network activity, it shall be assumed that the node that contains the IUT is in <math>s_{\text{NetInterface\_Sleep}}</math>, independent of power consumption;</li> <li>— if the LT detects network activity when <math>t_{\text{PwrSwitchOffDelay\_max}}</math> expires, the test shall be stopped.</li> </ul> |

Table 5 specifies how the NetInterface state  $s_{\text{NetInterface\_Off}}$  is effectuated and detected in the node that contains the IUT.

**Table 5 — Effectuate and detect s\_NetInterface\_Off state**

| Effectuate state                         | Detect state        |
|--|---------------------|
| The LT shall switch off the MOST output. | No network activity |

**7.1.7 Procedures**

Table 6 specifies the procedures of the LT.

**Table 6 — Procedures of the LT**

| Purpose          | Description  |
|------------------|--|
| Perform wake-up  | <p>a) If the LT contains the TimingMaster, it shall execute this sequence to perform a wake-up:</p> <ol style="list-style-type: none"> <li>1. switch on the MOST output;</li> <li>2. wait for network activity (timeout <math>t_{DeadLockShort}</math>);</li> <li>3. wait for stable lock (timeout <math>t_{DeadLockMid}</math>).</li> </ol> <p>b) If the LT does not contain the TimingMaster, it shall execute this sequence to perform a wake-up:</p> <ol style="list-style-type: none"> <li>1. generate a wake-up event;</li> <li>2. wait for network activity;</li> <li>3. switch on the MOST output;</li> <li>4. wait for stable lock;</li> <li>5. wait for the lock flag to evaluate to true.</li> </ol> <p>If the LT does not generate a wake-up event and detects network activity, it shall switch on the MOST output.</p> <p>In some cases, the node that contains the IUT needs some preconditions for wake-up. These preconditions are established before testing is started. The preconditions depend on the device manufacturer.</p> <p>If EnhancedTestability.AutoWakeup is triggered, the node that contains the IUT does not enter s_NetInterface_Sleep before creating the corresponding wake-up event. The node enters s_NetInterface_Off. This state is not detectable by monitoring the power consumption of the MOST device that contains the IUT. With entering state s_NetInterface_Off, the node that contains the IUT switches off the MOST output.</p> |
| Perform shutdown | <p>a) If the IUT is part of the PowerMaster, the LT shall execute this sequence to perform shutdown:</p> <ul style="list-style-type: none"> <li>— trigger shutdown by means of FBlock EnhancedTestability;</li> <li>— if no network activity is detected, the node that contains the IUT has performed shutdown;</li> <li>— if <math>t_{DeadLockMid}</math> expires after triggering shutdown and the LT still detects network activity, it shall switch off the MOST output.</li> </ul> <p>b) If the IUT is part of a PowerSlave, the LT shall switch off the MOST output to perform shutdown.</p> <p>c) If the IUT is part of the PowerMaster, no preconditions are established that prevent the node that contains the IUT from performing shutdown.</p>  |
| Generate unlock  | To generate an unlock event of predictable duration, the LT  |

Table 6 (continued)

| Purpose                             | Description   |
|-------------------------------------|---|
|                                     | <ul style="list-style-type: none"> <li>— shall invalidate or delay the preamble at the beginning of at least every third network frame during the period of unlock (see ISO 21806-6), and</li> <li>— shall avoid a PLL unlock.</li> </ul> |
| Network change event with unlock    | The NCE shall be generated between the TimingMaster and the node that contains the IUT.   |
| Network change event without unlock | The NCE shall be generated between the node that contains the IUT and the TimingMaster.   |

### 7.1.8 Violation of prerequisites of the CTC

If the node that contains the IUT does not meet the prerequisites of the CTC (such as network activity, lock, FBlock scan performed, central registry state OK), the CTC results in "IUT not ok: The IUT does not meet the prerequisites".

## 7.2 CTC items

### 7.2.1 FBlock EnhancedTestability

The FBlock EnhancedTestability is used to trigger sequences that are specified in the CTC. These are normally triggered by a project specific, sometimes complicated, mechanism. FBlock EnhancedTestability implements neither notification nor processing messages. The UT shall initialise FBlock EnhancedTestability every time the `s_NetInterface_Normal_Operation` state is reached. The FBlock is only available during `s_NetInterface_Normal_Operation`. All properties are reset to their default state when entering `s_NetInterface_Normal_Operation`, unless otherwise stated. The functions in this FBlock describe a general interface for starting functionality partly implemented in the application, partly in the MOST network service. If an application callback returns wrong or unexpected values, the FBlock sends an error message with `ErrorCode 0B16` (device malfunction).

If the FBlock EnhancedTestability returns errors, the corresponding CTC result is indicated as "IUT not ok".

### 7.2.2 Multi-node devices

A multi-node device is a MOST device with an external MOST physical interface, which is connected to more than one internal node.

When dealing with devices with several external MOST physical interfaces, each interface shall be treated as a separate IUT. One of those interfaces may belong to a multi-node device. In a multi-node device, each node shall be treated individually.

NOTE A MOST device passes conformance testing successfully if all contained IUTs pass all relevant CTCs.

### 7.2.3 Node kinds excluded from conformance testing

CTCs for remote controlled nodes according to ISO 21806-2 are not in the scope of this document.

## 8 CTC specification

### 8.1 Static FBlock behaviour

#### 8.1.1 CTC\_2.1.0-1 – Generic FBlock property test

[Table 7](#) specifies the CTC\_2.1.0-1 - Generic FBlock property test.

**Table 7 — CTC\_2.1.0-1 - Generic FBlock property test**

| Item                     | Content  |
|--------------------------|--|
| <b>CTC # - Title</b>     | CTC_2.1.0-1 - Generic FBlock property test   |
| <b>Purpose</b>           | <p>This CTC verifies that FBlock communication with properties of the NetBlock, the NetworkMaster, sinks, and sources is possible.</p> <p>This CTC applies to all MOST devices.</p>  |
| <b>Reference</b>         | ISO 21806-2:2020, 7.6 AL – Operation type (OPType)   |
| <b>Prerequisite</b>      | The UT shall effectuate <code>s_NetInterface_Normal_Operation</code> in the node that contains the IUT.  |
| <b>Set-up</b>            | <ul style="list-style-type: none"> <li>— If the IUT is part of a MOST device that contains the TimingMaster, the LT pre-IUT shall be a TimingSlave.</li> <li>— If the IUT is part of a MOST device that does not contain the TimingMaster, the LT pre-IUT shall be the TimingMaster.</li> <li>— The LT post-IUT shall be a listen-only node.</li> </ul>  |
| <b>Step</b>              | <ol style="list-style-type: none"> <li>1. The UT shall set the <code>FBlockID</code> and <code>InstID</code> to the values of the current test iteration.</li> <li>2. The UT, using the LT pre-IUT, shall send <code>FBlockID.InstID.FktIDs.Get</code> to the IUT to retrieve the list of implemented functions of the IUT.</li> <li>3. The UT, in an outer loop, shall loop through the <code>FktIDs</code> that were returned in the list and correspond to properties.</li> <li>4. The UT, in an inner loop, shall loop through the <code>OPTypes</code> (<code>O<sub>16</sub></code> to <code>F<sub>16</sub></code>).</li> <li>5. UT: for every iteration of the loop, the UT, using the LT pre-IUT, shall send <code>FBlockID.InstID.FktID.OPType</code> to the IUT.</li> <li>6. The UT shall evaluate the response of the IUT as specified in <a href="#">Table 8</a>.</li> <li>7. The UT shall shut down the IUT and re-establish the prerequisites for the next test iteration, after processing all inner and outer loop iterations.</li> </ol> |
| <b>Iteration</b>         | <p>REPEAT Step 1 to Step 7 with the NetBlock and every available <code>InstID</code> (obtained by <code>NetBlock.FBlockIDs</code>) of the following FBlocks of the</p> <ul style="list-style-type: none"> <li>— IUT: NetworkMaster</li> <li>— IUT: FBlocks containing sink functions (identified by CTC TEST_GSI_GSO_identification);</li> <li>— IUT: FBlocks containing source functions (identified by CTC TEST_GSI_GSO_identification).</li> </ul> <p>REPEAT END.</p>   |
| <b>Expected response</b> | <p>Step 6: IUT ok: the IUT provides permitted responses as specified in <a href="#">Table 8</a>.</p> <p>Step 6: IUT not ok: the IUT does not respond as expected to the requests to properties.</p>  |

Table 7 (continued)

| Item   | Content  |
|--------|--|
| Remark | <ol style="list-style-type: none"> <li>1. Responses are only evaluated from actually tested FBlockID, InstID and FktID.</li> <li>2. Any other message of the node that contains the IUT is ignored during the CTC.</li> <li>3. The FBlock library determines whether the function is a property or a method.</li> <li>4. Usually, a MOST device does not implement all functions and OPTypes of the FBlock library; if implemented, they are expected to behave according to the FBlock library.</li> <li>5. No range checks are performed.</li> <li>6. In the case that the OPType test allows “no answer” as permitted response, the observation time period is <math>2 \times t_{Property}</math> for properties respectively <math>t_{DeadLockMid}</math> for methods.</li> <li>7. For the NetBlock, error 03<sub>16</sub> (function not available) is also a permitted response.</li> <li>8. All commands (OPType 0<sub>16</sub> to 8<sub>16</sub>) from the node that contains the IUT are ignored for all FBlocks.</li> <li>9. For the NetBlock and the NetworkMaster FBlock, OPTypes 9<sub>16</sub> to F<sub>16</sub> are not tested.</li> </ol> |

Table 8 specifies the OPTypes for CTC\_2.1.0-1 - Generic FBlock property test.

Table 8 — OPTypes for CTC\_2.1.0-1 - Generic FBlock property test

| OPType          | OPType name and parameters                           | Permitted response  |
|-----------------|--|---|
| 0 <sub>16</sub> | Set<br>(without parameter)                           | <ul style="list-style-type: none"> <li>— Status;</li> <li>— No answer (within <math>2 \times t_{Property}</math>);</li> <li>— All errors except 01<sub>16</sub>, 02<sub>16</sub>, 03<sub>16</sub>, 0A<sub>16</sub>, 0C<sub>16</sub>.</li> </ul> |
| 1 <sub>16</sub> | Get<br>(without parameter)                           | <ul style="list-style-type: none"> <li>— Status;</li> <li>— All errors except 01<sub>16</sub>, 02<sub>16</sub>, 03<sub>16</sub>, 0A<sub>16</sub>, 0C<sub>16</sub>.</li> </ul>   |
| 2 <sub>16</sub> | SetGet<br>(without parameter)                        | <ul style="list-style-type: none"> <li>— Status;</li> <li>— All errors except 01<sub>16</sub>, 02<sub>16</sub>, 03<sub>16</sub>, 0A<sub>16</sub>, 0C<sub>16</sub>.</li> </ul>   |
| 3 <sub>16</sub> | Increment(1)   | <ul style="list-style-type: none"> <li>— Status;</li> <li>— All errors except 01<sub>16</sub>, 02<sub>16</sub>, 03<sub>16</sub>, 0A<sub>16</sub>, 0C<sub>16</sub>.</li> </ul>   |
| 4 <sub>16</sub> | Decrement(1)   | <ul style="list-style-type: none"> <li>— Status;</li> <li>— All errors except 01<sub>16</sub>, 02<sub>16</sub>, 03<sub>16</sub>, 0A<sub>16</sub>, 0C<sub>16</sub>.</li> </ul>   |
| 5 <sub>16</sub> | Not allowed  | Not allowed   |
| 6 <sub>16</sub> | StartResultAck<br>(SenderHandle 1234 <sub>16</sub> ) | <ul style="list-style-type: none"> <li>— ErrorAck(SenderHandle 1234<sub>16</sub>, ErrorCode 04<sub>16</sub>);</li> <li>— Error(ErrorCode 04<sub>16</sub>).</li> </ul>   |
| 7 <sub>16</sub> | AbortAck<br>(SenderHandle 1234 <sub>16</sub> )       | <ul style="list-style-type: none"> <li>— ErrorAck(SenderHandle 1234<sub>16</sub>, ErrorCode 04<sub>16</sub>);</li> <li>— Error(ErrorCode 04<sub>16</sub>).</li> </ul>   |
| 8 <sub>16</sub> | StartAck<br>(SenderHandle 1234 <sub>16</sub> )       | <ul style="list-style-type: none"> <li>— ErrorAck(SenderHandle 1234<sub>16</sub>, ErrorCode 04<sub>16</sub>);</li> <li>— Error(ErrorCode 04<sub>16</sub>).</li> </ul>   |
| 9 <sub>16</sub> | ErrorAck   | No answer   |
| A <sub>16</sub> | ProcessingAck  | No answer   |
| B <sub>16</sub> | Processing   | No answer   |
| C <sub>16</sub> | Status   | No answer   |

**Table 8** (continued)

| OPType          | OPType name and parameters | Permitted response |
|-----------------|----------------------------|--------------------|
| D <sub>16</sub> | ResultAck                  | No answer          |
| E <sub>16</sub> | Not allowed                | Not allowed        |
| F <sub>16</sub> | Error                      | No answer          |

**8.1.2 CTC\_2.1.0-2 – Generic FBlock method test**

Table 9 specifies the CTC\_2.1.0-2 - Generic FBlock method test.

**Table 9 — CTC\_2.1.0-2 - Generic FBlock method test**

| Item                     | Content  |
|--------------------------|--|
| <b>CTC # - Title</b>     | CTC_2.1.0-2 - Generic FBlock method test   |
| <b>Purpose</b>           | This CTC verifies that FBlock communication with methods of the NetBlock, the NetworkMaster, sinks, and sources is possible.<br>This CTC applies to all MOST devices.  |
| <b>Reference</b>         | ISO 21806-2:2020, 7.6 AL – Operation type (OPType)   |
| <b>Prerequisite</b>      | The UT shall effectuate <code>s_NetInterface_Normal_Operation</code> in the node that contains the IUT.  |
| <b>Set-up</b>            | <ul style="list-style-type: none"> <li>— If the IUT is part of a MOST device that contains the TimingMaster, the LT pre-IUT shall be a TimingSlave.</li> <li>— If the IUT is part of a MOST device that does not contain the TimingMaster, the LT pre-IUT shall be the TimingMaster.</li> <li>— The LT post-IUT shall be a listen-only node.</li> </ul>  |
| <b>Step</b>              | <ol style="list-style-type: none"> <li>1. The UT shall set the <code>FBlockID</code> and <code>InstID</code> to the values of the current test iteration.</li> <li>2. The UT shall use the LT pre-IUT to send <code>FBlockID.InstID.FktIDs.Get</code> to the IUT to retrieve the list of implemented functions of the IUT.</li> <li>3. The UT in an outer loop, the UT shall loop through the <code>FktIDs</code> that correspond to methods, which are reported in the list.</li> <li>4. The UT in an inner loop, the UT shall loop through the <code>OPTypes</code> (<code>O<sub>16</sub></code> to <code>F<sub>16</sub></code>).</li> <li>5. The UT for every iteration of the loop, the UT, using the LT pre-IUT, shall send <code>FBlockID.InstID.FktID.OPType</code> to the IUT.</li> <li>6. The UT shall evaluate the response of the IUT as specified in Table 10.</li> <li>7. The UT after processing all inner and outer loop iterations, the UT shall shutdown the IUT and re-establish the prerequisites for the next test iteration.</li> </ol> |
| <b>Iteration</b>         | Repeat Step 1 to Step 7 with the NetBlock and every available <code>InstID</code> (obtained by <code>NetBlock.FBlockIDs</code> ) of the following FBlocks of the <ul style="list-style-type: none"> <li>— IUT: NetworkMaster;</li> <li>— IUT: FBlocks containing sink functions (identified by <code>TEST_GSI_GSO_identification</code>);</li> <li>— IUT: FBlocks containing source functions (identified by <code>TEST_GSI_GSO_identification</code>).</li> </ul> Repeat END.   |
| <b>Expected response</b> | Step 6: IUT ok: the IUT responds as expected to the requests to methods.<br>Step 6: IUT not ok: the IUT does not respond as expected to the requests to methods.   |

Table 9 (continued)

| Item   | Content  |
|--------|--|
| Remark | <ol style="list-style-type: none"> <li>1. Responses are only evaluated from actually tested FBlockID, InstID and FktID.</li> <li>2. Any other message of the node that contains the IUT are ignored during the CTC.</li> <li>3. The FBlock library determines whether the function is a property or a method.</li> <li>4. Usually, a MOST device does not implement all functions and OPTypes of the FBlock library; if implemented, they are expected to behave according to the FBlock library.</li> <li>5. There are no range checks.</li> <li>6. Each OPType test presumes that the previous test completes the communication process, for example if after StartResult the Result is not received, then the process is expected to be terminated by sending Abort. If Abort fails, the IUT is reset to finish the test step and continue with the next step.</li> <li>7. In the case that the OPType test allows “no answer” as permitted response, the observation time period is <math>2 \times t_{\text{Property}}</math> for properties respectively <math>t_{\text{DeadlockMid}}</math> for methods.</li> <li>8. In the case that the OPType test allows Processing as permitted response, Processing might be received before result or an error, maximum 50 Processing are accepted by the UT, then Abort is sent.</li> <li>9. For the NetBlock, error 03<sub>16</sub> (function not available) is also a permitted response.</li> <li>10. All commands (OPType 0<sub>16</sub> to 8<sub>16</sub>) from the node that contains the IUT are ignored for all FBlocks.</li> <li>11. For the NetBlock and the Network Master FBlock, OPTypes 9<sub>16</sub> to F<sub>16</sub> not tested.</li> <li>12. During this CTC it is considered that sending of an error is optional in function DTCP_Control.</li> </ol> |

Table 10 specifies the OPTypes for CTC\_2.1.0-2 - Generic FBlock method test.

Table 10 — OPTypes for CTC\_2.1.0-2 - Generic FBlock method test

| OPType          | OPType name and parameters | Permitted response  |
|-----------------|----------------------------|---|
| 0 <sub>16</sub> | Start                      | <ul style="list-style-type: none"> <li>— None;</li> <li>— Result;</li> <li>— Processing;</li> <li>— All errors except 01<sub>16</sub>, 02<sub>16</sub>, 03<sub>16</sub>, 0A<sub>16</sub>, 0C<sub>16</sub>.</li> </ul> |
| 1 <sub>16</sub> | Abort                      | <ul style="list-style-type: none"> <li>— None;</li> <li>— All errors except 01<sub>16</sub>, 02<sub>16</sub>, 03<sub>16</sub>, 0A<sub>16</sub>, 0C<sub>16</sub>.</li> </ul>   |
| 2 <sub>16</sub> | StartResult                | <ul style="list-style-type: none"> <li>— ProcessingResult;</li> <li>— All errors except 01<sub>16</sub>, 02<sub>16</sub>, 03<sub>16</sub>, 0A<sub>16</sub>, 0C<sub>16</sub>.</li> </ul>                               |
| 3 <sub>16</sub> | Increment                  | — Error 04 <sub>16</sub>  |
| 4 <sub>16</sub> | Decrement                  | — Error 04 <sub>16</sub>  |
| 5 <sub>16</sub> | Not allowed                | Not allowed   |

Table 10 (continued)

| OType           | OType name and parameters                            | Permitted response  |
|-----------------|--|---|
| 6 <sub>16</sub> | StartResultAck<br>(SenderHandle 1234 <sub>16</sub> ) | <ul style="list-style-type: none"> <li>— ProcessingAck(SenderHandle 1234<sub>16</sub>);</li> <li>— ResultAck(SenderHandle 1234<sub>16</sub>);</li> <li>— All ErrorAcks (SenderHandle 1234<sub>16</sub>) except 01<sub>16</sub>, 02<sub>16</sub>, 03<sub>16</sub>, 0A<sub>16</sub>, 0C<sub>16</sub>;</li> <li>— All Errors except 01<sub>16</sub>, 02<sub>16</sub>, 03<sub>16</sub>, 0A<sub>16</sub>, 0C<sub>16</sub>.</li> </ul>                  |
| 7 <sub>16</sub> | AbortAck<br>(SenderHandle 1234 <sub>16</sub> )       | <ul style="list-style-type: none"> <li>— None;</li> <li>— All ErrorAcks (SenderHandle 1234<sub>16</sub>) except 01<sub>16</sub>, 02<sub>16</sub>, 03<sub>16</sub>, 0A<sub>16</sub>, 0C<sub>16</sub>;</li> <li>— All Errors except 01<sub>16</sub>, 02<sub>16</sub>, 03<sub>16</sub>, 0A<sub>16</sub>, 0C<sub>16</sub>.</li> </ul>   |
| 8 <sub>16</sub> | StartAck<br>(SenderHandle 1234 <sub>16</sub> )       | <ul style="list-style-type: none"> <li>— None;</li> <li>— ResultAck(SenderHandle 1234<sub>16</sub>);</li> <li>— ProcessingAck(SenderHandle 1234<sub>16</sub>);</li> <li>— All ErrorAcks (SenderHandle 1234<sub>16</sub>) except 01<sub>16</sub>, 02<sub>16</sub>, 03<sub>16</sub>, 0A<sub>16</sub>, 0C<sub>16</sub>;</li> <li>— All Errors except 01<sub>16</sub>, 02<sub>16</sub>, 03<sub>16</sub>, 0A<sub>16</sub>, 0C<sub>16</sub>.</li> </ul> |
| 9 <sub>16</sub> | ErrorAck   | No answer   |
| A <sub>16</sub> | ProcessingAck  | No answer   |
| B <sub>16</sub> | Processing   | No answer   |
| C <sub>16</sub> | Result   | No answer   |
| D <sub>16</sub> | ResultAck  | No answer   |
| E <sub>16</sub> | Not allowed  | Not allowed   |
| F <sub>16</sub> | Error  | No answer   |

## 8.2 Power management

### 8.2.1 Power management - PowerMaster

#### 8.2.1.1 CTC\_2.3.1-3 - Timeout execute/timeout suspend test

Table 11 specifies the CTC\_2.3.1-3 - Timeout execute/timeout suspend test.

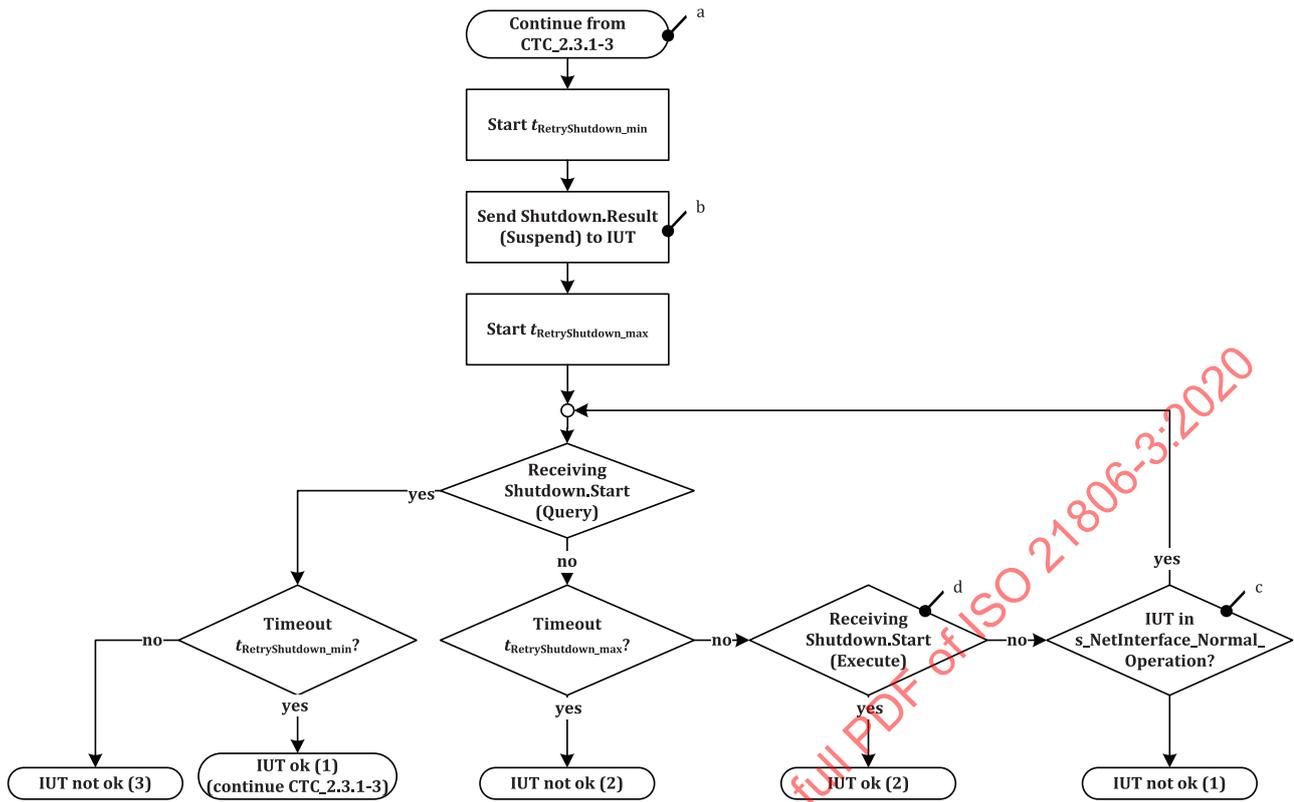
Table 11 — CTC\_2.3.1-3 – Timeout execute/timeout suspend test

| Item                     | Content  |
|--------------------------|--|
| <b>CTC # - Title</b>     | CTC_2.3.1-3 – Timeout execute/timeout suspend test   |
| <b>Purpose</b>           | <p>This CTC verifies that the PowerMaster is capable of correctly performing a shutdown, when there are no shutdown-suspend requests from the PowerSlaves within the specified time constraints:</p> <ul style="list-style-type: none"> <li>— for Shutdown.Start (Query), <math>t_{\text{WaitSuspend}}</math> is used;</li> <li>— for Shutdown.Start (Execute), <math>t_{\text{ShutdownWait}} + t_{\text{SSOShutdown}}</math> is used.</li> </ul> <p>This CTC applies to all MOST devices that contain the PowerMaster.</p>  |
| <b>Reference</b>         | ISO 21806-2:2020, 6.9.3 APP – Network shutdown   |
| <b>Prerequisite</b>      | <ul style="list-style-type: none"> <li>— The UT shall effectuate <code>s_NetInterface_Normal_Operation</code> in the node that contains the IUT.</li> <li>— Preconditions that prevent the node that contains the IUT from performing shutdown shall be switched off.</li> </ul>   |
| <b>Set-up</b>            | <ul style="list-style-type: none"> <li>— If the IUT is part of a MOST device that contains the TimingMaster, the LT pre-IUT shall be a TimingSlave.</li> <li>— If the IUT is part of a MOST device that does not contain the TimingMaster, the LT pre-IUT shall be the TimingMaster.</li> <li>— The LT post-IUT shall be a listen-only node.</li> <li>— PowerSlaves in multi-node devices shall be configured to not send <code>NetBlock.Shutdown.Result (Suspend)</code>.</li> </ul>  |
| <b>Step</b>              | 1. The UT shall execute the sequence as specified in <a href="#">Figure 5</a> .  |
| <b>Iteration</b>         | Not applicable   |
| <b>Expected response</b> | <p>Step 1: IUT ok: the IUT performs a shutdown correctly.</p> <p>Step 1: IUT not ok (1): the IUT does not start the shutdown procedure.</p> <p>Step 1: IUT not ok (2): the IUT sends <code>Shutdown.Start (Execute)</code> too early.</p> <p>Step 1: IUT not ok (3): the IUT switches off the MOST output too early.</p> <p>Step 1: IUT not ok (4): the IUT switches off the MOST output too late or does not set the shutdown flag.</p> <p>Step 1: IUT not ok (5): the IUT does not perform <code>s_NetInterface_Normal_Operation</code> during <math>t_{\text{WaitSuspend}}</math></p> |
| <b>Remark</b>            | ---  |



Table 12 — CTC\_2.3.1-6 - Timeout suspend test

| Item                     | Content  |
|--------------------------|--|
| <b>CTC # - Title</b>     | CTC_2.3.1-6 - Timeout suspend test   |
| <b>Purpose</b>           | This CTC verifies that the PowerMaster is capable of postponing the execution of a shutdown due to shutdown-suspend requests from the PowerSlaves and that it repeats the shutdown attempt within the specified time constraints.<br>This CTC applies to all MOST devices that contain the PowerMaster.  |
| <b>Reference</b>         | ISO 21806-2:2020, 6.9.3 APP - Network shutdown   |
| <b>Prerequisite</b>      | The UT shall effectuate <code>s_NetInterface_Normal_Operation</code> in the node that contains the IUT.  |
| <b>Set-up</b>            | <ul style="list-style-type: none"> <li>— If the IUT is part of a MOST device that contains the TimingMaster, the LT pre-IUT shall be a TimingSlave.</li> <li>— If the IUT is part of a MOST device that does not contain the TimingMaster, the LT pre-IUT shall be the TimingMaster.</li> <li>— The LT post-IUT shall be a listen-only node.</li> <li>— PowerSlaves in multi-node devices shall be configured to not send <code>Shutdown.Result(Suspend)</code>.</li> </ul>            |
| <b>Step</b>              | 1. The UT shall execute the sequence as specified in <a href="#">Figure 6</a> .  |
| <b>Iteration</b>         | Not applicable   |
| <b>Expected response</b> | <p>Step 1: IUT ok (1): the IUT postpones the execution of the shutdown.</p> <p>Step 1: IUT ok (2): the IUT overrides the suspend request.</p> <p>Step 1: IUT not ok (1): the IUT does not remain in <code>s_NetInterface_Normal_Operation</code> during <math>t_{\text{RetryShutdown}}</math>.</p> <p>Step 1: IUT not ok (2): the IUT does not send <code>Shutdown.Start(Query)</code>.</p> <p>Step 1: IUT not ok (3): the IUT sends <code>Shutdown.Start(Query)</code> too early.</p> |
| <b>Remark</b>            | ---  |



- a The CTC shall continue from CTC\_2.3.1-3.
- b The UT, using the LT pre-IUT, shall send a Shutdown.Result (Suspend) to the IUT before  $t_{WaitSuspend}$  expires.
- c The node that contains the IUT stays in `s_NetInterface_Normal_Operation` until  $t_{RetryShutdown_min}$  expires. `s_NetInterface_Normal_Operation` shall be checked every 500 ms.
- d The IUT starts a new shutdown attempt before  $t_{RetryShutdown_max}$  expires.

Figure 6 — CTC\_2.3.1-6 - Timeout suspend test

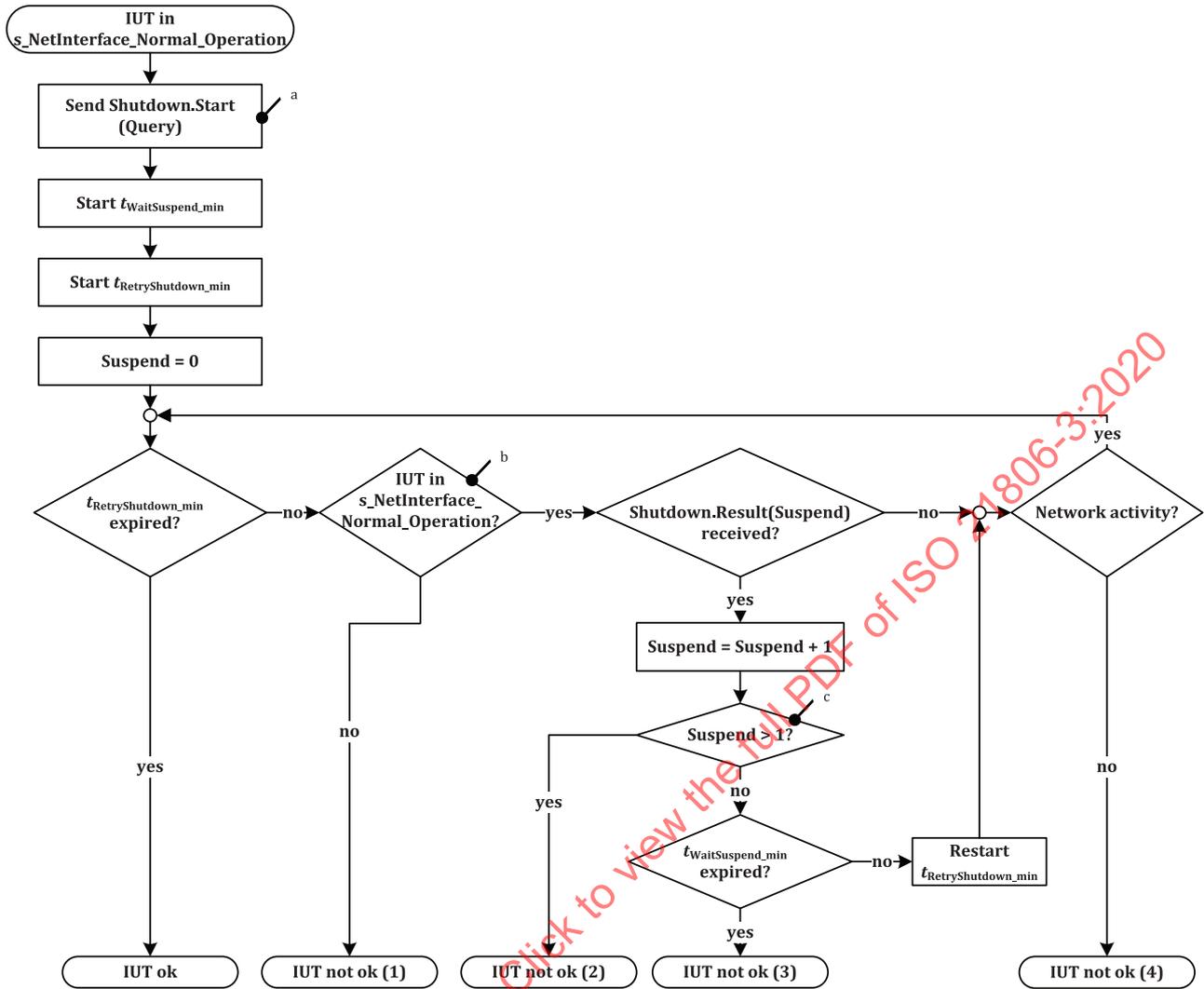
## 8.2.2 Power management - PowerSlave

### 8.2.2.1 CTC\_2.3.2-2 - Shutdown.Start(Query) test

Table 13 specifies the CTC\_2.3.2-2 - Shutdown.Start(Query) test.

Table 13 — CTC\_2.3.2-2 - Shutdown.Start(Query) test

| Item                     | Content   |
|--------------------------|---|
| <b>CTC # - Title</b>     | CTC_2.3.2-2 - Shutdown.Start(Query) test  |
| <b>Purpose</b>           | <p>This CTC verifies that a PowerSlave does not switch off the MOST output due to reception of Shutdown.Start(Query).</p> <p>The CTC also verifies that, if the PowerSlave attempts to suspend the shutdown, it does so within the specified time constraints.</p> <p>This CTC applies to all MOST devices that do not contain the PowerMaster.</p>   |
| <b>Reference</b>         | ISO 21806-2:2020, 6.9.3 APP - Network shutdown  |
| <b>Prerequisite</b>      | The UT shall effectuate <code>s_NetInterface_Normal_Operation</code> in the node that contains the IUT.   |
| <b>Set-up</b>            | <ul style="list-style-type: none"> <li>— If the IUT is part of a MOST device that contains the TimingMaster, the LT pre-IUT shall be a TimingSlave.</li> <li>— If the IUT is part of a MOST device that does not contain the TimingMaster, the LT pre-IUT shall be the TimingMaster.</li> <li>— The LT post-IUT shall be in listen-only node.</li> </ul>  |
| <b>Step</b>              | 1. The UT shall execute the sequence as specified in <a href="#">Figure 7</a> .   |
| <b>Iteration</b>         | Not applicable  |
| <b>Expected response</b> | <p>Step 1: IUT ok: the IUT remains in <code>s_NetInterface_Normal_Operation</code>.</p> <p>Step 1: IUT not ok (1): the IUT does not remain in <code>s_NetInterface_Normal_Operation</code> after Shutdown.Start(Query).</p> <p>Step 1: IUT not ok (2): the IUT sends <code>Shutdown.Result(Suspend)</code> too often.</p> <p>Step 1: IUT not ok (3): the IUT sends <code>Shutdown.Result(Suspend)</code> too late.</p> <p>Step 1: IUT not ok (4): the IUT switches off the MOST output by itself.</p> |
| <b>Remark</b>            | If CTC ends with "IUT ok", continue with CTC_2.3.2-3 - CTC_2.3.2-3 - Shutdown.Start(Execute) test.  |



- a The UT, using the LT pre-IUT, shall send Shutdown.Start (Query).
- b The node that contains the IUT remains in s\_NetInterface\_Normal\_Operation. s\_NetInterface\_Normal\_Operation shall be checked every 500 ms.
- c The IUT is allowed to send Shutdown.Result (Suspend) once. In that case, Shutdown.Result (Suspend) is sent within tWaitSuspend\_min after Shutdown.Start (Query).

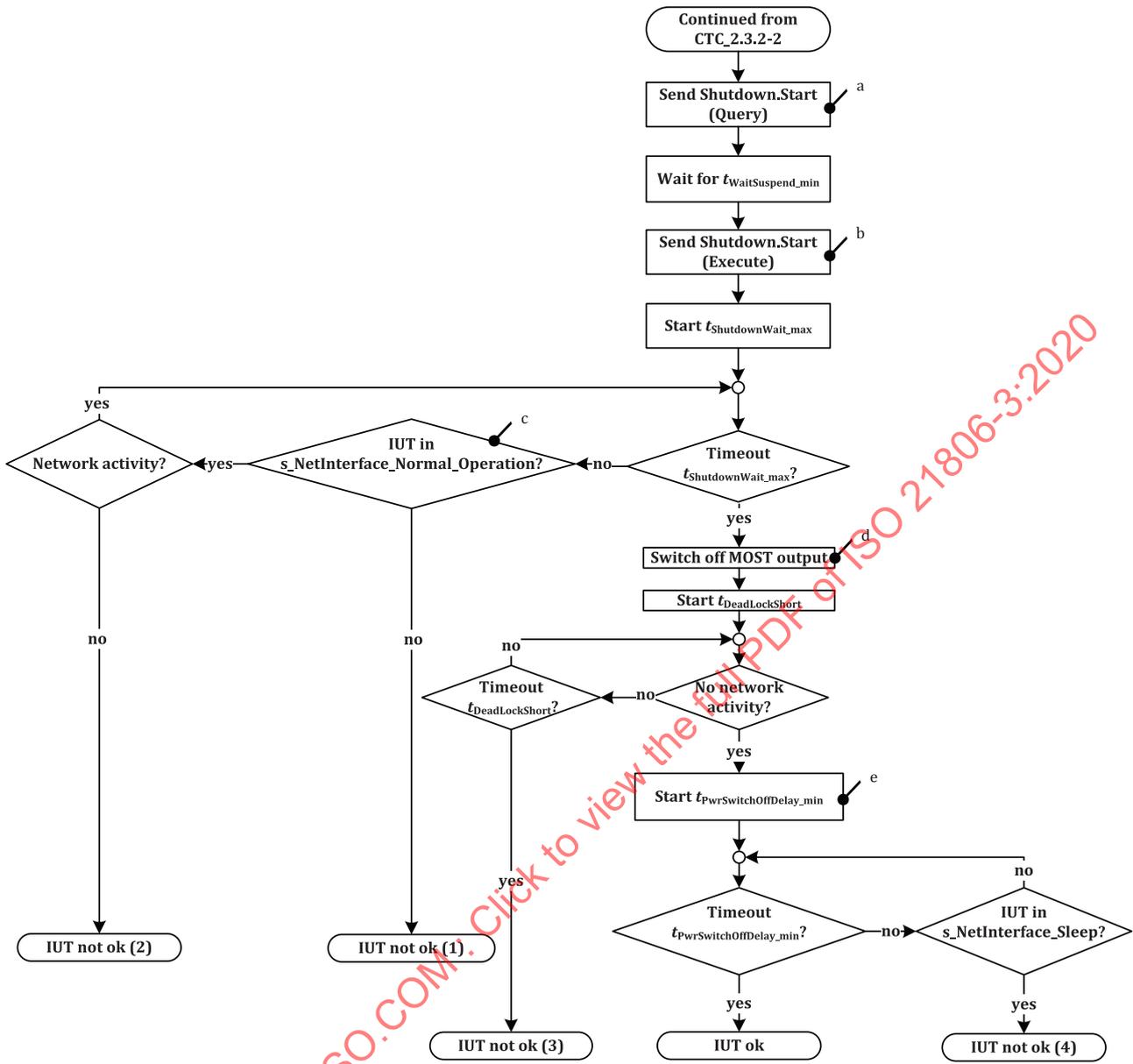
Figure 7 — CTC\_2.3.2-2 - Shutdown.Start(Query) test

8.2.2.2 CTC\_2.3.2-3 - Shutdown.Start(Execute) test

Table 14 specifies the CTC\_2.3.2-3 - Shutdown.Start(Execute) test.

Table 14 — CTC\_2.3.2-3 – Shutdown.Start(Execute) test

| Item                     | Content   |
|--------------------------|---|
| <b>CTC # - Title</b>     | CTC_2.3.2-3 – Shutdown.Start(Execute) test  |
| <b>Purpose</b>           | This CTC verifies that a PowerSlave, after receiving Shutdown.Start(Execute), remains in s_NetInterface_Normal_Operation until the LT pre-IUT switches off the MOST output.<br>Additionally, it verifies that the IUT does not enter s_NetInterface_Sleep before $t_{PwrSwitchOffDelay}$ expires.<br>This CTC applies to all MOST devices that do not contain the PowerMaster.  |
| <b>Reference</b>         | ISO 21806-2:2020, 6.9.3 APP – Network shutdown  |
| <b>Prerequisite</b>      | The CTC shall continue directly from CTC_2.3.2-2 – Shutdown.Start(Query) test.  |
| <b>Set-up</b>            | <ul style="list-style-type: none"> <li>— If the IUT is part of a MOST device that contains the TimingMaster, the LT pre-IUT shall be a TimingSlave.</li> <li>— If the IUT is part of a MOST device that does not contain the TimingMaster, the LT pre-IUT shall be the TimingMaster.</li> <li>— The LT post-IUT shall be a listen-only node.</li> <li>— Power consumption of the IUT is monitored.</li> </ul>   |
| <b>Step</b>              | 1. The IUT shall execute the sequence as specified in <a href="#">Figure 8</a> .  |
| <b>Iteration</b>         | Not applicable  |
| <b>Expected response</b> | <p>Step 1: IUT ok: the IUT remains in s_NetInterface_Normal_Operation.</p> <p>Step 1: IUT not ok (1): the IUT does not remain in s_NetInterface_Normal_Operation after reception of Shutdown.Start(Execute).</p> <p>Step 1: IUT not ok (2): the IUT switches off the MOST output by itself too early.</p> <p>Step 1: IUT not ok (3): the IUT does not switch off the MOST output within <math>t_{DeadLockShort}</math>.</p> <p>Step 1: IUT not ok (4): the IUT enters s_NetInterface_Sleep too early.</p> |
| <b>Remark</b>            | ---   |



- a The UT, using the LT pre-IUT, shall send `Shutdown.Start (Query)` and wait for  $t_{WaitSuspend\_min}$ .
- b The UT, using the LT pre-IUT, shall send `Shutdown.Start (Execute)`.
- c The IUT prepares for shutdown. The node that contains the IUT remains in `NetInterface` state `s_NetInterface_Normal_Operation`. `s_NetInterface_Normal_Operation` shall be checked every 500 ms.
- d Any kind of communication is possible until the LT pre-IUT switches off the MOST output. The IUT does not switch off the MOST before the LT pre-IUT does.
- e When the LT pre-IUT switches off the MOST output, the node that contains the IUT does not enter `s_NetInterface_Sleep` before  $t_{PwrSwitchOffDelay\_min}$  is expired.

Figure 8 — CTC\_2.3.2-3 - Shutdown.Start(Execute) test

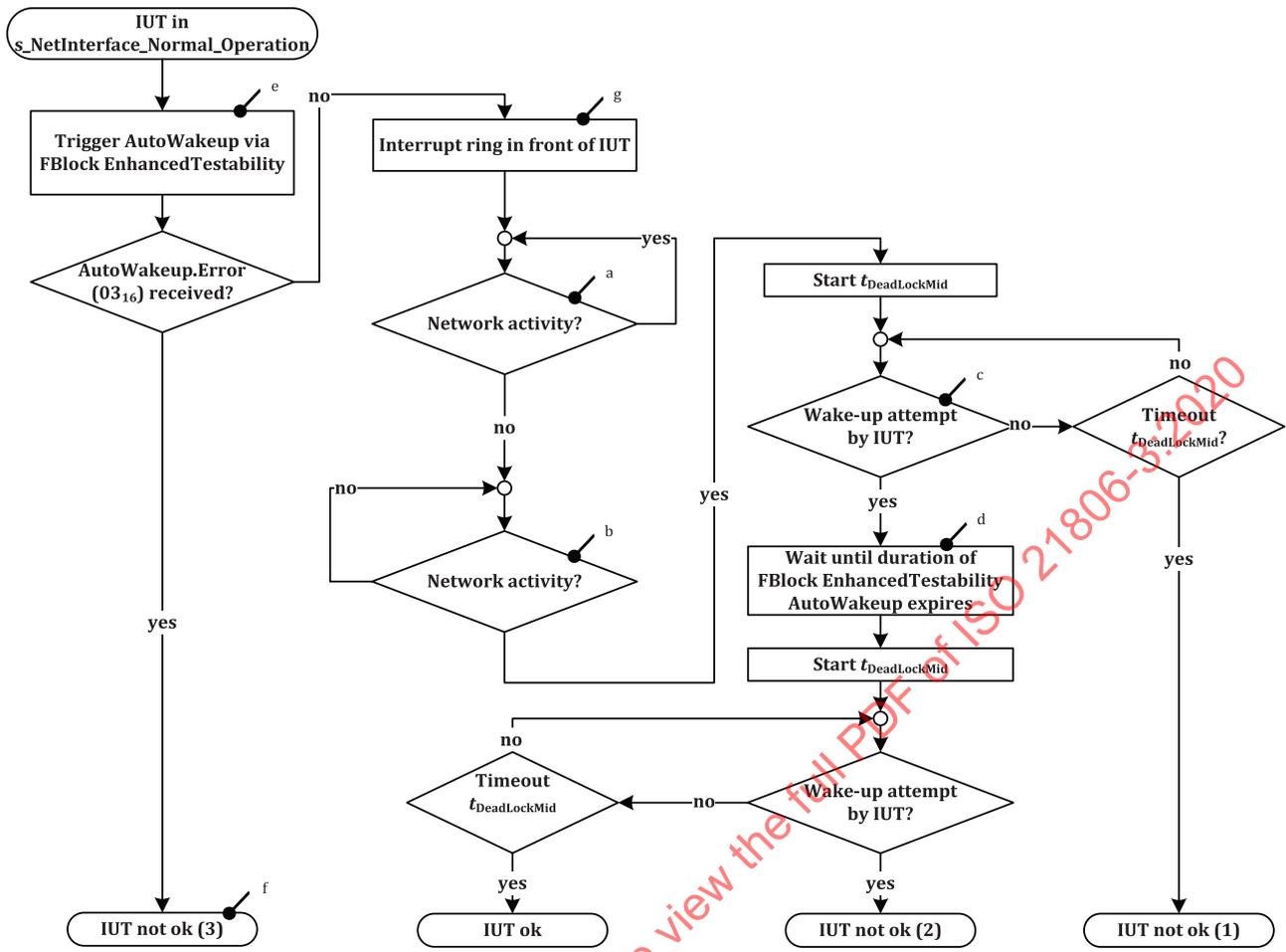
### 8.3 Error management

#### 8.3.1 CTC\_2.4.1-2 - Restart continue test

Table 15 specifies the CTC\_2.4.1-2 - Restart continue test.

Table 15 — CTC\_2.4.1-2 - Restart continue test

| Item                     | Content   |
|--------------------------|---|
| <b>CTC # - Title</b>     | CTC_2.4.1-2 - Restart continue test   |
| <b>Purpose</b>           | This CTC verifies that the PowerMaster is capable of performing a network startup attempt.<br>This CTC applies to all MOST devices that contain the PowerMaster and the TimingMaster in the same node and are capable of performing a network startup (i.e. switching on their MOST outputs). |
| <b>Reference</b>         | ISO 21806-2:2020, 6.9.2 APP - Network wake-up and startup   |
| <b>Prerequisite</b>      | The UT shall effectuate <code>s_NetInterface_Normal_Operation</code> in the node that contains the IUT.   |
| <b>Set-up</b>            | — The LT pre-IUT shall be a TimingSlave.<br>— The LT post-IUT shall be a listen-only node.  |
| <b>Step</b>              | 1. The UT shall execute the sequence as specified in <a href="#">Figure 9</a> .   |
| <b>Iteration</b>         | Not applicable  |
| <b>Expected response</b> | Step 1: IUT ok: the IUT performs a wake-up attempt.<br>Step 1: IUT not ok (1): the IUT does not perform at least one wake-up attempt.<br>Step 1: IUT not ok (2): the IUT continues wake-up attempts without wake-up event.<br>Step 1: IUT not ok (3): the IUT does not support wake-up.       |
| <b>Remark</b>            | ---   |



- a To avoid deadlocks, CTC shall be aborted when  $t_{DeadLockMid}$  expires. The IUT is expected to switch off the MOST output at the latest after  $t_{SSO\_Shutdown\_max}$  expires within  $t_{Shutdown\_max}$ .
- b To avoid deadlocks, CTC shall be aborted when  $t_{DeadLockMid}$  expires. The IUT is expected to switch on the MOST output after  $DelayTime$  of `AutoWakeup` and a manufacturer dependent delay.
- c The IUT performs at least one wake-up attempt. The wake-up attempt shall consist of:
  - 1) MOST output on (at 1<sup>st</sup> loop already on),
  - 2) wait for  $t_{Config}$ ,
  - 3) MOST output off, and
  - 4) wait for  $t_{Restart}$ .
- d `EnhancedTestability.AutoWakeup` is used to simulate the wake-up event. The CTC shall pause until the timeout for `AutoWakeup` ( $Duration = 15$  s) occurs. When the qualified local wake-up event disappears, the IUT does not perform further wake-up attempts. To provide sufficient time, the UT may wait in addition for  $t_{DeadLockMid}$  to expire.
- e The UT, using the LT pre-IUT, shall send `EnhancedTestability.AutoWakeup` to the IUT, using  $Duration = 15$  s to simulate an extended local wake-up event. Use `EnhancedTestability.AutoWakeup` with the following parameters:
  - $DelayTime = 1$  s;
  - $Duration = 15$  s.
- f IUT does not support wake-up.
- g The LT pre-IUT shall interrupt the ring in front of the IUT.

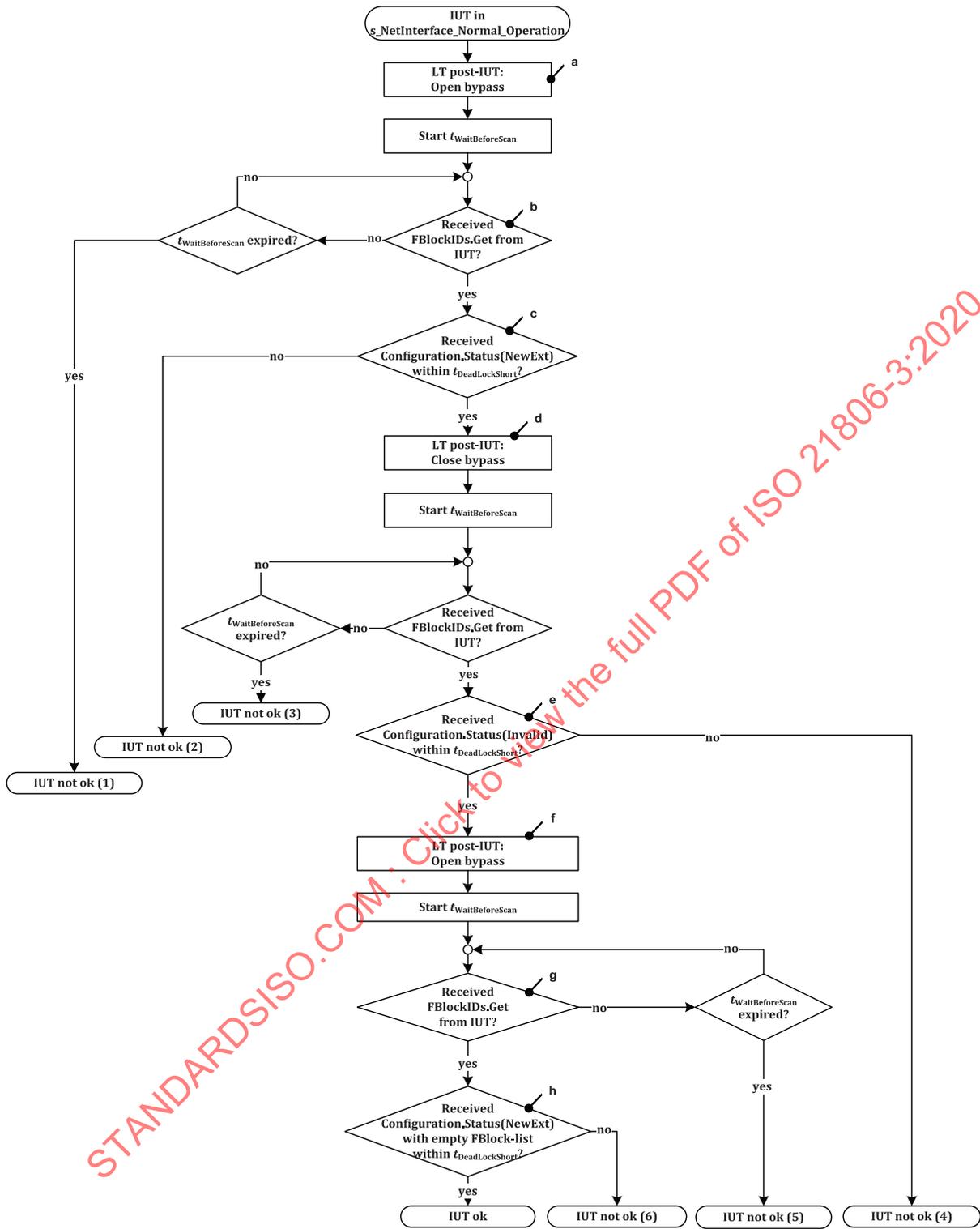
Figure 9 — CTC\_2.4.1-2 - Restart continue test

### 8.3.2 CTC\_2.4.1-9 – Reaction on network change event test

Table 16 specifies the CTC\_2.4.1-9 – Reaction on network change event test.

**Table 16 — CTC\_2.4.1-9 – Reaction on network change event test**

| Item                     | Content   |
|--------------------------|---|
| <b>CTC # - Title</b>     | CTC_2.4.1-9 – Reaction on network change event test   |
| <b>Purpose</b>           | This CTC verifies that the NetworkMaster reacts correctly to NCEs by initiating an FBlock scan, announcing the central registry status and remaining within the specified time constraints.<br><br>This CTC applies to all MOST devices that contain the NetworkMaster.   |
| <b>Reference</b>         | ISO 21806-2:2020, 6.8.3.2 APP – Network change event  |
| <b>Prerequisite</b>      | The UT shall effectuate <code>s_NetInterface_Normal_Operation</code> in the node that contains the IUT.   |
| <b>Set-up</b>            | — The LT pre-IUT shall be a TimingSlave.<br>— The LT post-IUT shall be a TimingSlave with its bypass closed.  |
| <b>Step</b>              | 1. The UT shall execute the sequence as specified in <a href="#">Figure 10</a> .  |
| <b>Iteration</b>         | Not applicable  |
| <b>Expected response</b> | Step 1: IUT ok: the IUT performs the expected actions after an NCE.<br>Step 1: IUT not ok (1): the IUT does not check the FBlock configuration.<br>Step 1: IUT not ok (2): the IUT does not send <code>Configuration.Status(NewExt)</code> .<br>Step 1: IUT not ok (3): the IUT does not check the FBlock configuration.<br>Step 1: IUT not ok (4): the IUT does not send <code>Configuration.Status(Invalid)</code> .<br>Step 1: IUT not ok (5): the IUT does not check the FBlock configuration.<br>Step 1: IUT not ok (6): the IUT does not send <code>Configuration.Status(NewExt)</code> with empty FBlock list. |
| <b>Remark</b>            | When the NCE is generated, an unlock might occur.   |



- a The UT, using the LT post-IUT, shall open its bypass to generate an NCE.
- b The IUT starts an FBlock scan within  $t_{WaitBeforeScan}$ . The UT shall respond to the `FBlockIDs.Get` request to LT post-IUT. The UT shall respond to every FBlock configuration check of the IUT addressed at the LT pre-IUT. The FBlock list shall differ from that reported for the LT post-IUT.
- c The IUT sends `Configuration.Status(NewExt)`. The timer starts with the beginning of the FBlock configuration check.
- d The LT post-IUT shall close its bypass. The IUT starts an FBlock scan within  $t_{WaitBeforeScan}$ .

- e The IUT sends `Configuration.Status(Invalid)`. The timer starts with the beginning of the FBlock configuration check.
- f The LT post-IUT shall generate an NCE without FBlock changes, that is, the LT post-IUT shall open its bypass and respond with an empty FBlock list to the FBlock scan of the IUT.
- g The IUT starts an FBlock scan within  $t_{\text{WaitBeforeScan}}$ . The UT shall respond with empty `FBlockID` list to the `FBlockIDs.Get` request to LT post-IUT.
- h The IUT sends `Configuration.Status(NewExt)` with an empty FBlock list. The timer starts with the beginning of the FBlock configuration check.

**Figure 10 — CTC\_2.4.1-9 - Reaction of network change event test**

## 8.4 Central registry

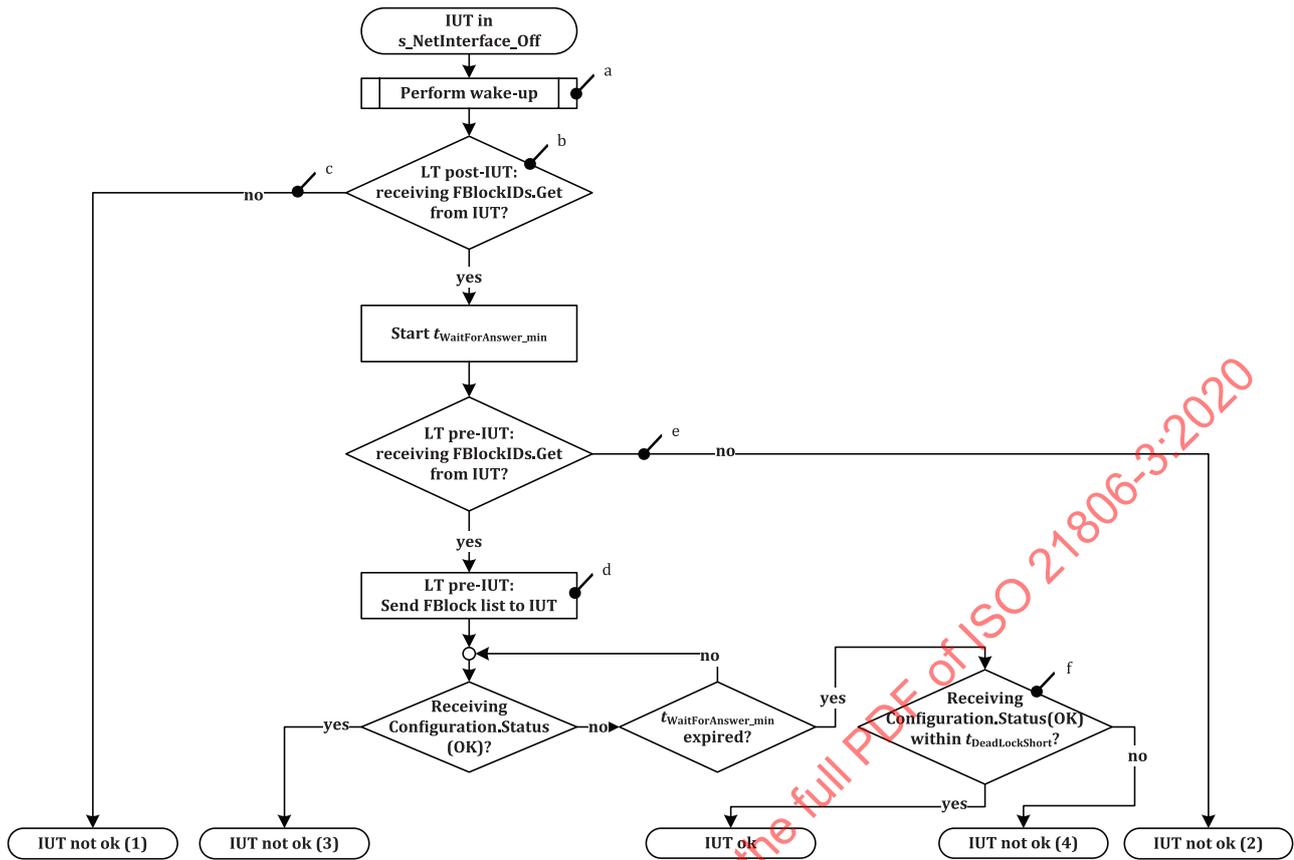
### 8.4.1 Central registry handling (NetworkMaster)

#### 8.4.1.1 CTC\_2.6.2-1 - FBlock polling test

[Table 17](#) specifies the CTC\_2.6.2-1 - FBlock polling test.

**Table 17 — CTC\_2.6.2-1 - FBlock polling test**

| Item                     | Content   |
|--------------------------|---|
| <b>CTC # - Title</b>     | CTC_2.6.2-1 - FBlock polling test   |
| <b>Purpose</b>           | This CTC verifies that the NetworkMaster requests the FBlock configuration from the NetworkSlaves, using <code>FBlockIDs.Get</code> .<br>The CTC also verifies that the NetworkMaster distributes the central registry state and respects the relevant time constraints.<br>This CTC applies to all MOST devices that contain the NetworkMaster.  |
| <b>Reference</b>         | ISO 21806-2:2020, 6.8.3.6.2 APP – Configuration request description   |
| <b>Prerequisite</b>      | — The UT shall effectuate <code>s_NetInterface_Off</code> in the node that contains the IUT.<br>— The ring shall be closed.   |
| <b>Set-up</b>            | — The LT pre-IUT shall be a TimingSlave.<br>— The LT post-IUT shall be a TimingSlave.   |
| <b>Step</b>              | 1. The UT shall execute the sequence as specified in <a href="#">Figure 11</a> .  |
| <b>Iteration</b>         | Not applicable  |
| <b>Expected response</b> | Step 1: IUT ok: the IUT builds and distributes the central registry.<br>Step 1: IUT not ok (1): the IUT does not check the FBlocks of the LT post-IUT.<br>Step 1: IUT not ok (2): the IUT does not check the FBlocks of the LT pre-IUT.<br>Step 1: IUT not ok (3): the IUT does not wait long enough for the response of the LT pre-IUT ( $t_{\text{WaitForAnswer\_min}}$ ).<br>Step 1: IUT not ok (4): the IUT does not send <code>Configuration.Status(OK)</code> . |
| <b>Remark</b>            | ---   |



- a After wake-up, the IUT checks all FBlocks of the LT pre-IUT and of the LT post-IUT. If the IUT polls the LT pre-IUT before the LT post-IUT, the roles of the LT pre-IUT and the LT post-IUT shall be swapped.
- b When the IUT queries the LT post-IUT, the UT shall start  $t_{WaitForAnswer\_min}$ . For the LT post-IUT, the UT shall not respond to the FBlock scan.
- c The UT shall stop the CTC if the IUT does not send `FBlockIDs.Get` within  $t_{DeadLockShort}$ .
- d For the LT pre-IUT, the UT shall respond to the FBlock request by the IUT with an FBlock list.
- e The UT shall stop the CTC if the IUT does not send `FBlockIDs.Get` within  $t_{DeadLockShort}$ .
- f When the central registry is built up successfully, the IUT broadcasts `Configuration.Status(OK)` after  $t_{WaitForAnswer\_min}$  expires.

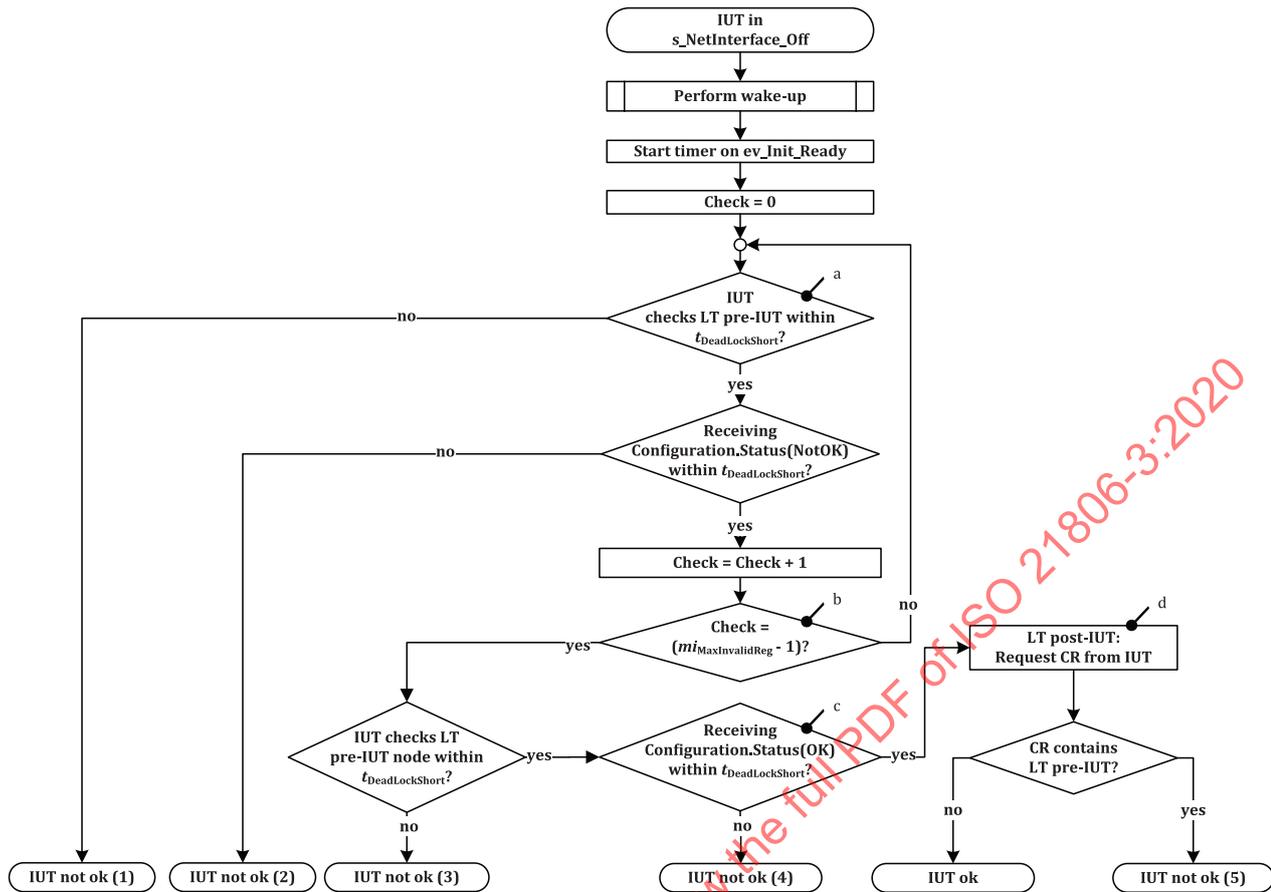
Figure 11 — CTC\_2.6.2-1 - FBlock polling test

8.4.1.2 CTC\_2.6.2-3a - Device ignore test (a)

Table 18 specifies the CTC\_2.6.2-3a - Device ignore test (a).

Table 18 — CTC\_2.6.2-3a - Device ignore test (a)

| Item                     | Content  |
|--------------------------|--|
| <b>CTC # - Title</b>     | CTC_2.6.2-3a - Device ignore test (a)  |
| <b>Purpose</b>           | <p>This CTC verifies that the NetworkMaster detects that a node does not have a valid logical node address, sets the central registry status to <code>NotOK</code>, attempts to gather FBlock information from that node up to the maximum number of invalid node registrations, and finally sets the central registry status to <code>OK</code>.</p> <p>This CTC applies to all MOST devices that contain the NetworkMaster.</p>  |
| <b>Reference</b>         | ISO 21806-2:2020, 6.8.3.7 APP – Invalid announcements – Node address   |
| <b>Prerequisite</b>      | <ul style="list-style-type: none"> <li>— The UT shall effectuate <code>s_NetInterface_Off</code> in the node that contains the IUT.</li> <li>— The ring shall be closed.</li> </ul>  |
| <b>Set-up</b>            | <ul style="list-style-type: none"> <li>— The LT pre-IUT shall be a TimingSlave with its logical node address set to <code>uninitialised_node_address</code>.</li> <li>— The LT post-IUT shall be a TimingSlave.</li> </ul>   |
| <b>Step</b>              | 1. The UT shall execute the sequence as specified in <a href="#">Figure 12</a> .   |
| <b>Iteration</b>         | Not applicable   |
| <b>Expected response</b> | <p>Step 1: IUT ok: the IUT handles a node with an invalid logical node address correctly.</p> <p>Step 1: IUT not ok (1): the IUT does not check the LT pre-IUT.</p> <p>Step 1: IUT not ok (2): the IUT does not broadcast <code>Configuration.Status(NotOK)</code>.</p> <p>Step 1: IUT not ok (3): the IUT checks the LT pre-IUT less than <math>mi_{MaxInvalidReg}</math> times.</p> <p>Step 1: IUT not ok (4): the IUT does not broadcast <code>Configuration.Status(OK)</code>.</p> <p>Step 1: IUT not ok (5): the IUT stores the LT pre-IUT into the central registry.</p> |
| <b>Remark</b>            | ---  |



- a After wake-up, the IUT sends `FBlockIDs.Get`. The UT, using the LT pre-IUT, shall respond with `uninitialised_node_address`. The IUT detects that the LT pre-IUT has an `uninitialised_node_address` and broadcasts `Configuration.Status(NotOK)`.
- b The LT pre-IUT shall not change its address. The IUT checks the LT pre-IUT as often as defined in  $mi_{MaxInvalidReg}$ .
- c The IUT sends `Configuration.Status(OK)`. The IUT is allowed to send `Configuration.Status(NotOK)` or start a rescan before broadcasting `Configuration.Status(OK)`.
- d The IUT ignores the LT pre-IUT, until the next NCE or network startup. The IUT does not store the address of the LT pre-IUT in the central registry. The UT, using the LT post-IUT, shall respond with a valid address. It requests the central registry from the IUT at end of the CTC.

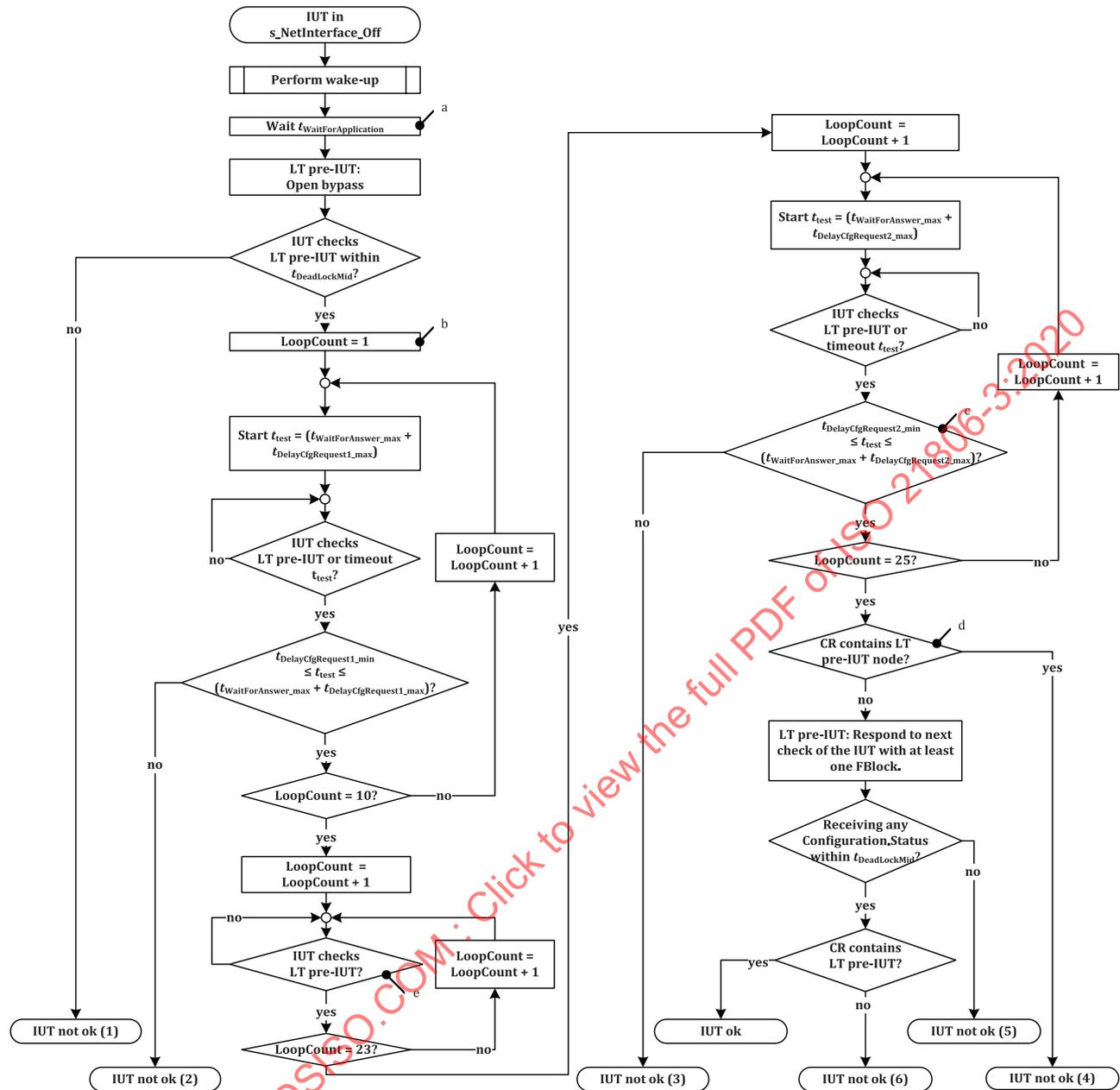
Figure 12 — CTC\_2.6.2-3a - Device ignore test (a)

8.4.1.3 CTC\_2.6.2-3b – Device ignore test (b)

Table 19 specifies the CTC\_2.6.2-3b – Device ignore test (b).

Table 19 — CTC\_2.6.2-3b – Device ignore test (b)

| Item                     | Content   |
|--------------------------|---|
| <b>CTC # - Title</b>     | CTC_2.6.2-3b – Device ignore test (b)   |
| <b>Purpose</b>           | <p>This CTC verifies that the NetworkMaster uses <math>t_{\text{DelayCfgRequest1}}</math> or <math>t_{\text{DelayCfgRequest2}}</math> to introduce pauses between requests to non-responding nodes.</p> <p>The CTC also verifies that the NetworkMaster adds the node to the central registry when it finally answers.</p> <p>This CTC applies to all MOST devices that contain the NetworkMaster.</p>  |
| <b>Reference</b>         | ISO 21806-2:2020, 6.8.3.6.6 APP – Non-responding NetworkSlaves  |
| <b>Prerequisite</b>      | <ul style="list-style-type: none"> <li>— The UT shall effectuate <code>s_NetInterface_Off</code> in the node that contains the IUT.</li> <li>— The ring shall be closed.</li> </ul>   |
| <b>Set-up</b>            | <ul style="list-style-type: none"> <li>— The LT pre-IUT shall be a TimingSlave with its bypass closed.</li> <li>— The LT post-IUT shall be a TimingSlave.</li> </ul>  |
| <b>Step</b>              | 1. The UT shall execute the sequence as specified in <a href="#">Figure 13</a> .  |
| <b>Iteration</b>         | Not applicable  |
| <b>Expected response</b> | <p>Step 1: IUT ok: the IUT handles the non-responding node correctly.</p> <p>Step 1: IUT not ok (1): the IUT does not check the LT pre-IUT.</p> <p>Step 1: IUT not ok (2): the IUT checks the LT pre-IUT not within <math>t_{\text{DelayCfgRequest1\_min/max}}</math>.</p> <p>Step 1: IUT not ok (3): the IUT checks the LT pre-IUT not within <math>t_{\text{DelayCfgRequest2\_min/max}}</math>.</p> <p>Step 1: IUT not ok (4): the IUT erroneously stores the LT pre-IUT into the central registry.</p> <p>Step 1: IUT not ok (5): the IUT does not detect delayed responding the LT pre-IUT.</p> <p>Step 1: IUT not ok (6): the IUT does not store the LT pre-IUT into the central registry.</p> |
| <b>Remark</b>            | <p>Not responding to requests of the IUT means:</p> <ol style="list-style-type: none"> <li>1. The UT acknowledges reception but does not respond to the request from the IUT (it does not send any status; the IUT waits for <math>t_{\text{WaitForAnswer\_min}}</math>; refer to CTC_2.6.2-1).</li> <li>2. The CTC does not focus on the border between loop count 20 and 21. The CTC only checks whether the IUT changes from short check interval to long check interval.</li> </ol>   |



- a Manufacturer dependent.
- b After wake-up, the IUT detects that the LT pre-IUT does not respond to its requests. The first check is performed and then the loop starts with '1' as the retries are counted. The UT shall prevent potential shutdown during the CTC by using the LT post-IUT to send Shutdown.Result (Suspend) to the PowerMaster if it receives Shutdown.Start (Query).
- c The IUT does not stop checking the LT pre-IUT. Verify that the IUT checks the LT pre-IUT with a long check interval.
- d The IUT does not add the LT pre-IUT to the central registry. The UT, using the LT pre-IUT, shall wait for Configuration.Status (OK) before checking the central registry. After loop count 25, the UT, using the LT pre-IUT, shall respond to requests from the IUT. The IUT adds the LT pre-IUT to the central registry.
- e Depending on the loop count, the IUT checks the LT pre-IUT with short interval ( $t_{WaitForAnswer} + t_{DelayCfgRequest1}$ ) or long interval ( $t_{WaitForAnswer} + t_{DelayCfgRequest2}$ ). To avoid deadlocks, the UT shall abort the CTC if  $t_{DeadLockMid}$  expires without check.

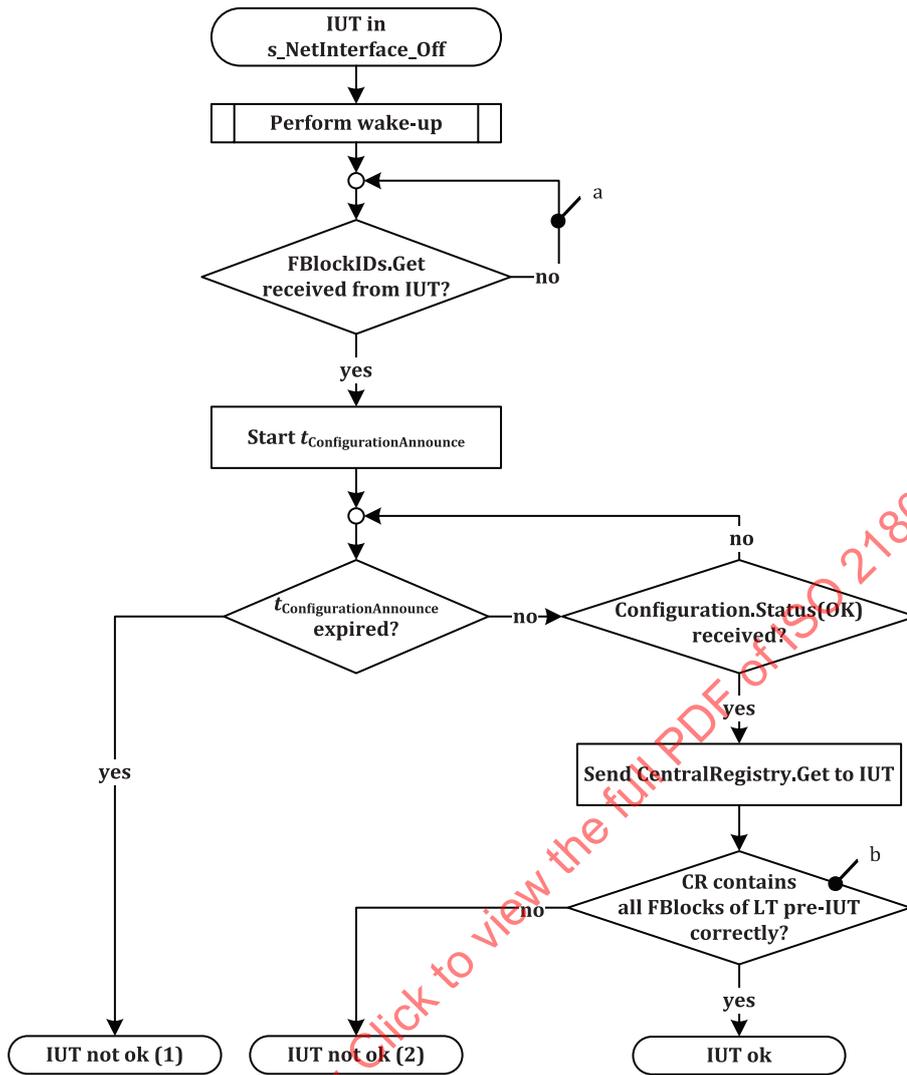
Figure 13 — CTC\_2.6.2-3b - Device ignore test (b)

#### 8.4.1.4 CTC\_2.6.2-4a – Device integration test (a)

Table 20 specifies the CTC\_2.6.2-4a – Device integration test (a).

**Table 20 — CTC\_2.6.2-4a – Device integration test (a)**

| Item                     | Content  |
|--------------------------|--|
| <b>CTC # - Title</b>     | CTC_2.6.2-4a – Device integration test (a)   |
| <b>Purpose</b>           | This CTC verifies that the NetworkMaster distributes the central registry state before $t_{\text{ConfigurationAnnounce}}$ expires.<br>This CTC applies to all MOST devices that contain NetworkMaster.   |
| <b>Reference</b>         | ISO 21806-2:2020:<br>— 6.8.3.6 APP – Scanning the MOST network;<br>— 6.13.2.3 APP – Constraint $t_{\text{ConfigurationAnnounce}}$  |
| <b>Prerequisite</b>      | — The UT shall effectuate <code>s_NetInterface_Off</code> in the node that contains the IUT.<br>— The ring shall be closed.  |
| <b>Set-up</b>            | — The LT pre-IUT shall be a TimingSlave with valid address. It shall respond to <code>FBlockIDs.Get</code> from the IUT.<br>— The LT post-IUT shall be a listen-only node.   |
| <b>Step</b>              | 1. The UT shall execute the sequence as specified in <a href="#">Figure 14</a> .   |
| <b>Iteration</b>         | Not applicable   |
| <b>Expected response</b> | Step 1: IUT ok: the IUT builds the central registry, which contains the expected entry.<br>Step 1: IUT not ok (1): the IUT does not send <code>Configuration.Status(OK)</code> within $t_{\text{ConfigurationAnnounce}}$<br>Step 1: IUT not ok (2): the IUT does not add the LT pre-IUT to the central registry. |
| <b>Remark</b>            | ---  |



- a After wake-up, the IUT adds the LT pre-IUT to the central registry and sends Configuration.Status(OK) within  $t_{ConfigurationAnnounce}$ . The IUT shall stop the CTC if the IUT does not send FBlockIDs.Get within  $t_{DeadLockShort}$ .
- b The central registry shall contain all FBlocks associated with the address of the LT pre-IUT with correct IDs.

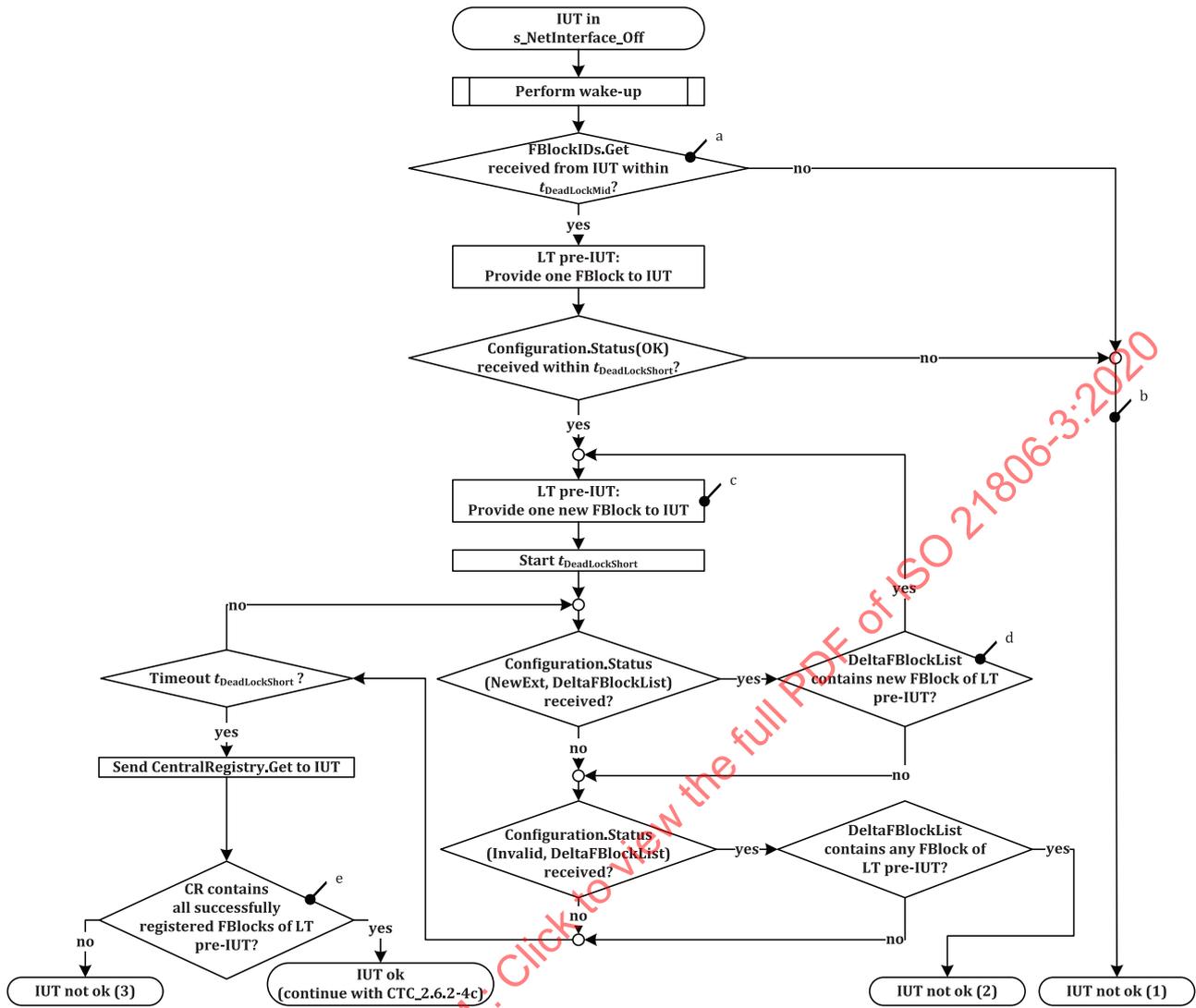
Figure 14 — CTC\_2.6.2-4a - Device integration test (a)

8.4.1.5 CTC\_2.6.2-4b – Device integration test (b)

Table 21 specifies the CTC\_2.6.2-4b – Device integration test (b).

Table 21 — CTC\_2.6.2-4b – Device integration test (b)

| Item                     | Content   |
|--------------------------|---|
| <b>CTC # - Title</b>     | CTC_2.6.2-4b – Device integration test (b)  |
| <b>Purpose</b>           | This CTC verifies that the NetworkMaster stops adding FBlocks to the central registry when the central registry is full.<br>This CTC applies to all MOST devices that contain the NetworkMaster.  |
| <b>Reference</b>         | ISO 21806-2:2020:<br>— 6.8.2 APP – Central registry state;<br>— 6.8.3.9.3 Appearing FBlocks in central registry state OK.   |
| <b>Prerequisite</b>      | — The UT shall effectuate <code>s_NetInterface_Off</code> in the node that contains the IUT.<br>— The ring shall be closed.   |
| <b>Set-up</b>            | — The LT pre-IUT shall be a TimingSlave with valid address.<br>— The LT post-IUT shall be a TimingSlave with valid address.   |
| <b>Step</b>              | 1. The UT shall execute the sequence as specified in <a href="#">Figure 15</a> .  |
| <b>Iteration</b>         | Not applicable  |
| <b>Expected response</b> | Step 1: IUT ok: the IUT stops adding FBlocks when the central registry is full.<br>Step 1: IUT not ok (1): the IUT does not perform initial scan and/or does not send <code>Configuration.Status(OK)</code> in time.<br>Step 1: IUT not ok (2): the IUT deletes FBlocks associated with the LT pre-IUT from the central registry.<br>Step 1: IUT not ok (3): the IUT does not store all successfully registered FBlocks associated with the LT pre-IUT in the central registry.       |
| <b>Remark</b>            | 1. If the IUT supports dynamic central registry (detectable via <code>FBlock EnhancedTestability</code> ), the LT pre-IUT and the LT post-IUT together have 257 FBlocks.<br>2. The UT is able to provide more FBlocks than the central registry of the IUT can store (detectable via <code>FBlock EnhancedTestability.CentralRegistrySize.Get</code> ).<br>3. Potentially, the IUT adds own FBlocks to the central registry which reduces the available size of the central registry. |



- a The UT, using the LT post-IUT, shall respond with an empty FBlock list to any FBlockIDs.Get from the IUT during whole CTC. The UT, using the LT pre-IUT, shall provide more FBlocks than the IUT is able to store in the central registry.
- b The UT shall stop the CTC if the IUT does not perform the initial scan correctly.
- c To fill up the central registry, the UT shall provide step by step single FBlocks until the IUT stops broadcasting ConfigurationStatus(NewExt, ...). The UT, using the LT pre-IUT, shall send FBlockIDs.Status(FBlockIDList).  
The FBlockIDList shall contain all FBlocks provided before and one new FBlock.
- d If the IUT sends FBlockIDs.Get to the LT pre-IUT during the CTC, the UT, using the LT pre-IUT, shall respond with an FBlock list that contains all FBlocks the LT pre-IUT indicates to the IUT at this time. This shall be done to prevent the IUT from deleting FBlocks associated with the LT pre-IUT from the central registry.  
If the IUT tries to change the InstID of an FBlock associated with the LT pre-IUT, the UT shall execute that request according to ISO 21806-2.
- e All FBlocks associated with the LT pre-IUT that have been accepted by the IUT are contained in the central registry at the end of the CTC. It shall not contain FBlocks associated with the LT pre-IUT that are not indicated by the IUT with Configuration.Status(NewExt, DeltaFBlockList). Exception: The first FBlock provided by the UT, using the LT pre-IUT, during the initial scan is not announced with Configuration.Status(NewExt, DeltaFBlockList) but shall be stored in the central registry.

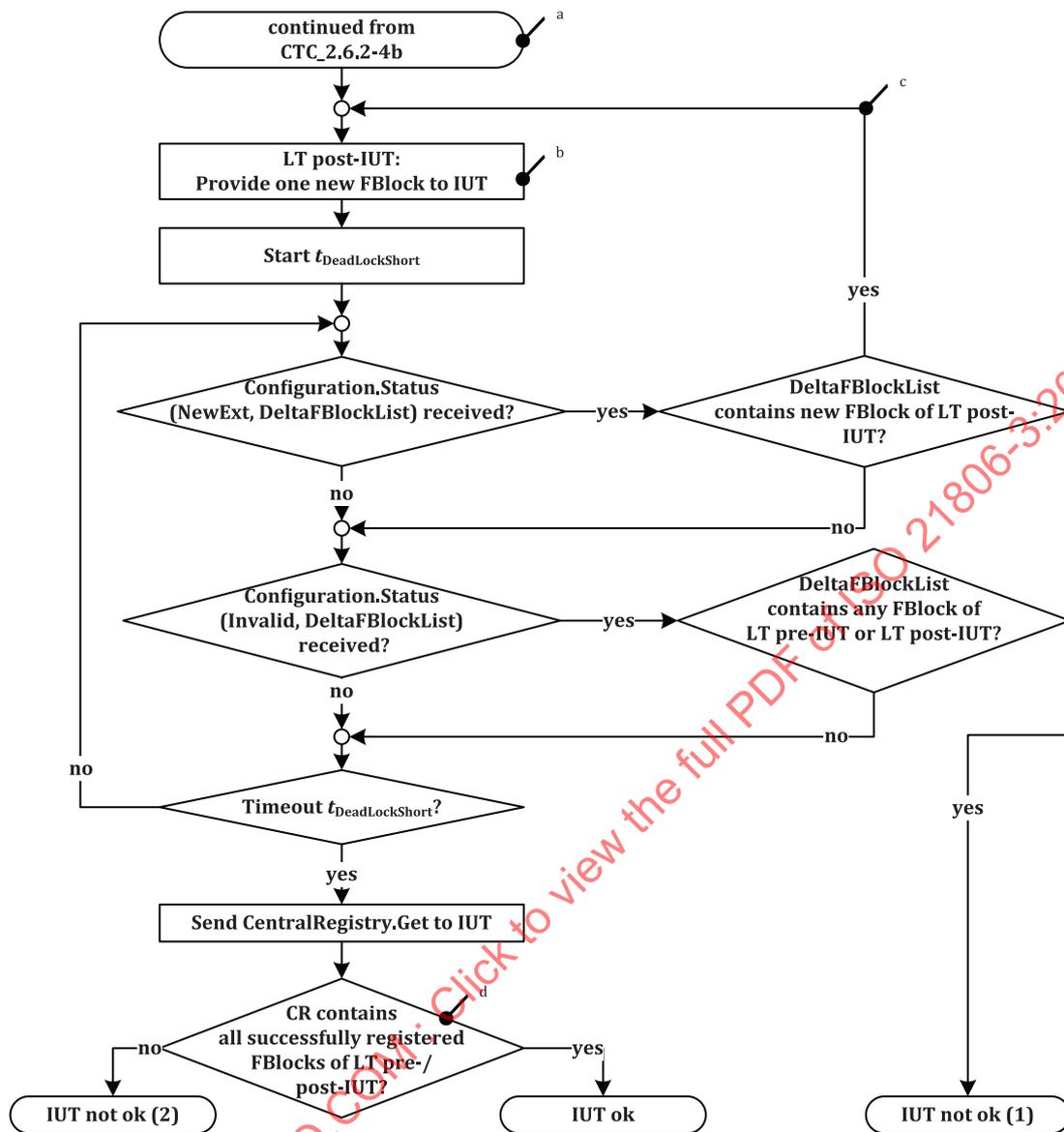
Figure 15 — CTC\_2.6.2-4b - Device integration test (b)

#### 8.4.1.6 CTC\_2.6.2-4c – Device integration test (c)

[Table 22](#) specifies the CTC\_2.6.2-4c – Device integration test (c).

**Table 22 — CTC\_2.6.2-4c – Device integration test (c)**

| Item                     | Content  |
|--------------------------|--|
| <b>CTC # - Title</b>     | CTC_2.6.2-4c – Device integration test (c)   |
| <b>Purpose</b>           | This CTC verifies that the NetworkMaster, when the central registry is full, does not substitute entries which already exist in the central registry with recently announced FBlocks.<br>This CTC applies to all MOST devices that contain the NetworkMaster.  |
| <b>Reference</b>         | ISO 21806-2:2020:<br>— 6.8.2 APP – Central registry state;<br>— 6.8.3.9.3 Appearing FBlocks in central registry state OK.  |
| <b>Prerequisite</b>      | Continued from 2.6.2-4b; “IUT ok”.   |
| <b>Set-up</b>            | — The LT pre-IUT shall be a TimingSlave with valid address.<br>— The LT post-IUT shall be a TimingSlave with valid address.  |
| <b>Step</b>              | 1. The UT shall execute the sequence as specified in <a href="#">Figure 16</a> .   |
| <b>Iteration</b>         | Not applicable   |
| <b>Expected response</b> | Step 1: IUT ok: the IUT does not replace FBlock entries in the central registry.<br>Step 1: IUT not ok (1): the IUT deletes FBlocks associated with the LT pre-IUT or the LT post-IUT from the central registry.<br>Step 1: IUT not ok (2): the IUT does not add all successfully registered FBlocks associated with the LT pre-IUT and the LT post-IUT to the central registry. |
| <b>Remark</b>            | If the IUT supports dynamic central registry (detectable via FBlock EnhancedTestability), the LT pre-IUT and the LT post-IUT together have 257 FBlocks.  |



- a The CTC shall continue from CTC\_2.6.2-4b; "IUT ok". The UT, using the LT post-IUT, shall be able to provide more FBlocks than the central registry of the IUT can store (detectable via FBlock EnhancedTestability.CentralRegistrySize.Get). Potentially, the IUT adds own FBlocks to the central registry, which reduces the available size of the central registry.
- b To fill up the central registry, the UT, using the LT post-IUT, shall provide single FBlocks until the IUT stops broadcasting ConfigurationStatus (NewExt, ...). If the IUT sends FBlockIDs.Get to the LT pre-IUT or the LT post-IUT during the CTC, the UT shall respond with the same FBlock list as before for the LT pre-IUT and the LT post-IUT. This prevents the IUT from deleting FBlocks associated with the LT pre-IUT from the central registry.
- c The IUT adds all FBlocks associated with the LT post-IUT that the IUT accepts to the central registry. Additionally, the IUT adds all FBlocks associated with the LT pre-IUT (based on CTC\_2.6.2-4b) to the central registry. The UT, using the LT post-IUT, shall send FBlockIDs.Status (FBlockIDList). The FBlockIDList shall contain all FBlocks provided before and one new FBlock. If the IUT tries to change the InstID of an FBlock associated with the LT pre-IUT or the LT post-IUT, the UT shall execute that request according to ISO 21806-2.

- d The central registry shall contain all FBlocks associated with the LT pre-IUT and the LT post-IUT that have been registered successfully. It shall not contain FBlocks associated with the LT pre-IUT or the LT post-IUT that are not indicated by the IUT with `Configuration.Status(NewExt, DeltaFBlockList)`.  
 Exception: The first FBlock provided by the UT, using the LT pre-IUT, during the initial scan is not announced with `Configuration.Status(NewExt, DeltaFBlockList)` but shall be stored in the central registry.

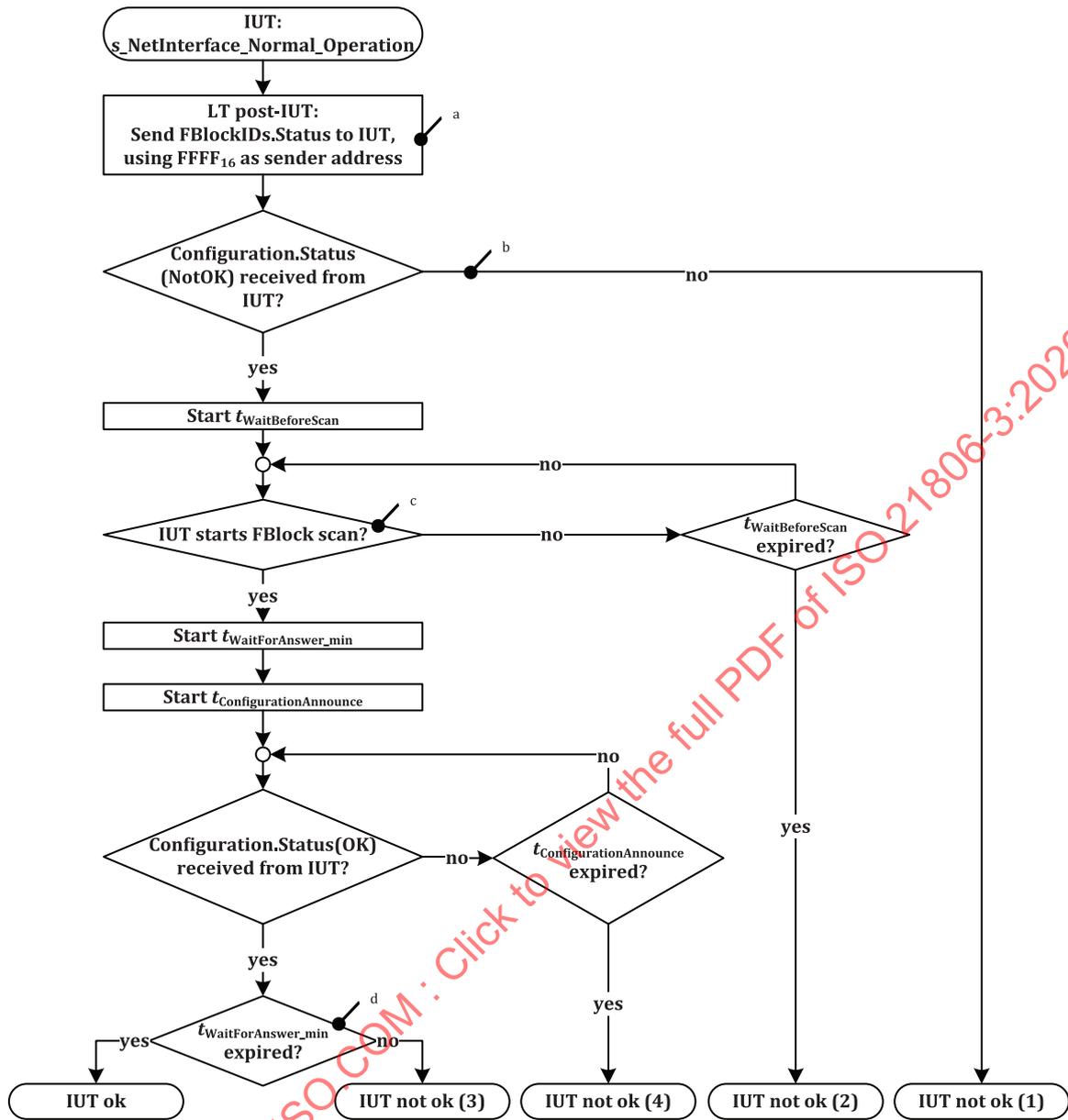
**Figure 16 — CTC\_2.6.2-4c - Device integration test (c)**

**8.4.1.7 CTC\_2.6.2-5 – Config(OK) delay test**

Table 23 specifies the CTC\_2.6.2-5 – Config(OK) delay test.

**Table 23 — CTC\_2.6.2-5 – Config(OK) delay test**

| Item                     | Content  |
|--------------------------|--|
| <b>CTC # - Title</b>     | CTC_2.6.2-5 – Config(OK) delay test  |
| <b>Purpose</b>           | This CTC verifies that the NetworkMaster, after sending <code>Configuration.Status(NotOK)</code> , starts an FBlock scan before $t_{\text{WaitBeforeScan}}$ expires.<br>This CTC also verifies that the NetworkMaster does not send <code>Configuration.Status(OK)</code> before $t_{\text{WaitForAnswer}}$ expires.<br>This CTC applies to all MOST devices that contain the NetworkMaster.   |
| <b>Reference</b>         | ISO 21806-2:2020:<br>— 6.8.3.3.3 APP – Setting the central registry state to NotOK;<br>— 6.8.3.6.6 APP – Non-responding NetworkSlaves.   |
| <b>Prerequisite</b>      | The UT shall effectuate <code>s_NetInterface_Normal_Operation</code> in the node that contains the IUT.  |
| <b>Set-up</b>            | — The LT pre-IUT shall be a TimingSlave.<br>— The LT post-IUT shall be a TimingSlave.  |
| <b>Step</b>              | 1. The UT shall execute the sequence as specified in <a href="#">Figure 17</a> .   |
| <b>Iteration</b>         | Not applicable   |
| <b>Expected response</b> | Step 1: IUT ok: the IUT respects the timing constraints for announcing the central registry.<br>Step 1: IUT not ok (1): the IUT does not send <code>Configuration.Status(NotOK)</code> .<br>Step 1: IUT not ok (2): the IUT starts FBlock scan too late.<br>Step 1: IUT not ok (3): the IUT sends <code>Configuration.Status(OK)</code> too early.<br>Step 1: IUT not ok (4): the IUT does not send <code>Configuration.Status(OK)</code> in time. |
| <b>Remark</b>            | ---  |



- a The UT, using the LT post-IUT, shall send FBlockIDs.Status to the IUT, using sender address FFFF<sub>16</sub>.
- b The IUT sends Configuration.Status (NotOK). The UT shall stop the CTC if the IUT does not send Configuration.Status (NotOK) within t<sub>DeadLockShort</sub>.
- c The IUT starts an FBlock scan before t<sub>WaitBeforeScan</sub> expires. During this FBlock scan, the UT shall not respond to requests from the IUT to the LT post-IUT.
- d The IUT does not send Configuration.Status (OK) before t<sub>WaitForAnswer\_min</sub> expires.

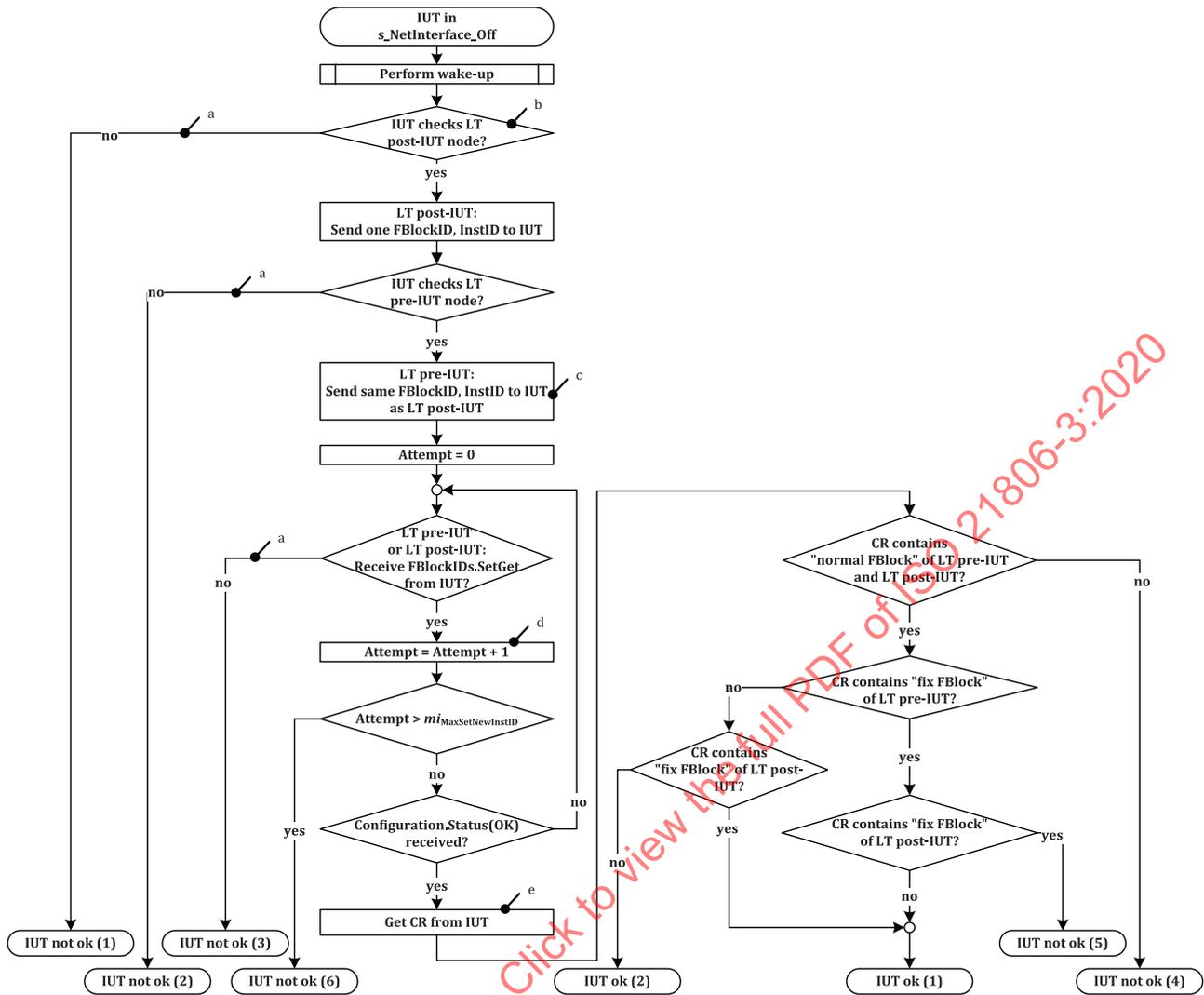
Figure 17 — CTC\_2.6.2-5 - Config(OK) delay test

8.4.1.8 CTC\_2.6.2-6 - Double FBlock test

Table 24 specifies the CTC\_2.6.2-6 - Double FBlock test.

Table 24 — CTC\_2.6.2-6 – Double FBlock test

| Item                     | Content  |
|--------------------------|--|
| <b>CTC # - Title</b>     | CTC_2.6.2-6 – Double FBlock test   |
| <b>Purpose</b>           | This CTC verifies that the NetworkMaster detects duplicate <code>InstIDs</code> , attempts to correct those, and, if not successful, does not include the affected FBlocks in the central registry.<br>This CTC applies to all MOST devices that contain the NetworkMaster.  |
| <b>Reference</b>         | ISO 21806-2:2020, 6.8.3.8 APP – Invalid announcements — <code>InstID</code>  |
| <b>Prerequisite</b>      | <ul style="list-style-type: none"> <li>— The UT shall effectuate <code>s_NetInterface_Off</code> in the node that contains the IUT.</li> <li>— The ring shall be closed.</li> </ul>  |
| <b>Set-up</b>            | <ul style="list-style-type: none"> <li>— If the IUT is part of a MOST device that contains the TimingMaster, the LT pre-IUT shall be a TimingSlave.</li> <li>— If the IUT is part of a MOST device that does not contain the TimingMaster, the LT pre-IUT shall be the TimingMaster.</li> <li>— The LT post-IUT shall be a TimingSlave.</li> </ul>   |
| <b>Step</b>              | 1. The UT shall execute the sequence as specified in <a href="#">Figure 18</a> .   |
| <b>Iteration</b>         | Not applicable   |
| <b>Expected response</b> | <p>Step 1: IUT ok (1): the IUT handles duplicate <code>InstIDs</code> correctly.</p> <p>Step 1: IUT ok (2): “fix FBlock” neither of the LT pre-IUT nor of the LT post-IUT stored in the central registry of the IUT.</p> <p>Step 1: IUT not ok (1): the IUT does not check the LT post-IUT.</p> <p>Step 1: IUT not ok (2): the IUT does not check the LT pre-IUT.</p> <p>Step 1: IUT not ok (3): the IUT does not change <code>FBlockID</code> of the LT pre-IUT or the LT post-IUT.</p> <p>Step 1: IUT not ok (4): the IUT does not store “normal FBlock” into the central registry.</p> <p>Step 1: IUT not ok (5): the IUT stores “fix FBlocks” of the LT pre-IUT and the LT post-IUT into the central registry.</p> <p>Step 1: IUT not ok (6): the IUT tries to change <code>FBlockIDs</code> of the LT pre-IUT or the LT post-IUT too often.</p> |
| <b>Remark</b>            | <ol style="list-style-type: none"> <li>1. During the FBlock scan, the IUT might start checking the LT pre-IUT before the LT post-IUT responds.</li> <li>2. The UT assigns 2 FBlocks to the LT pre-IUT and 2 FBlocks to the LT post-IUT. One with normal behaviour (named “normal FBlock”) that changes its <code>InstID</code> if commanded by the IUT and one FBlock which does not change its <code>InstID</code> if commanded (named “fix FBlock”).</li> <li>3. For the “fix FBlocks”, the UT uses the same <code>FBlockID</code> and <code>InstID</code> for the LT pre-IUT and the LT post-IUT. For the “normal FBlock” the <code>FBlockIDs</code> and <code>InstIDs</code> differ.</li> </ol>  |



- a The UT shall stop the CTC if the IUT does not send FBlockIDs. Get within  $t_{DeadLockShort}$
- b After wake-up, the IUT performs an FBlock scan. It scans the MOST devices in correct order. Check of the LT post-IUT and the LT pre-IUT can be overlapped. The IUT can also start with checking the LT pre-IUT. The UT, using the LT pre-IUT and the LT post-IUT, shall send the same FBlockID, InstID to the IUT.
- c The UT, using the LT pre-IUT and the LT post-IUT, answers to requests to the associated NetBlocks without changing their FBlock configuration.
- d The IUT detects the identical FBlocks and tries to change the InstID of the FBlock associated with the LT pre-IUT or the LT post-IUT. The UT shall not execute the requested InstID change.
- e As soon as the IUT sends Configuration.Status(OK), the UT shall request the central registry. The IUT provides a central registry that contains the FBlocks associated with the LT pre-IUT and the LT post-IUT. It does not contain both "fix FBlocks".

Figure 18 — CTC\_2.6.2-6 - Double FBlock test

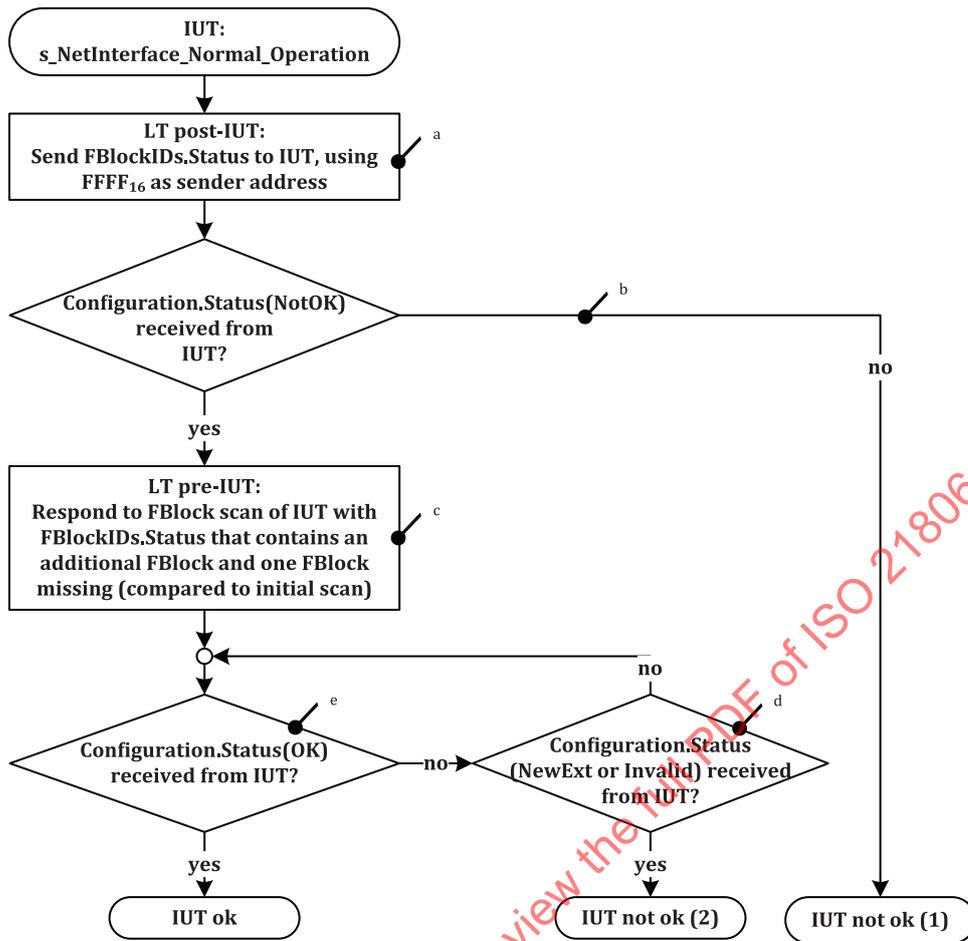
8.4.1.9 CTC\_2.6.2-7 - Config(New) order test

Table 25 specifies the CTC\_2.6.2-7 - Config(New) order test.

Table 25 — CTC\_2.6.2-7 - Config(New) order test

| Item                     | Content  |
|--------------------------|--|
| <b>CTC # - Title</b>     | CTC_2.6.2-7 - Config(New) order test   |
| <b>Purpose</b>           | This CTC verifies that the NetworkMaster sends Configuration.Status (NotOK) when encountering uninitialised node addresses and does not announce FBlocks with NewExt, New, or Invalid before sending an initial Configuration.Status (OK).<br>This CTC applies to all MOST devices that contain the NetworkMaster. |
| <b>Reference</b>         | ISO 21806-2:2020, 6.8.3.3 APP – Setting the central registry state   |
| <b>Prerequisite</b>      | The UT shall effectuate s_NetInterface_Normal_Operation in the node that contains the IUT.   |
| <b>Set-up</b>            | — The LT pre-IUT shall be a TimingSlave.<br>— The LT post-IUT shall be a TimingSlave.  |
| <b>Step</b>              | 1. The UT shall execute the sequence as specified in <a href="#">Figure 19</a> .   |
| <b>Iteration</b>         | Not applicable   |
| <b>Expected response</b> | Step 1: IUT ok: the IUT reports central registry updates in the correct order.<br>Step 1: IUT not ok (1): the IUT does not send Configuration.Status (NotOK).<br>Step 1: IUT not ok (2): the IUT sends Configuration.Status (NewExt) or Configuration.Status (Invalid) before sending Configuration.Status (OK).   |
| <b>Remark</b>            | ---  |

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- a The UT, using the LT post-IUT, shall send `FBlockIDs.Status` to the IUT with sender address `FFFF16`.
- b The IUT sends `Configuration.Status(NotOK)`. The UT shall stop the CTC if the IUT does not send `Configuration.Status(NotOK)` within  $t_{DeadLockShort}$ .
- c The UT, using the LT pre-IUT, shall respond to FBlock scans within the period of time that the IUT waits for the answer of the LT post-IUT. The UT shall not respond to requests from the IUT to the LT post-IUT during the FBlock scan. During the FBlock scan, the UT, using the LT pre-IUT, shall indicate an additional FBlock and one missing FBlock (compared to the initial FBlock scan).
- d The IUT does not broadcast `Configuration.Status(NewExt/New/Invalid)`. The UT shall stop the CTC if the IUT does not perform FBlock scan within  $t_{DeadLockMid}$ .
- e The IUT broadcasts `Configuration.Status(OK)`.

Figure 19 — CTC\_2.6.2-7 - Config(New) order test

8.4.1.10 CTC\_2.6.3-1 - FBlock status change detection test

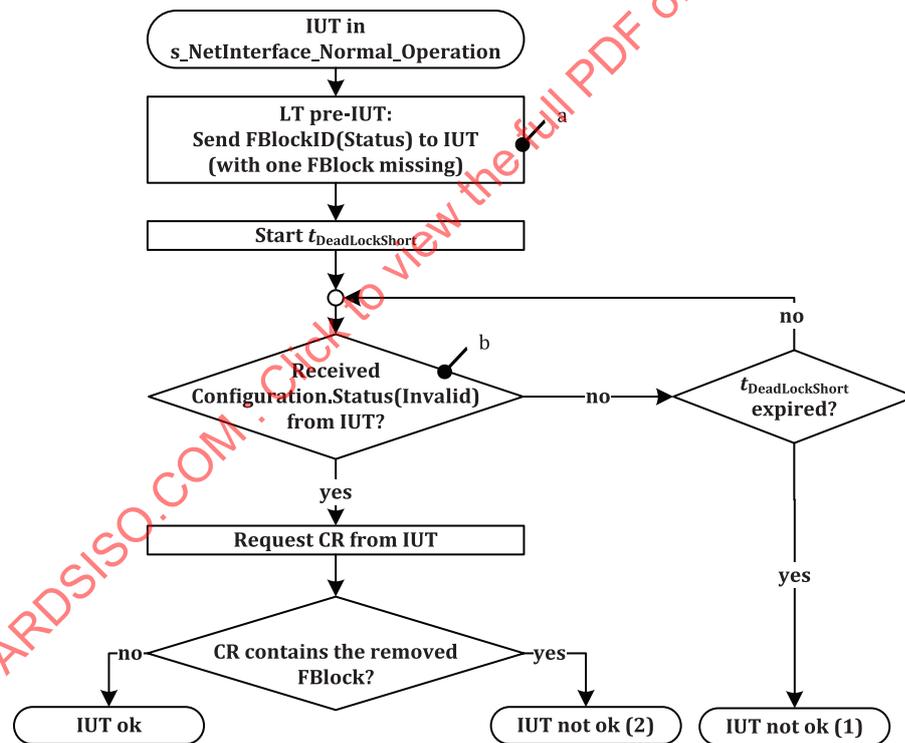
Table 26 specifies the CTC\_2.6.3-1 - FBlock status change detection test.

Table 26 — CTC\_2.6.3-1 - FBlock status change detection test

| Item          | Content  |
|---------------|--|
| CTC # - Title | CTC_2.6.3-1 - FBlock status change detection test  |
| Purpose       | This CTC verifies that the NetworkMaster detects removed FBlocks in the FBlock list and reacts by sending <code>Configuration.Status(Invalid)</code> .<br>This CTC applies to all MOST devices that contain the NetworkMaster. |

Table 26 (continued)

| Item              | Content  |
|-------------------|--|
| Reference         | ISO 21806-2:2020, 6.8.3.9.2 APP – Disappearing FBlocks in central registry state OK  |
| Prerequisite      | <ul style="list-style-type: none"> <li>— The UT shall effectuate <code>s_NetInterface_Normal_Operation</code> in the node that contains the IUT.</li> <li>— The ring shall be closed.</li> </ul>   |
| Set-up            | <ul style="list-style-type: none"> <li>— The LT pre-IUT shall be a TimingSlave with valid address.</li> <li>— The LT post-IUT shall be a listen-only node.</li> </ul>  |
| Step              | 1. The UT shall execute the sequence as specified in <a href="#">Figure 20</a> .   |
| Iteration         | Not applicable   |
| Expected response | Step 1: IUT ok: the IUT correctly handles removed FBlocks.<br>Step 1: IUT not ok (1): the IUT does not broadcast <code>Configuration.Status(Invalid)</code> .<br>Step 1: IUT not ok (2): the IUT does not delete unavailable FBlock from central registry. |
| Remark            | ---  |



- a The UT, using the LT pre-IUT, shall remove an FBlock from the central registry and indicate it to the IUT.
- b The IUT broadcasts `Configuration.Status(Invalid)` and deletes the FBlock from the central registry.

Figure 20 — CTC\_2.6.3-1 - FBlock status change detection test

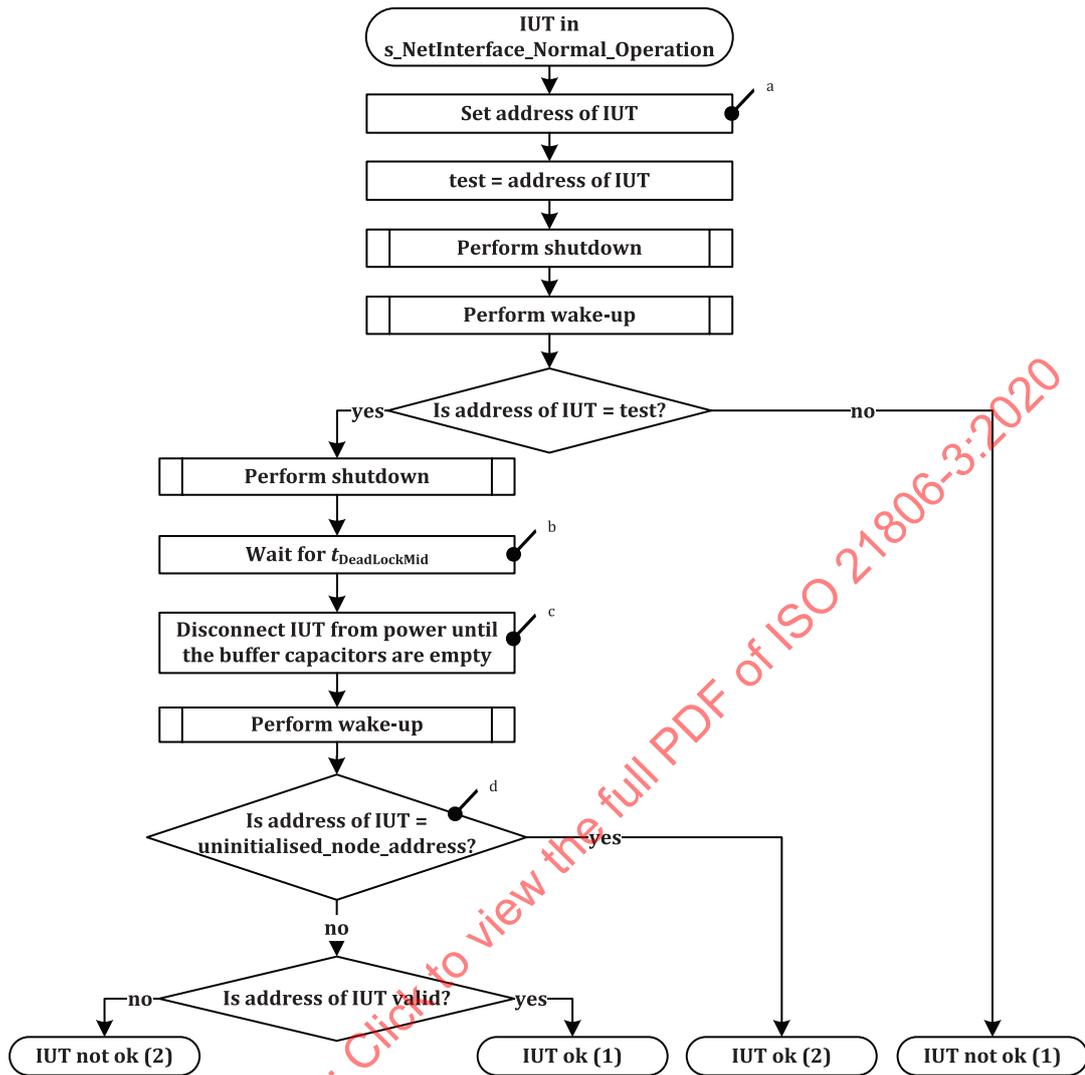
8.4.2 Central registry handling test (NetworkSlave)

8.4.2.1 CTC\_2.6.4-1 - Address initialisation test

[Table 27](#) specifies the CTC\_2.6.4-1 - Address initialisation test.

Table 27 — CTC\_2.6.4-1 – Address initialisation test

| Item                     | Content  |
|--------------------------|--|
| <b>CTC # – Title</b>     | CTC_2.6.4-1 – Address initialisation test  |
| <b>Purpose</b>           | <p>This CTC verifies that a NetworkSlave sets its logical node by request of the NetworkMaster and retains it until the NetworkSlave is disconnected from power.</p> <p>Afterwards, it is verified that the NetworkSlave has a valid node address or an uninitialised node address.</p> <p>This CTC applies to all MOST devices that do not contain the NetworkMaster.</p>                                       |
| <b>Reference</b>         | ISO 21806-2:2020, 6.8.4.3 APP – Specific behaviour after ev_Init_Ready   |
| <b>Prerequisite</b>      | <ul style="list-style-type: none"> <li>— The UT shall effectuate <code>s_NetInterface_Normal_Operation</code> in the node that contains the IUT.</li> <li>— The ring shall be closed.</li> </ul>   |
| <b>Set-up</b>            | <ul style="list-style-type: none"> <li>— The LT pre-IUT shall be the TimingMaster.</li> <li>— The LT post-IUT shall be a listen-only node.</li> </ul>  |
| <b>Step</b>              | 1. The UT shall execute the sequence as specified in <a href="#">Figure 21</a> .   |
| <b>Iteration</b>         | Not applicable   |
| <b>Expected response</b> | <p>Step 1: IUT ok (1): the IUT uses a valid node address.</p> <p>Step 1: IUT ok (2): the IUT uses the <code>uninitialised_node_address</code> before deriving a new address from the node position.</p> <p>Step 1: IUT not ok (1): the IUT changes address between two operation cycles.</p> <p>Step 1: IUT not ok (2): the IUT neither uses <code>uninitialised_node_address</code> nor valid node address.</p> |
| <b>Remark</b>            | After startup, an address other than a valid node address (in static or dynamic address range) or <code>uninitialised_node_address</code> is not permitted for the IUT.  |



- a The UT, using the LT pre-IUT, shall set the address of the IUT to a value that is different from the default address of the node. It shall be a valid address. The case that the address cannot be set is tolerated. The UT shall store the address of the IUT.
- b The node that contains the IUT is able to store its state into non-volatile memory before switching off the power supply.  
If  $t_{DeadLockMid}$  is not sufficient, the manufacturer shall provide  $t_{PwrSwitchOffDelay\_max}$ .
- c Disconnect the IUT for a minimum of one minute from power.
- d After shutdown, disconnecting from power (for at least 1 minute) and wake-up, the IUT uses a valid node address (in static or dynamic address range) or `uninitialised_node_address`.

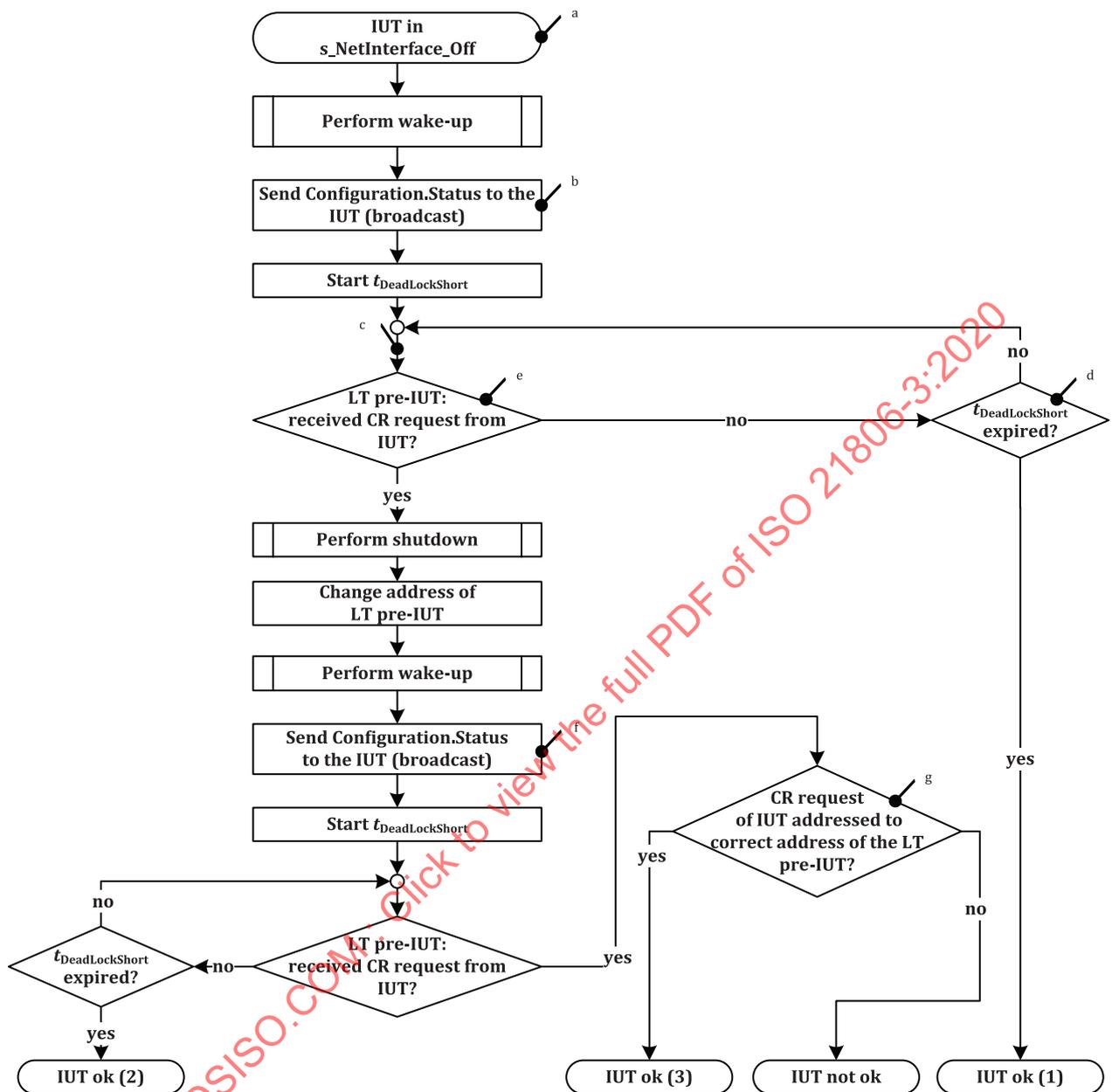
Figure 21 — CTC\_2.6.4-1 - Address initialisation test

8.4.2.2 CTC\_2.6.4-3 - NetworkMaster address storage test

Table 28 specifies the CTC\_2.6.4-3 - NetworkMaster address storage test.

**Table 28 — CTC\_2.6.4-3 - NetworkMaster address storage test**

| Item                     | Content  |
|--------------------------|--|
| <b>CTC # - Title</b>     | CTC_2.6.4-3 - NetworkMaster address storage test   |
| <b>Purpose</b>           | <p>This CTC verifies that a NetworkSlave correctly derives the address of the NetworkMaster from <code>Configuration.Status</code> messages.</p> <p>This CTC applies to all MOST devices that contain a NetworkSlave that communicates with the NetworkMaster during <code>s_NetInterface_Normal_Operation</code>.</p>   |
| <b>Reference</b>         | ISO 21806-2:2020, 6.8.4.3.4 APP - Deriving the logical node address of the NetworkMaster   |
| <b>Prerequisite</b>      | <ul style="list-style-type: none"> <li>— The UT shall effectuate <code>s_NetInterface_Off</code> in the node that contains the IUT.</li> <li>— The ring shall be closed.</li> </ul>  |
| <b>Set-up</b>            | <ul style="list-style-type: none"> <li>— If the IUT is part of a MOST device that contains the TimingMaster, the LT pre-IUT shall be a TimingSlave.</li> <li>— If the IUT is part of a MOST device that does not contain the TimingMaster, the LT pre-IUT shall be the TimingMaster.</li> <li>— The LT post-IUT shall be a listen-only node.</li> </ul>        |
| <b>Step</b>              | 1. The UT shall execute the sequence as specified in <a href="#">Figure 22</a> .   |
| <b>Iteration</b>         | <p>The CTC shall be performed three times, using</p> <ul style="list-style-type: none"> <li>— <code>Configuration.Status (OK)</code>,</li> <li>— <code>Configuration.Status (NewExt)</code> with empty FBlock list, and</li> <li>— <code>Configuration.Status (Invalid)</code>.</li> </ul>   |
| <b>Expected response</b> | <p>Step 1: IUT ok (1): the IUT never requests the central registry.</p> <p>Step 1: IUT ok (2): the IUT does not request the central registry after shutdown and subsequent wake-up.</p> <p>Step 1: IUT ok (3): the IUT uses the correct address for the NetworkMaster.</p> <p>Step 1: IUT not ok: the IUT uses an incorrect address for the NetworkMaster.</p> |
| <b>Remark</b>            | In the case of <code>Invalid</code> , the <code>FBlockID C8<sub>16</sub>, InstID 01<sub>16</sub></code> is used.   |



- a The UT shall perform a wake-up. Between test loops, switch off the MOST output without sending Shutdown. Start(Execute).
- b After the node that contains the IUT is woken up, the UT, using the LT pre-IUT, shall broadcast Configuration.Status. The IUT derives the address of the LT pre-IUT. The CTC shall be performed three times:
  - with sending Configuration.Status (OK);
  - with sending Configuration.Status (NewExt) with empty FBlock list;
  - with sending Configuration.Status (Invalid) with FBlock not implemented in the IUT (use FBlockID C8<sub>16</sub>, InstID 01<sub>16</sub>).
- c The UT, using the LT pre-IUT, shall trigger the IUT to send any message to the NetworkMaster (LT pre-IUT) to check whether the IUT has derived the address correctly. The IUT requests the central registry from the NetworkMaster automatically.
- d If the IUT does not request the central registry within  $t_{DeadLockShort}$  this function is not supported by the IUT.

- e The request is addressed to the correct logical address of the LT pre-IUT. After shutdown, the UT shall change the address of the LT pre-IUT.
- f Send same Configuration.Status as above.
- g The IUT responds to the new address of the LT pre-IUT.

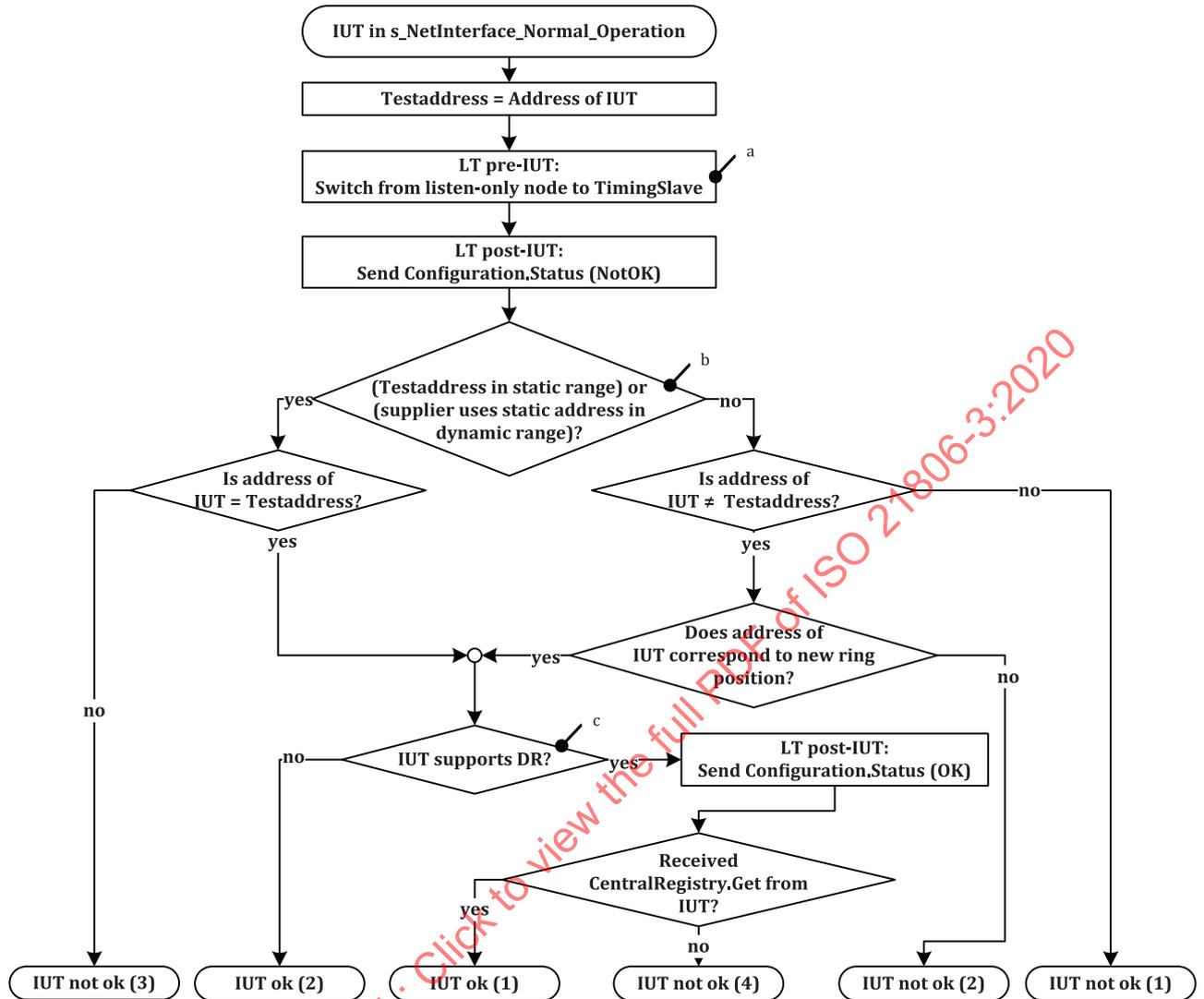
Figure 22 — CTC\_2.6.4-3 - NetworkMaster address storage test

8.4.2.3 CTC\_2.6.4-4 - Address reinitialisation test

Table 29 specifies the CTC\_2.6.4-4 - Address reinitialisation test.

Table 29 — CTC\_2.6.4-4 - Address reinitialisation test

| Item                     | Content  |
|--------------------------|--|
| <b>CTC # - Title</b>     | CTC_2.6.4-4 - Address reinitialisation test  |
| <b>Purpose</b>           | This CTC verifies that a NetworkSlave is capable of changing its logical node address after an NCE and subsequently rebuilding its decentral registry.<br>This CTC applies to all MOST devices that do not contain the NetworkMaster.  |
| <b>Reference</b>         | ISO 21806-2:2020:<br>— 6.8.2.2 APP - Central registry state NotOK;<br>— 7.2.2 AL - 16-bit addressing.  |
| <b>Prerequisite</b>      | — The UT shall effectuate s_NetInterface_Normal_Operation in the node that contains the IUT.<br>— The ring shall be closed.  |
| <b>Set-up</b>            | — The LT pre-IUT shall be a listen-only node.<br>— The LT post-IUT shall be the TimingMaster.  |
| <b>Step</b>              | 1. The UT shall execute the sequence as specified in Figure 23.  |
| <b>Iteration</b>         | Not applicable   |
| <b>Expected response</b> | Step 1: IUT ok (1): the IUT builds its decentral registry.<br>Step 1: IUT ok (2): the IUT does not support a decentral registry.<br>Step 1: IUT not ok (1): the IUT (dynamic address) does not change its address after receiving Configuration.Status (NotOK).<br>Step 1: IUT not ok (2): the IUT (dynamic address) does not derive new address from ring position.<br>Step 1: IUT not ok (3): the IUT (not dynamic address) does not use previous address.<br>Step 1: IUT not ok (4): the IUT does not delete/rebuild decentral registry (if supported). |
| <b>Remark</b>            | --   |



- a During runtime, the UT shall change the position of the node that contains the IUT (by connecting the previously disconnected MOST node in the LT pre-IUT to the network as TimingSlave). The UT, using the LT post-IUT, shall send Configuration.Status (NotOK).
- b If the address of the IUT is in the dynamic address range, it derives a new address from its ring position, else it continues using its previous address. Dynamic address range:  $0100_{16} - 013F_{16}$ .
- c If the IUT does not request the central registry within  $t_{DeadLockShort}$  after Configuration.Status (OK) (during network startup), the IUT does not support the decentral registry. If the IUT implements a decentral registry, the UT, using the LT post-IUT, shall send Configuration.Status (OK). The IUT sends CentralRegistry (Get) to the LT post-IUT to rebuild its decentral registry.

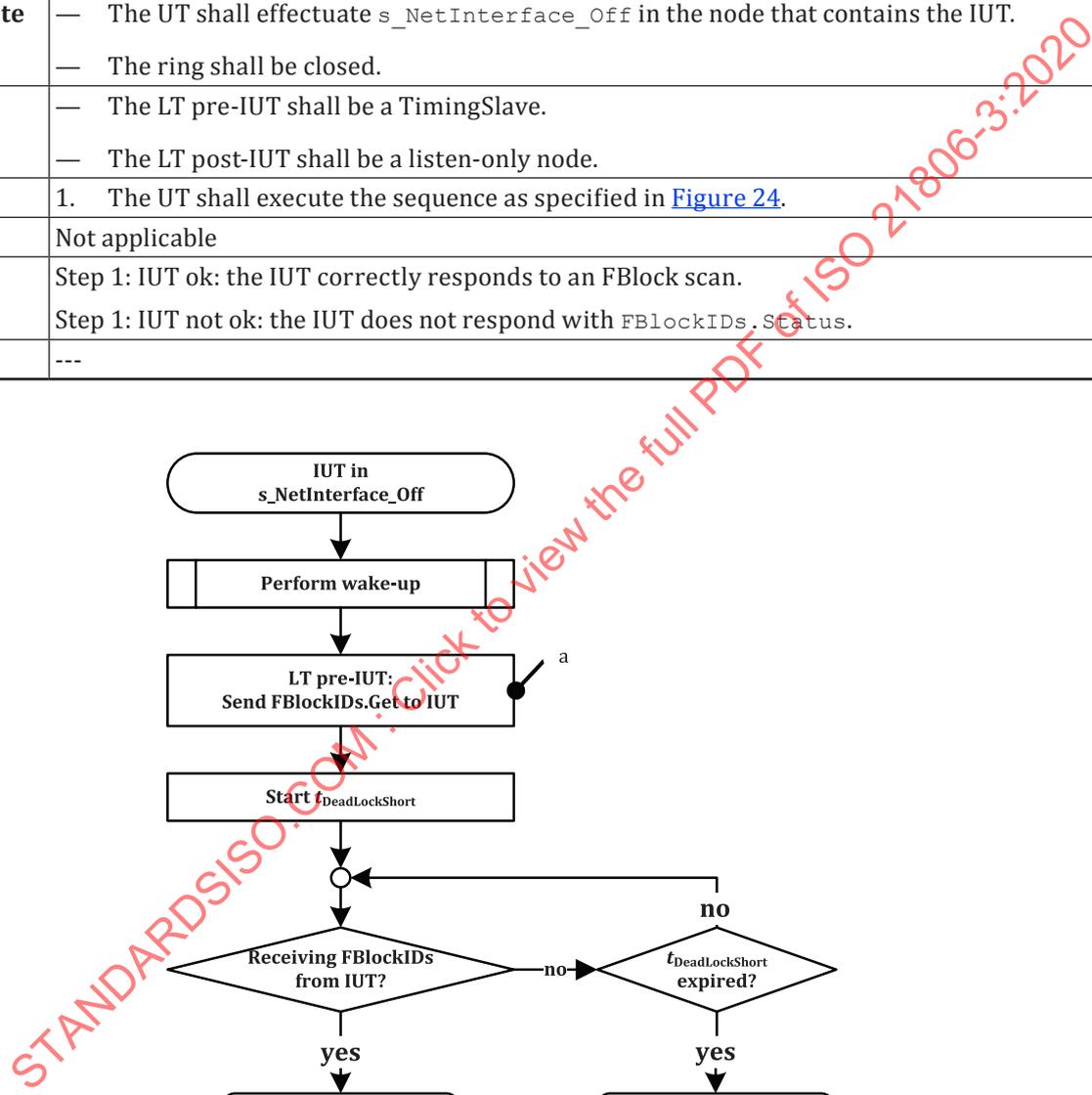
Figure 23 — CTC\_2.6.4-4 - Address reinitialisation test

8.4.2.4 CTC\_2.6.4-8 - FBlock response test

Table 30 specifies the CTC\_2.6.4-8 - FBlock response test.

Table 30 — CTC\_2.6.4-8 - FBlock response test

| Item              | Content  |
|-------------------|--|
| CTC # - Title     | CTC_2.6.4-8 - FBlock response test   |
| Purpose           | This CTC verifies that a NetworkSlave correctly responds to an FBlock scan.<br>This CTC applies to all MOST devices that do not contain the NetworkMaster. |
| Reference         | ISO 21806-2:2020, 6.8.4.4.3 APP - Configuration requests and configuration changes   |
| Prerequisite      | — The UT shall effectuate <code>s_NetInterface_Off</code> in the node that contains the IUT.<br>— The ring shall be closed.                                |
| Set-up            | — The LT pre-IUT shall be a TimingSlave.<br>— The LT post-IUT shall be a listen-only node.   |
| Step              | 1. The UT shall execute the sequence as specified in <a href="#">Figure 24</a> .   |
| Iteration         | Not applicable   |
| Expected response | Step 1: IUT ok: the IUT correctly responds to an FBlock scan.<br>Step 1: IUT not ok: the IUT does not respond with <code>FBlockIDs.Status</code> .         |
| Remark            | ---  |



<sup>a</sup> After wake-up, the UT, using the LT pre-IUT, shall send `FBlockIDs.Get` to the IUT after the IUT is in the ring (check NCE; timeout 5 s) and then wait `t_WaitBeforeScan`. The IUT responds with `FBlockIDs.Status`.

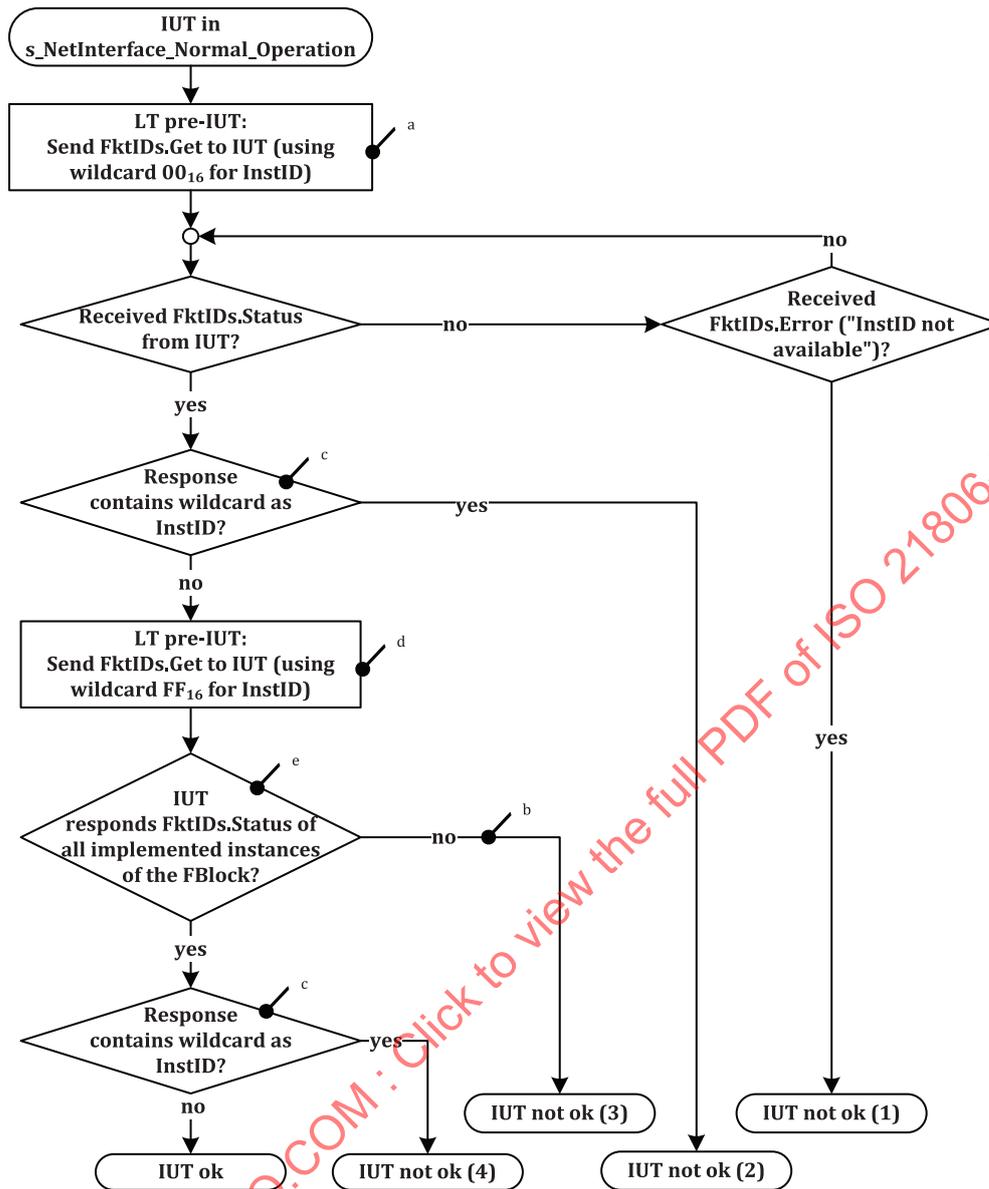
Figure 24 — CTC\_2.6.4-8 - FBlock response test

8.4.3 CTC\_2.6.4-10 - InstID wildcard test

Table 31 specifies the CTC\_2.6.4-10 - InstID wildcard test.

Table 31 — CTC\_2.6.4-10 – InstID wildcard test

| Item                     | Content   |
|--------------------------|---|
| <b>CTC # - Title</b>     | CTC_2.6.4-10 – InstID wildcard test   |
| <b>Purpose</b>           | This CTC verifies that a node correctly recognizes InstID wildcards and responds accordingly.<br>This CTC applies to all MOST devices.  |
| <b>Reference</b>         | ISO 21806-2:2020, 7.4.7 AL – Wildcard values for InstID   |
| <b>Prerequisite</b>      | The UT shall effectuate <code>s_NetInterface_Normal_Operation</code> in the node that contains the IUT.   |
| <b>Set-up</b>            | <ul style="list-style-type: none"> <li>— If the IUT is part of a MOST device that contains the TimingMaster, the LT pre-IUT shall be a TimingSlave.</li> <li>— If the IUT is part of a MOST device that does not contain the TimingMaster, the LT pre-IUT shall be the TimingMaster.</li> <li>— The LT post-IUT shall be a listen-only node.</li> </ul>   |
| <b>Step</b>              | 1. The UT shall execute the sequence as specified in <a href="#">Figure 25</a> .  |
| <b>Iteration</b>         | The CTC shall be performed with every single registered FBlock of the IUT.  |
| <b>Expected response</b> | <p>Step 1: IUT ok: the IUT correctly handles InstID wildcards.</p> <p>Step 1: IUT not ok (1): the IUT responds with error “InstID not available”.</p> <p>Step 1: IUT not ok (2): the IUT uses wildcard for responding <code>FktIDs.Status</code>.</p> <p>Step 1: IUT not ok (3): the IUT does not respond with <code>FktIDs.Status</code> of all implemented instances of the current FBlock.</p> <p>Step 1: IUT not ok (4): the IUT uses wildcard for responding <code>FktIDs.Status</code>.</p> |
| <b>Remark</b>            | The UT sends <code>FBlockIDs.Get</code> with <code>InstID = 00<sub>16</sub></code> to request a list of all implemented instances of an FBlock from the IUT.  |



- a The UT, using the LT pre-IUT, shall send `FktIDs.Get` to the IUT, using wildcard `0016` for the `InstID`. The IUT responds with `FktIDs.Status`. The response of the IUT does not contain any wildcard as `InstID`.
- b The UT shall stop the CTC if the IUT does not respond within  $t_{DeadLockShort}$  or responds with an error.
- c If the IUT is the NetworkMaster:
  - the IUT responds with its own `InstID`;
  - if this `InstID` is `0016`, the IUT is allowed to use `0016`.
- d The UT, using the LT pre-IUT, shall send `FktIDs.Get` to the IUT, using wildcard `FF16` for the `InstID`.
- e The IUT responds with `FktIDs.Status` and reports all implemented instances of the current FBlock. The response of the IUT does not contain any wildcard as `InstID`. Implemented instances mean: All instances, indicated by the IUT via `FBlockIDs.Get`

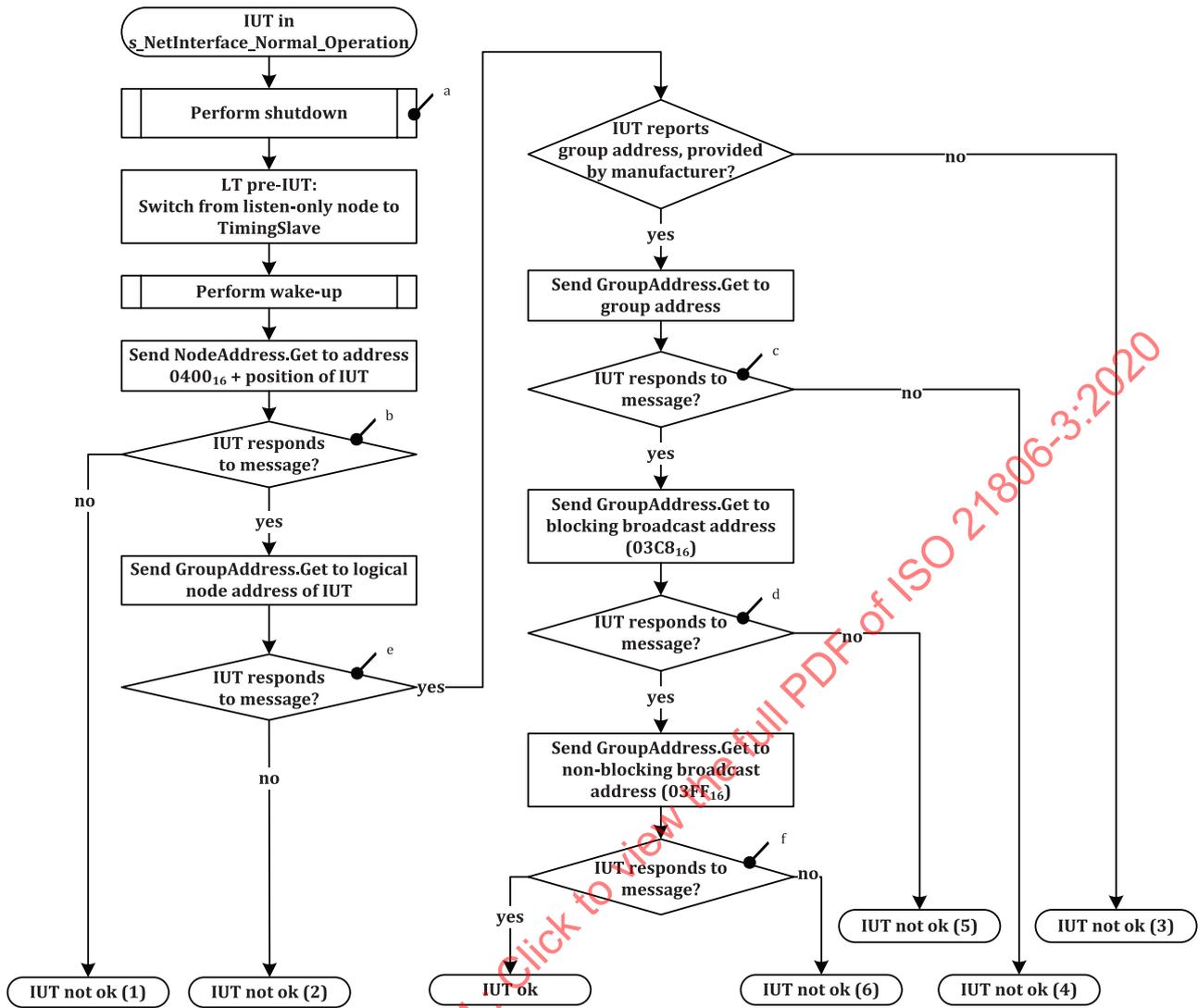
Figure 25 — CTC\_2.6.4-10 - InstID wildcard test

### 8.5 CTC\_2.7-1 - Node addressing test

Table 32 specifies the CTC\_2.7-1 - Node addressing test.

Table 32 — CTC\_2.7-1 – Node addressing test

| Item                     | Content   |
|--------------------------|---|
| <b>CTC # - Title</b>     | CTC_2.7-1 – Node addressing test  |
| <b>Purpose</b>           | This CTC verifies that a NetworkSlave correctly sets different address types and reacts to messages that are sent to addresses it is associated with.<br>This CTC applies to all MOST devices that do not contain the NetworkMaster.  |
| <b>Reference</b>         | ISO 21806-2:2020, 7.2.2 AL – 16-bit addressing  |
| <b>Prerequisite</b>      | <ul style="list-style-type: none"> <li>— The UT shall effectuate <code>s_NetInterface_Normal_Operation</code> in the node that contains the IUT.</li> <li>— The ring shall be closed.</li> </ul>  |
| <b>Set-up</b>            | <ul style="list-style-type: none"> <li>— Depending on the test step, the LT pre-IUT shall be a listen-only node or a TimingSlave.</li> <li>— The LT post-IUT shall be the TimingMaster.</li> </ul>  |
| <b>Step</b>              | 1. The UT shall execute the sequence as specified in <a href="#">Figure 26</a> .  |
| <b>Iteration</b>         | Not applicable  |
| <b>Expected response</b> | <p>Step 1: IUT ok: the IUT responds to messages to its associated addresses.</p> <p>Step 1: IUT not ok (1): the IUT does not respond to “node position addressing”.</p> <p>Step 1: IUT not ok (2): the IUT does not respond to “logical node addressing”.</p> <p>Step 1: IUT not ok (3): the IUT does not report the correct group address (differs from manufacturer information).</p> <p>Step 1: IUT not ok (4): the IUT does not respond to “group addressing”.</p> <p>Step 1: IUT not ok (5): the IUT does not respond to “blocking broadcast addressing”.</p> <p>Step 1: IUT not ok (6): the IUT does not respond to “non-broadcast addressing”.</p> |
| <b>Remark</b>            | ---   |



- a The UT shall shutdown the network and change the position of the node that contains the IUT. Then, the UT shall start up the network. The IUT derives the new node position address from its new node position.
- The UT shall send messages using the node position address, the logical node address, and the group address of the IUT, as well as the blocking broadcast address and the non-blocking broadcast address.
- b The IUT sends a valid status message. The sender's address and the property `NodeAddress` are correct.
- c The IUT sends a valid status message. The sender's address is correct.
- d The IUT sends a valid status message. The sender's address is correct.
- e The IUT sends a valid status message. The sender's address and the property `NodeAddress` are correct.
- f The IUT sends a valid status message. The sender's address is correct.

Figure 26 — CTC\_2.7-1 - Node addressing test

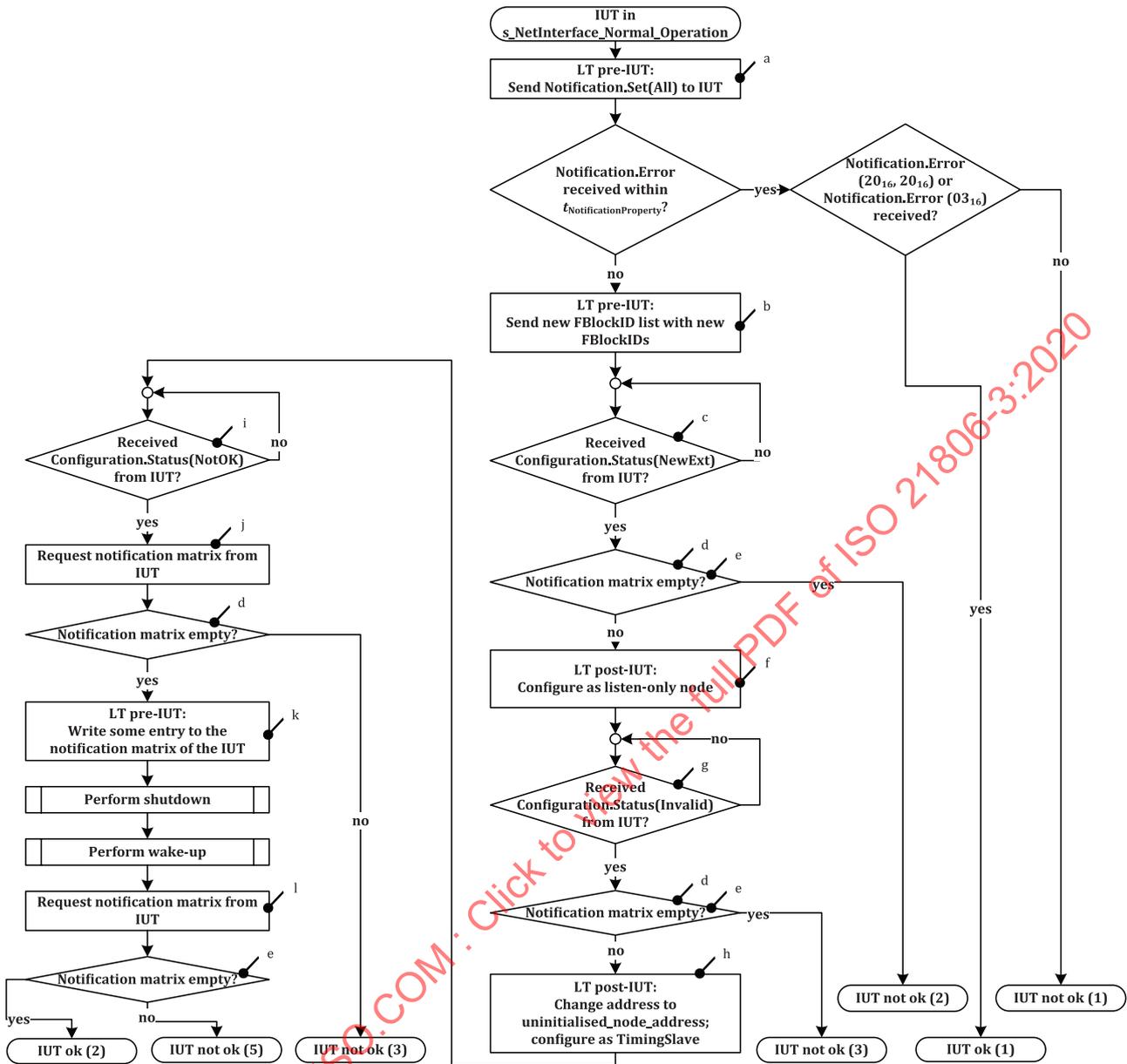
## 8.6 Notification matrix test

### 8.6.1 CTC\_2.8.3-1a - Notification matrix storage test (NetworkMaster)

Table 33 specifies the CTC\_2.8.3-1a - Notification matrix storage test (NetworkMaster).

Table 33 — CTC\_2.8.3-1a – Notification matrix storage test (NetworkMaster)

| Item                     | Content  |
|--------------------------|--|
| <b>CTC # - Title</b>     | CTC_2.8.3-1a – Notification matrix storage test (NetworkMaster)  |
| <b>Purpose</b>           | This CTC verifies that the NetworkMaster handles notifications correctly in conjunction with additions to and deletions from the central registry.<br>This CTC applies to all MOST devices that contain the NetworkMaster.   |
| <b>Reference</b>         | ISO 21806-2:2020, 6.6 APP – Notification mechanisms  |
| <b>Prerequisite</b>      | <ul style="list-style-type: none"> <li>— The UT shall effectuate <code>s_NetInterface_Normal_Operation</code> in the node that contains the IUT.</li> <li>— The ring shall be closed.</li> </ul>   |
| <b>Set-up</b>            | <ul style="list-style-type: none"> <li>— The LT pre-IUT shall be a TimingSlave.</li> <li>— The LT post-IUT is a listen-only node or TimingSlave, depending on the current test step.</li> </ul>  |
| <b>Step</b>              | 1. The UT shall execute the sequence as specified in <a href="#">Figure 27</a> .   |
| <b>Iteration</b>         | The CTC shall be performed with every FBlock and function of the IUT that implements notifications, registered in the central registry except FBlock PowerMaster. All FBlocks shall be tested within one test loop (no separate test loop for every FBlock) to increase stress of the IUT.   |
| <b>Expected response</b> | <p>Step 1: IUT ok (1): the IUT does not support notifications.</p> <p>Step 1: IUT ok (2): the IUT handles notifications correctly.</p> <p>Step 1: IUT not ok (1): the IUT does not store any entry into its notification matrix.</p> <p>Step 1: IUT not ok (2): the IUT deletes the notification matrix after <code>Configuration</code>.<br/><code>Status (NewExt)</code>.</p> <p>Step 1: IUT not ok (3): the IUT deletes the notification matrix after <code>Configuration</code>.<br/><code>Status (Invalid)</code>.</p> <p>Step 1: IUT not ok (4): the IUT does not delete the notification matrix after <code>Configuration</code>.<br/><code>Status (NotOK)</code>.</p> <p>Step 1: IUT not ok (5): the IUT does not delete the notification matrix after <code>ev_Init_Ready</code>.</p> |
| <b>Remark</b>            | <ol style="list-style-type: none"> <li>1. At the beginning of the CTC, the notification matrix of the IUT does not contain any entry of the UT.</li> <li>2. The CTC is based on generation of NCEs so the IUT performs an FBlock scan (check the FBlock configuration) on each NCE and the UT sets the FBlocks and address associated with the LT post-IUT accordingly.</li> </ol>   |



- a The UT, using the LT pre-IUT, shall add entries to the notification matrix of the IUT by sending `Notification.Set(All)`.  
If the IUT supports notification, it does not respond with a notification error.
- b The UT shall send new `FBlockID` lists with new `FBlockIDs` to the IUT.
- c The IUT sends `Configuration.Status(NewExt)`. It does not delete the entries from the notification matrix.
- d Detectable by requesting notification matrix from the IUT. Check only entries made by the UT.
- e Each notified function of each `FBlock` of the IUT is checked.
- f The LT post-IUT shall leave the ring by disconnecting its MOST node from the network and leaving only the listen-only node connected.
- g The IUT sends `Configuration.Status(Invalid)`. Its notification matrix is not empty. The UT shall stop the CTC if the IUT does not respond within  $t_{DeadLockShort}$ .
- h The LT post-IUT shall change the logical node address of its MOST node to `uninitialised_node_address` and connect it to the network again. The UT shall respond to queries from the IUT to the LT post-IUT.
- i The IUT broadcasts `Configuration.Status(NotOK)`.

- j The IUT broadcasts `Configuration.Status (OK)` and its notification matrix does not contain any entry of the UT. Wait for `Configuration.Status (OK)` before requesting the notification matrix.
- k The UT, using the LT pre-IUT, shall write some entry to the notification matrix of the IUT and shutdown the network.
- l Check only entries made by the UT. At the next wake-up, the notification matrix of the IUT does not contain any entry of the UT.

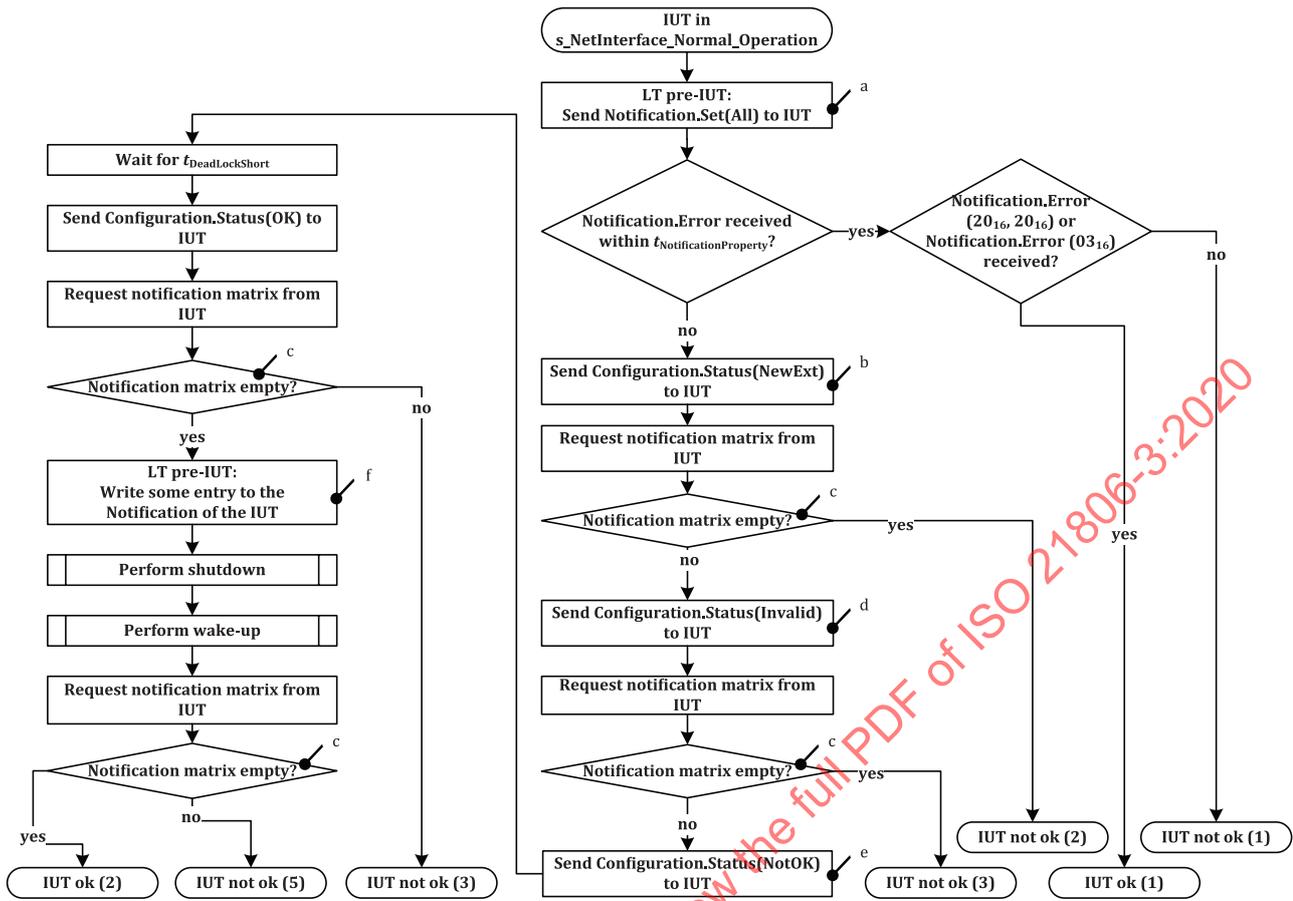
**Figure 27 — CTC\_2.8.3-1a - Notification matrix storage test (NetworkMaster)**

### 8.6.2 CTC\_2.8.3-1b – Notification matrix storage test (NetworkSlave)

Table 34 specifies the CTC\_2.8.3-1b – Notification matrix storage test (NetworkSlave).

**Table 34 — CTC\_2.8.3-1b - Notification matrix storage test (NetworkSlave)**

| Item                     | Content  |
|--------------------------|--|
| <b>CTC # - Title</b>     | CTC_2.8.3-1b – Notification matrix storage test (NetworkSlave)   |
| <b>Purpose</b>           | This CTC verifies that a NetworkSlave handles its notification matrix and corresponding requests correctly.<br>This CTC applies to all MOST devices that do not contain the NetworkMaster.   |
| <b>Reference</b>         | ISO 21806-2:2020, 6.6 APP – Notification mechanisms  |
| <b>Prerequisite</b>      | — The UT shall effectuate <code>s_NetInterface_Normal_Operation</code> in the node that contains the IUT.<br>— The ring shall be closed.   |
| <b>Set-up</b>            | — The LT pre-IUT shall be the TimingMaster.<br>— The LT post-IUT shall be a listen-only node.  |
| <b>Step</b>              | 1. The UT shall execute the sequence as specified in <a href="#">Figure 28</a> .   |
| <b>Iteration</b>         | The CTC shall be performed with every FBlock and function of the IUT, registered in the central registry except FBlock PowerMaster. All FBlocks shall be tested within one test loop (no separate test loop for every FBlock) to increase stress of the IUT.   |
| <b>Expected response</b> | Step 1: IUT ok (1): the IUT passes the CTC. It does not support notification.<br>Step 1: IUT ok (2): the IUT passes the CTC.<br>Step 1: IUT not ok (1): the IUT does not store any entry into its notification matrix.<br>Step 1: IUT not ok (2): the IUT deletes notification matrix if <code>Configuration.Status (NewExt)</code> is received.<br>Step 1: IUT not ok (3): the IUT deletes notification matrix if <code>Configuration.Status (Invalid)</code> is received.<br>Step 1: IUT not ok (4): the IUT does not delete the notification matrix if <code>Configuration.Status (NotOK)</code> is received.<br>Step 1: IUT not ok (5): the IUT does not delete the notification matrix after <code>ev_Init_Ready</code> . |
| <b>Remark</b>            | At the beginning of the CTC, the notification of the IUT does not contain any entry associated with the LT pre-IUT.  |



- a The UT, using the LT pre-IUT, shall request addition to the notification matrix of the IUT by sending Notification.Set(All). If the IUT supports notification, it does not generate a notification error.
- b The UT shall send Configuration.Status(NewExt). The IUT does not delete the entries from the notification matrix.
- c Check only entries associated with the LT pre-IUT. Each notified function of each FBlock of the IUT is checked.
- d The UT shall send Configuration.Status(Invalid). The node that is stored in the notification matrix (LT pre-IUT) shall not be indicated as invalid. The IUT does not delete the entries from the notification matrix.
- e The UT shall send Configuration.Status(NotOK). The IUT deletes the entries of the UT from the notification matrix.
- f The UT, using the LT pre-IUT, shall write an entry to the notification matrix of the IUT. The UT shall shutdown the network. At the next wake-up, the notification matrix of the IUT does not contain any entry of the UT.

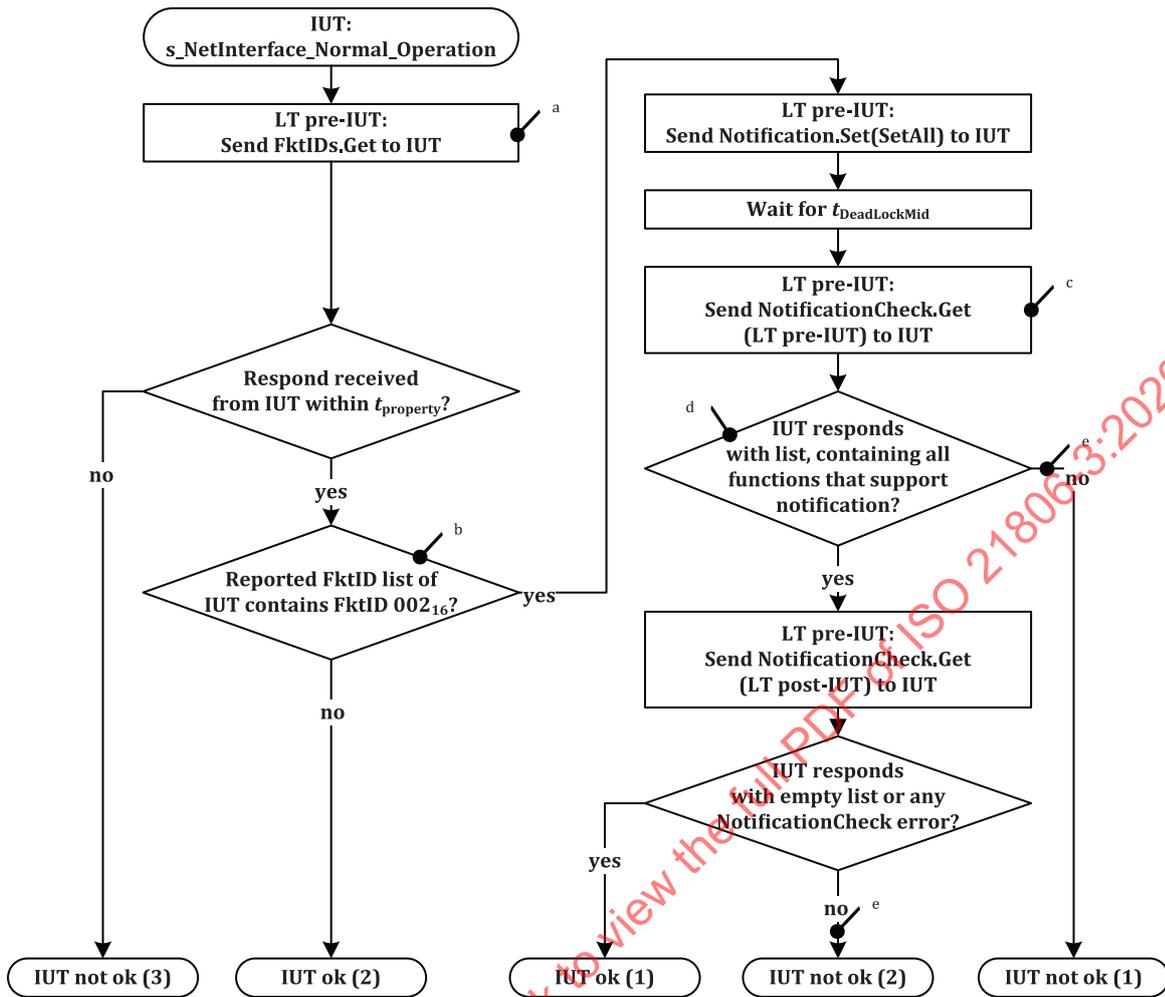
Figure 28 — CTC\_2.8.3-1b - Notification matrix storage test (Slave)

8.6.3 CTC\_2.8.3-2 - NotificationCheck test

Table 35 specifies the CTC\_2.8.3-2 - NotificationCheck test.

Table 35 — CTC\_2.8.3-2 - NotificationCheck test

| Item                     | Content   |
|--------------------------|---|
| <b>CTC # - Title</b>     | CTC_2.8.3-2 - NotificationCheck test  |
| <b>Purpose</b>           | This CTC verifies that a node correctly responds to NotificationCheck requests.<br>This CTC applies to all MOST devices.  |
| <b>Reference</b>         | ISO 21806-2:2020, 7.5 AL - Function identifier (FktID)  |
| <b>Prerequisite</b>      | The UT shall effectuate <code>s_NetInterface_Normal_Operation</code> in the node that contains the IUT.   |
| <b>Set-up</b>            | <ul style="list-style-type: none"> <li>— If the IUT is part of a MOST device that contains the TimingMaster, the LT pre-IUT shall be a TimingSlave.</li> <li>— If the IUT is part of a MOST device that does not contain the TimingMaster, the LT pre-IUT shall be the TimingMaster.</li> <li>— The LT post-IUT shall be a TimingSlave.</li> </ul>  |
| <b>Step</b>              | 1. The UT shall execute the sequence as specified in <a href="#">Figure 29</a> .  |
| <b>Iteration</b>         | The CTC shall be performed with every FBlock of the IUT that has an entry in the central registry.  |
| <b>Expected response</b> | <p>Step 1: IUT ok (1): the IUT responds to <code>NotificationCheck</code> requests.</p> <p>Step 1: IUT ok (2): the IUT does not support <code>NotificationCheck</code>.</p> <p>Step 1: IUT not ok (1): the IUT does not respond with a list, containing all functions that support notification.</p> <p>Step 1: IUT not ok (2): the list from the IUT (for the LT post-IUT) is not empty.</p> <p>Step 1: IUT not ok (3): the IUT does not respond to <code>FktIDs.Get</code> in time.</p> |
| <b>Remark</b>            | ---   |



- a The UT, using the LT pre-IUT, shall request the FktID list of the IUT to check whether NotificationCheck is supported. The UT shall not add entries associated with the LT post-IUT to the notification matrix of the IUT.
- b If the IUT supports NotificationCheck, the UT, using the LT pre-IUT, shall request notification entries for all properties of the current FBlock that support notification from the IUT. FktID 002<sub>16</sub> = NotificationCheck.
- c The UT, using the LT pre-IUT, shall send NotificationCheck to the IUT.
- d The IUT responds with a list that contains all functions supporting notification. If the FBlock does not contain any functions that support notification, the IUT may respond with an empty list or any NotificationCheck error.
- e The UT shall stop the CTC if the IUT does not respond within t<sub>DeadLockShort</sub>.

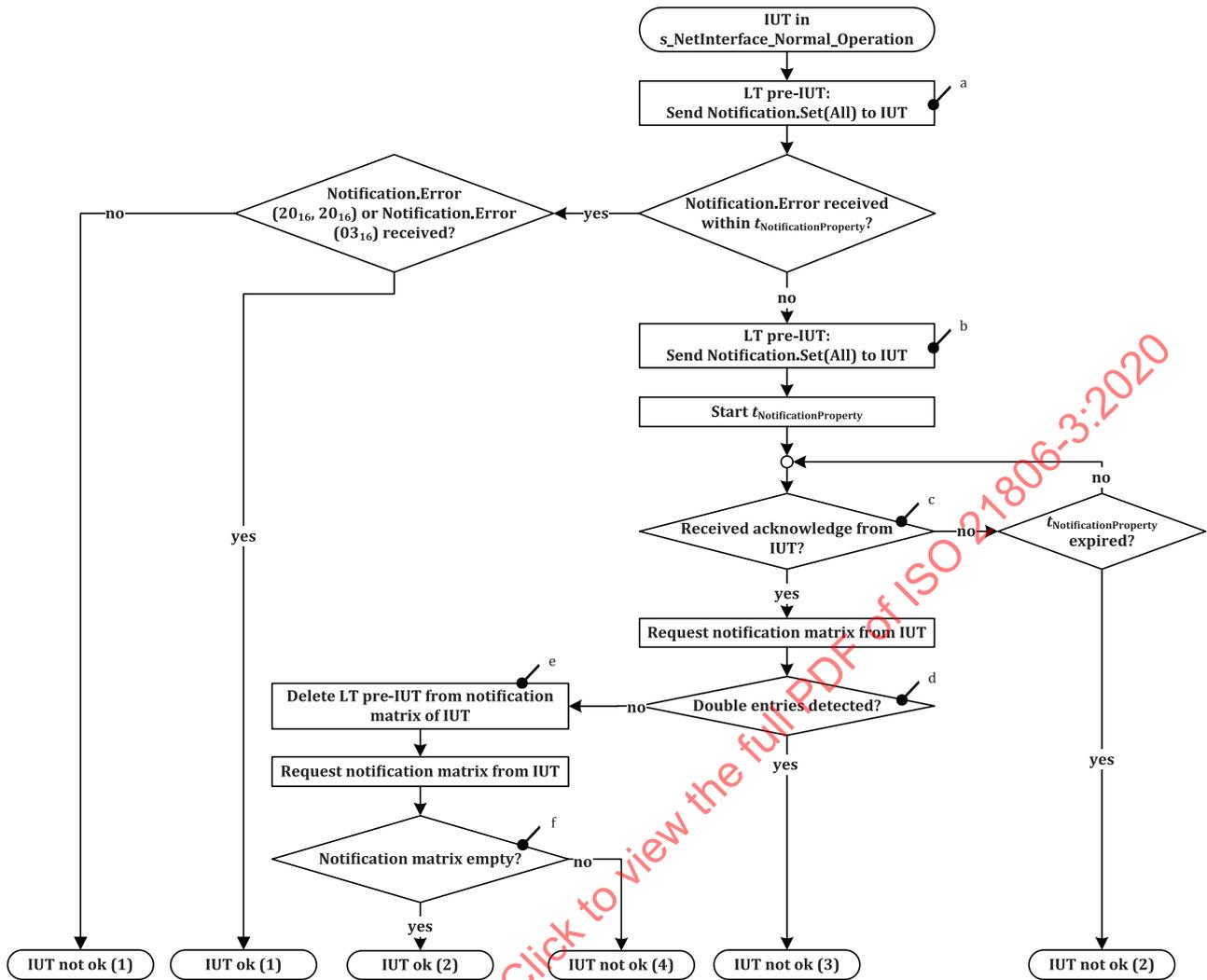
Figure 29 — CTC\_2.8.3-2 - NotificationCheck test

#### 8.6.4 CTC\_2.8.3-7 - Notification matrix double entry test

Table 36 specifies the CTC\_2.8.3-7 – Notification matrix double entry test.

Table 36 — CTC\_2.8.3-7 – Notification matrix double entry test

| Item                     | Content  |
|--------------------------|--|
| <b>CTC # - Title</b>     | CTC_2.8.3-7 – Notification matrix double entry test  |
| <b>Purpose</b>           | This CTC verifies that a node handles requests for creation of duplicate entries in the notification matrix correctly.<br>This CTC applies to all MOST devices.  |
| <b>Reference</b>         | ISO 21806-2:2020, 6.6 APP – Notification mechanisms  |
| <b>Prerequisite</b>      | <ul style="list-style-type: none"> <li>— The UT shall effectuate <code>s_NetInterface_Normal_Operation</code> in the node that contains the IUT.</li> <li>— The ring shall be closed.</li> </ul>   |
| <b>Set-up</b>            | <ul style="list-style-type: none"> <li>— If the IUT is part of a MOST device that contains the TimingMaster, the LT pre-IUT shall be a TimingSlave.</li> <li>— If the IUT is part of a MOST device that does not contain the TimingMaster, the LT pre-IUT shall be the TimingMaster.</li> <li>— The LT post-IUT shall be a TimingSlave.</li> </ul>   |
| <b>Step</b>              | 1. The UT shall execute the sequence as specified in <a href="#">Figure 30</a> .   |
| <b>Iteration</b>         | The CTC shall be performed with every registered FBlock and function of the IUT, registered in the central registry except the FBlock PowerMaster. All FBlocks shall be tested within one test loop (no separate test loop for every FBlock) to increase stress of the IUT.  |
| <b>Expected response</b> | <p>Step 1: IUT ok (1): the IUT does not support notification.</p> <p>Step 1: IUT ok (2): the IUT handles duplicate notification requests correctly.</p> <p>Step 1: IUT not ok (1): the IUT does not store any entry into its notification matrix.</p> <p>Step 1: IUT not ok (2): the IUT does not acknowledge entries to the notification matrix within <math>t_{\text{NotificationProperty}}</math></p> <p>Step 1: IUT not ok (3): the IUT stores double entries into the notification matrix.</p> <p>Step 1: IUT not ok (4): the IUT does not delete entries from the notification matrix.</p> |
| <b>Remark</b>            | <ol style="list-style-type: none"> <li>1. To check whether the IUT supports notification, the FBlock EnhancedTestability can be used to check the size of the notification matrix.</li> <li>2. At the beginning of the CTC, the notification matrix of the IUT does not contain any entry associated with the LT pre-IUT.</li> </ol>   |



- a Add a notification entry for the LT pre-IUT to every function of the IUT that supports notification. To get all functions that can be used for notification, the UT, using the LT pre-IUT, shall send `Notification.Set(All)` to the IUT. If the IUT supports notification, it does not respond with a notification error.
- b Add a notification entry for the LT pre-IUT to every function of the IUT that supports notification, again. The UT, using the LT pre-IUT, shall send `Notification.Set(All)` to the IUT, again (try to generate duplicate entries).
- c Reception of the first (or single) segment of the first status or error message. For duplicate entries, the IUT sends the corresponding report but it does not modify the notification matrix.
- d It is tolerated if there is no entry associated with the LT pre-IUT; this does not lead to IUT not ok (3).
- e The UT shall remove entries associated with the LT pre-IUT from the notification matrix.
- f Check only entries associated with the LT pre-IUT. Each notified function of each FBlock of the IUT is checked. The notification matrix does not contain any entry associated with the LT pre-IUT.

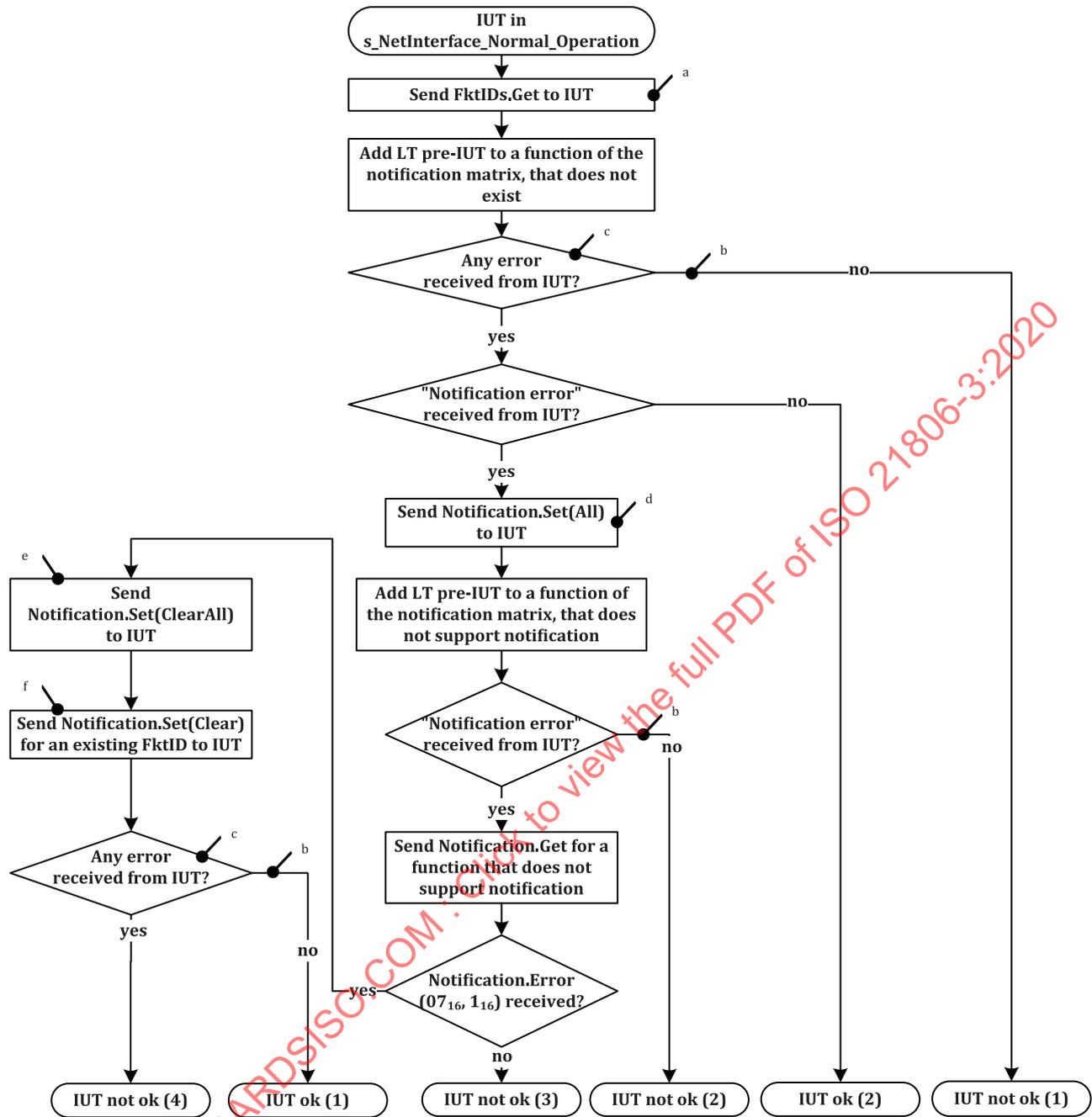
Figure 30 — CTC\_2.8.3-7 - Notification matrix double entry test

8.6.5 CTC\_2.8.3-10 – Notification error test

Table 37 specifies the CTC\_2.8.3-10 – Notification error test.

Table 37 — CTC\_2.8.3-10 – Notification error test

| Item                     | Content  |
|--------------------------|--|
| <b>CTC # - Title</b>     | CTC_2.8.3-10 – Notification error test   |
| <b>Purpose</b>           | This CTC verifies that a node responds with an error message to incorrect notification requests.<br>This CTC applies to all MOST devices.  |
| <b>Reference</b>         | ISO 21806-2:2020, 6.6 APP – Notification mechanisms  |
| <b>Prerequisite</b>      | <ul style="list-style-type: none"> <li>— The UT shall effectuate <code>s_NetInterface_Normal_Operation</code> in the node that contains the IUT.</li> <li>— The ring shall be closed.</li> </ul>   |
| <b>Set-up</b>            | <ul style="list-style-type: none"> <li>— If the IUT is part of a MOST device that contains the TimingMaster, the LT pre-IUT shall be a TimingSlave.</li> <li>— If the IUT is part of a MOST device that does not contain the TimingMaster, the LT pre-IUT shall be the TimingMaster.</li> <li>— The LT post-IUT shall be a listen-only node.</li> </ul>  |
| <b>Step</b>              | 1. The UT shall execute the sequence as specified in <a href="#">Figure 31</a> .   |
| <b>Iteration</b>         | The CTC shall be performed with every FBlock of the IUT registered in the central registry except FBlock PowerMaster.<br>All FBlocks shall be tested within one test loop (no separate test loop for every FBlock) to increase stress of the IUT.  |
| <b>Expected response</b> | <p>Step 1: IUT ok (1): the IUT handles faulty notification requests correctly.</p> <p>Step 1: IUT ok (2): the IUT does not support notification.</p> <p>Step 1: IUT not ok (1): the IUT does not send any error if the function does not exist.</p> <p>Step 1: IUT not ok (2): the IUT does not send <code>Notification.Error</code> if the function does not support notification.</p> <p>Step 1: IUT not ok (3): the IUT does not send <code>Notification.Error(07<sub>16</sub>, 01<sub>16</sub>)</code> if the requested function does not support notification.</p> <p>Step 1: IUT not ok (4): the IUT responds with an error to <code>Notification.Set(Clear)</code>.</p> |
| <b>Remark</b>            | ---  |



- a The UT, using the LT pre-IUT, shall request a list of all FktIDs of the IUT (by means of `FktIDs.Get`). It shall add a notification entry for function that does not exist. Use a function that is not received after sending `FktIDs.Get`.
- b Timeout:  $t_{DeadLockShort}$ .
- c The IUT responds with a notification error if the FBlock supports notification. If the IUT responds with `Error(2016, 2016)` or `Error(0316)`, the FBlock does not support notification.
- d The UT, using the LT pre-IUT, shall send `Notification.Set(All)` to the IUT to get a list of all functions that support notification. It shall add an entry for a function that does not support notification. By comparison of the results of `FktIDs.Get` and `Notification.Set(All)`, the functions that do not support notification are identified. The IUT responds with an error.  
If all functions support notification, skip this part and go directly to send `Notification.Set(ClearAll)` to the IUT.

- e The UT, using the LT pre-IUT, shall send `Notification.Get` for a function that does not support notification. The IUT responds with an error.
- f The UT, using the LT pre-IUT, shall clear the notification matrix by means of `Notification.Set(ClearAll)`.
- g The UT, using the LT pre-IUT, shall request deletion of an entry for an existing, not notified function from the notification matrix of the IUT. Use a function that supports notification. The IUT does not return any error.

**Figure 31 — CTC\_2.8.3-10 - Notification error test**

**8.7 CTC\_3.0-1 - TEST\_GSI\_GSO\_Identification**

The CTC for GSI and GSO identification is specified using message sequence charts, see Z.120<sup>[5]</sup>.

Table 38 specifies the TEST\_GSI\_GSO\_Identification.

**Table 38 — TEST\_GSI\_GSO\_Identification**

| Item                     | Content  |
|--------------------------|--|
| <b>CTC # - Title</b>     | CTC_3.0-1 - TEST_GSI_GSO_Identification  |
| <b>Purpose</b>           | <p>This CTC verifies</p> <ul style="list-style-type: none"> <li>— that a node reports source and sink FBlocks and responds to <code>FktIDs.Get</code> correctly;</li> <li>— <math>t_{Property}</math>;</li> <li>— reported <code>FktIDs</code>.</li> </ul> <p>This CTC applies to all MOST devices.</p>  |
| <b>Reference</b>         | ISO 21806-2:2020, 7.5 AL - Function Identifier (FktID)   |
| <b>Preconditions</b>     | The UT shall effectuate <code>s_NetInterface_Normal_Operation</code> in the node that contains the IUT.  |
| <b>Set-up</b>            | <ul style="list-style-type: none"> <li>— If the IUT is part of a MOST device that contains the <code>TimingMaster</code>, the LT pre-IUT shall be a <code>TimingSlave</code>;</li> <li>— If the IUT is part of a MOST device that does not contain the <code>TimingMaster</code>, the LT pre-IUT shall be the <code>TimingMaster</code>;</li> <li>— The LT post-IUT shall be a listen-only node.</li> </ul>  |
| <b>Step</b>              | 1. The UT shall execute the sequence as specified in Figure 32.  |
| <b>Iteration</b>         | The CTC shall be performed with every single FBlock, reported by the IUT by means of <code>NetBlock.FBlockIDs.Status</code> .  |
| <b>Expected response</b> | <p>Step 1: IUT ok (1): FBlock contains neither sink nor source.</p> <p>Step 1: IUT ok (2): FBlock contains a source.</p> <p>Step 1: IUT ok (3): FBlock contains a source and a sink.</p> <p>Step 1: IUT ok (4): FBlock contains a sink.</p> <p>Step 1: IUT not ok (1): the IUT does not send <code>FktIDs.Status</code>.</p> <p>Step 1: IUT not ok (2): FBlocks of the IUT, containing sinks/sources that do not match to the manufacturer list.</p> |

Table 38 (continued)

| Item          | Content  |
|---------------|--|
| <b>Remark</b> | <ol style="list-style-type: none"> <li data-bbox="312 320 1396 353">1. The CTC is performed to identify whether the IUT contains sink or source.</li> <li data-bbox="312 371 1396 432">2. The manufacturer provides a list with all FBlocks of the IUT, containing sink and/or source functionality.</li> <li data-bbox="312 450 1396 483">3. An IUT with result "IUT ok (1)" shall not run through any source or sink related test.</li> <li data-bbox="312 501 1396 562">4. An IUT with result "IUT ok (2)" shall run through all source related tests (relevant FBlocks only)</li> <li data-bbox="312 580 1396 640">5. An IUT with result "IUT ok (3)" shall run through all source and sink related tests (relevant FBlocks only)</li> <li data-bbox="312 658 1396 719">6. An IUT with result "IUT ok (4)" shall run through all sink related tests (relevant FBlocks only)</li> </ol> |

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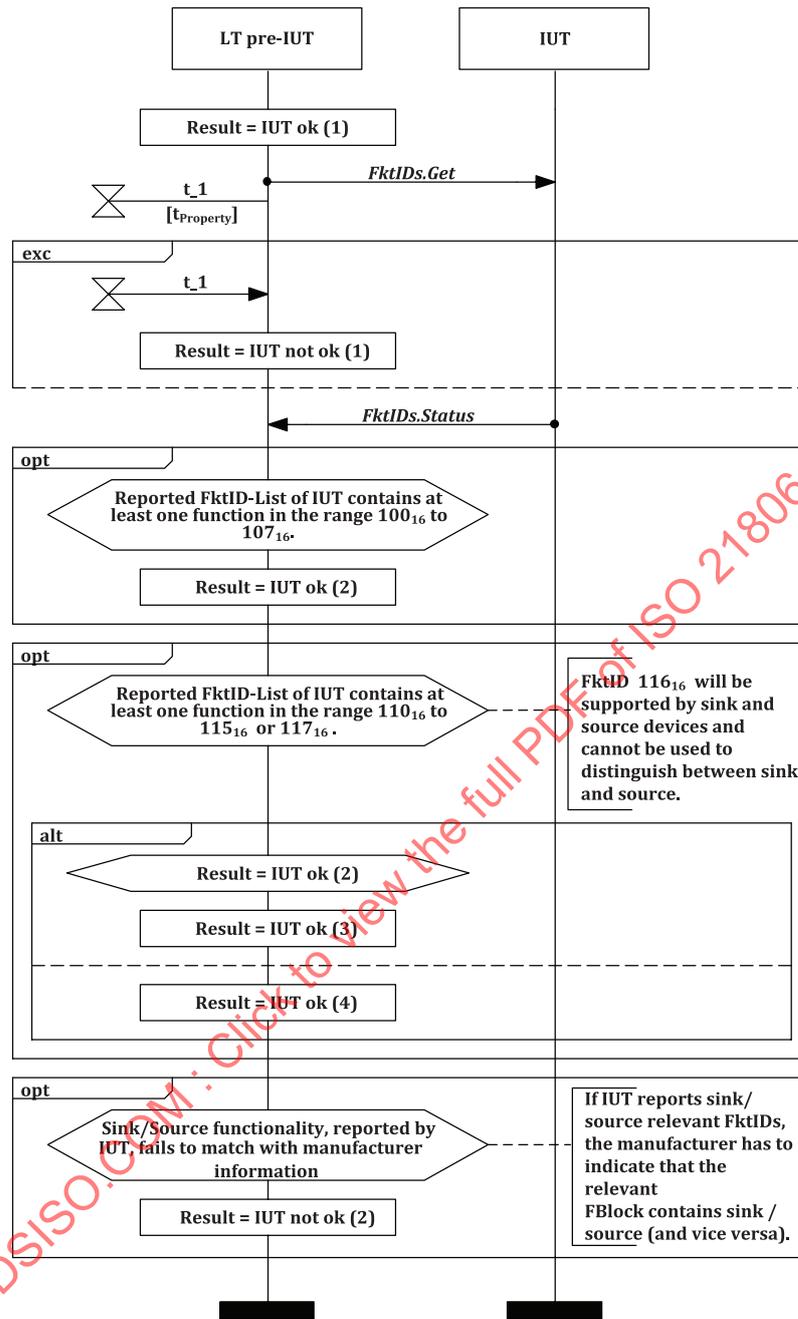


Figure 32 — CTC\_3.0-1 - TEST\_GSI\_GSO\_Identification

## 8.8 Obligatory tests for sink and source MOST devices

### 8.8.1 General

The CTCs for sink and sources are specified using message sequence charts, see Z.120[5].

### 8.8.2 Sink MOST devices

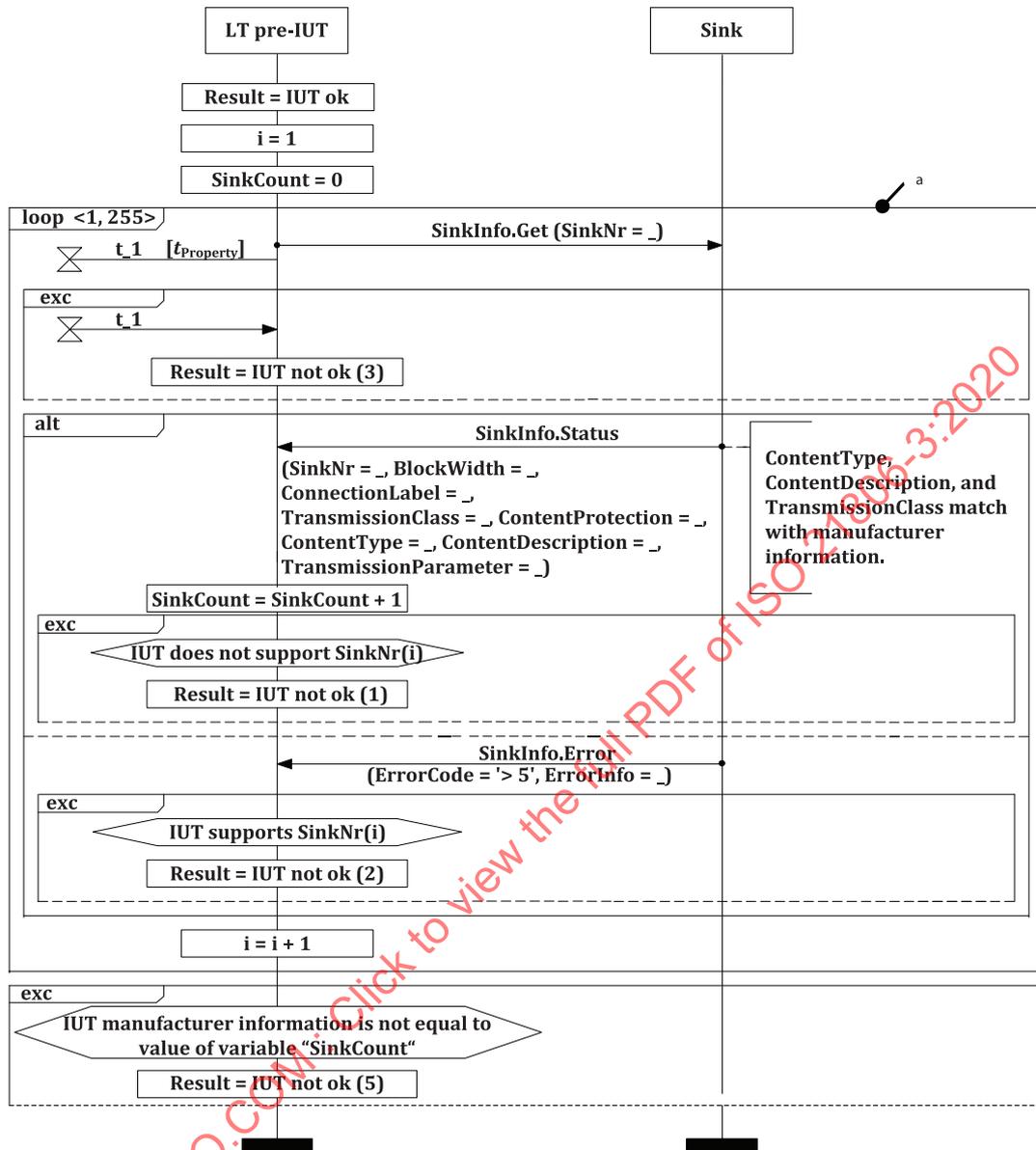
#### 8.8.2.1 CTC\_3.1-1 - TEST\_GSI\_SinkInfo

Table 39 specifies the CTC\_3.1-1 - TEST\_GSI\_SinkInfo.

Table 39 — CTC\_3.1-1 - TEST\_GSI\_SinkInfo

| Item                     | Content   |
|--------------------------|---|
| <b>CTC # - Title</b>     | CTC_3.1-1 - TEST_GSI_SinkInfo   |
| <b>Purpose</b>           | This CTC verifies that a sink correctly responds to SinkInfo requests.<br>This CTC applies to all MOST devices that contain at least one sink.  |
| <b>Reference</b>         | ISO 21806-2:2020, 6.10.2.3.1 APP - SinkInfo function  |
| <b>Preconditions</b>     | The UT shall effectuate <code>s_NetInterface_Normal_Operation</code> in the node that contains the IUT.   |
| <b>Set-up</b>            | <ul style="list-style-type: none"> <li>— If the IUT is part of a MOST device that contains the TimingMaster, the LT pre-IUT shall be a TimingSlave.</li> <li>— If the IUT is part of a MOST device that does not contain the TimingMaster, the LT pre-IUT shall be the TimingMaster.</li> <li>— The LT post-IUT shall be a listen-only node.</li> </ul>   |
| <b>Step</b>              | 1. The UT shall execute the sequence as specified in <a href="#">Figure 33</a> .  |
| <b>Iteration</b>         | The UT shall perform the CTC with every <code>SinkNr</code> (1 to 255) according to the manufacturer information.   |
| <b>Expected response</b> | <p>Step 1: IUT ok: the IUT correctly responds to SinkInfo requests.</p> <p>Step 1: IUT not ok (1): the IUT responds status although <code>SinkNr</code> not supported.</p> <p>Step 1: IUT not ok (2): the IUT responds with an error although <code>SinkNr</code> supported.</p> <p>Step 1: IUT not ok (3): the IUT does not respond within <code>t_Property</code> to <code>SinkInfo.Get</code>.</p> <p>Step 1: IUT not ok (5): the IUT manufacturer information is not equal to the reported number of sinks.</p> |
| <b>Remark</b>            | ---   |

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<sup>a</sup> The UT shall process each SinkNr by sending SinkInfo.Get to the IUT. The UT shall skip a sink if the bandwidth of the sink is not determinable because of the data type (ContentType C0<sub>16</sub> to EF<sub>16</sub>). If the IUT supports a SinkNr, it responds with the correct ContentType, ContentDescription (data type of the parameter) and TransmissionClass. If the IUT does not support a SinkNr, it responds with an error.

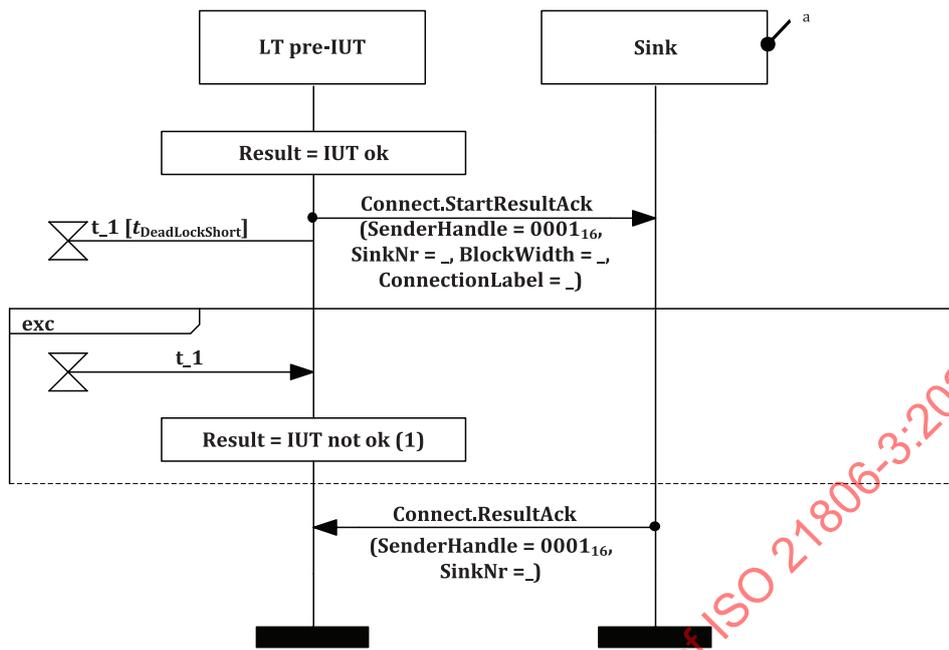
Figure 33 — CTC\_3.1-1 - TEST\_GSI\_SinkInfo

8.8.2.2 CTC\_3.1-3 - TEST\_GSI\_Connect

Table 40 specifies the CTC\_3.1-3 - TEST\_GSI\_Connect.

**Table 40 — CTC\_3.1-3 – TEST\_GSI\_Connect**

| Item                     | Content   |
|--------------------------|---|
| <b>CTC # - Title</b>     | CTC_3.1-3 – TEST_GSI_Connect  |
| <b>Purpose</b>           | <p>This CTC verifies that a sink handles <code>Connect.StartResultAck</code> correctly.</p> <p>This CTC applies to all MOST devices that contain at least one sink that supports transmission class “synchronous”.</p> <p>This CTC is only applicable if the node that contains the IUT does not contain the connection manager.</p>  |
| <b>Reference</b>         | ISO 21806-2:2020, 6.10.3.2.1 APP – Connect function   |
| <b>Preconditions</b>     | <ul style="list-style-type: none"> <li>— The UT shall effectuate <code>s_NetInterface_Normal_Operation</code> in the node that contains the IUT.</li> </ul>   |
| <b>Set-up</b>            | <ul style="list-style-type: none"> <li>— If the IUT is part of a MOST device that contains the <code>TimingMaster</code>, the LT pre-IUT shall be a <code>TimingSlave</code>.</li> <li>— If the IUT is part of a MOST device that does not contain the <code>TimingMaster</code>, the LT pre-IUT shall be the <code>TimingMaster</code>.</li> <li>— The LT post-IUT shall be a listen-only node.</li> </ul> |
| <b>Step</b>              | 1. The UT shall execute the sequence as specified in <a href="#">Figure 34</a> .  |
| <b>Iteration</b>         | The UT shall perform the CTC with every <code>SinkNr</code> that is supported by the IUT.   |
| <b>Expected response</b> | <p>Step 1: IUT ok: the IUT handles <code>Connect.StartResultAck</code> correctly.</p> <p>Step 1: IUT not ok (1): the IUT does not respond within <math>t_{DeadLockShort}</math>.</p>  |
| <b>Remark</b>            | <ol style="list-style-type: none"> <li>1. The UT ensures that the connections are in use by a source. Some sinks check the validity of the connections before connecting. Only if the <code>TimingMaster</code> indicates the connections as allocated by a source, the sink connects.</li> <li>2. Timeout <math>t_{DeadLockShort}</math> is valid for the whole CTC.</li> </ol>                            |



- <sup>a</sup> The UT shall process each SinkNr by sending Connect.StartResultAck to the IUT. The UT shall ensure that the connections to which a sink shall connect to are in use by a source.  
 The UT shall skip a sink if the bandwidth of the sink is not determinable because of the data type (ContentType C0<sub>16</sub> to EF<sub>16</sub>).  
 The IUT processes Connect.StartResultAck correctly.  
 The UT shall use Disconnect before processing the next sink to avoid resource overflow.

Figure 34 — CTC\_3.1-3 - TEST\_GSI\_Connect

8.8.2.3 CTC\_3.1-4 - TEST\_GSI\_Connect\_Repeat

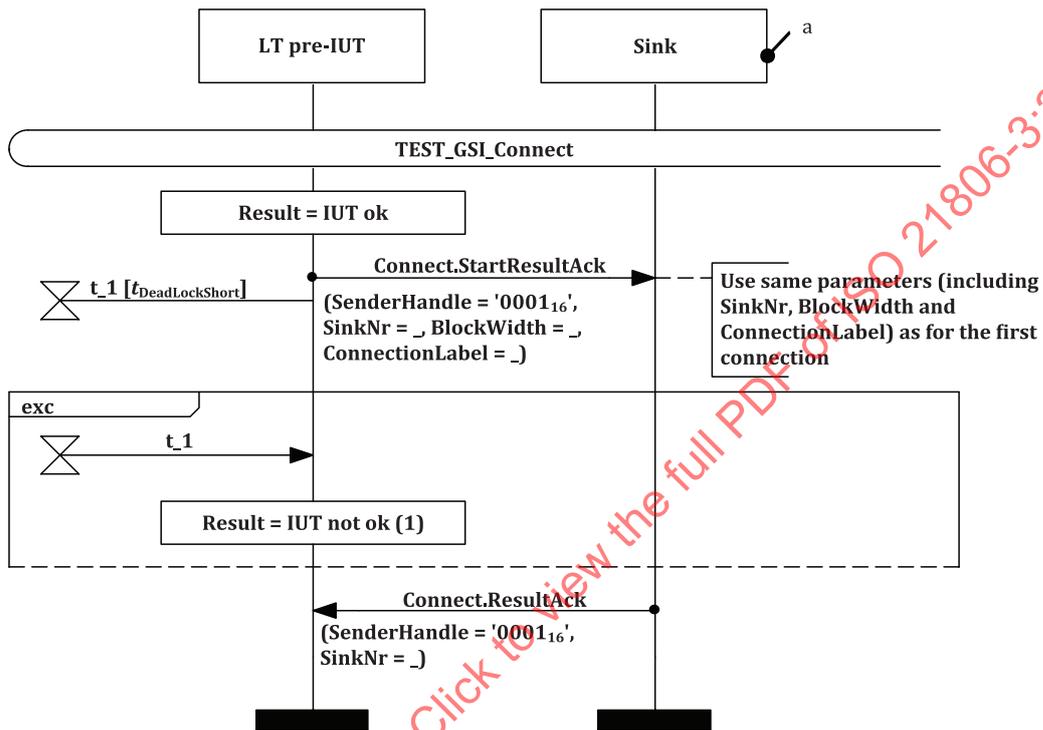
Table 41 specifies the CTC\_3.1-4 - TEST\_GSI\_Connect\_Repeat.

Table 41 — CTC\_3.1-4 - TEST\_GSI\_Connect\_Repeat

| Item                 | Content  |
|----------------------|--|
| <b>CTC # - Title</b> | CTC_3.1-4 - TEST_GSI_Connect_Repeat  |
| <b>Purpose</b>       | This CTC verifies that sink handles repeated Connect.StartResultAck commands correctly.<br>This CTC applies to all MOST devices that contain at least one sink that supports transmission class "synchronous".<br>This CTC is only applicable if the node that contains the IUT does not contain the connection manager. |
| <b>Reference</b>     | ISO 21806-2:2020, 6.10.3.2.1 APP – Connect function  |
| <b>Preconditions</b> | The UT shall effectuate s_NetInterface_Normal_Operation in the node that contains the IUT.   |
| <b>Set-up</b>        | — If the IUT is part of a MOST device that contains the TimingMaster, the LT pre-IUT shall be a TimingSlave.<br>— If the IUT is part of a MOST device that does not contain the TimingMaster, the LT pre-IUT shall be the TimingMaster.<br>— The LT post-IUT shall be a listen-only node.                                |
| <b>Step</b>          | 1. The UT shall execute the sequence as specified in Figure 35.  |

Table 41 (continued)

| Item              | Content  |
|-------------------|--|
| Iteration         | The UT shall perform the test with every SinkNr supported by the IUT.  |
| Expected response | Step 1: IUT ok: the IUT handles repeated Connect.StartResultAck commands correctly.<br>Step 1: IUT not ok: the IUT does not respond within $t_{DeadLockShort}$ . |
| Remark            | Timeout $t_{DeadLockShort}$ is valid for the whole CTC.  |



<sup>a</sup> The UT shall process each SinkNr by sending Connect.StartResultAck to the IUT. The UT shall repeat the Connect.StartResultAck request.  
The UT shall skip a sink if the bandwidth of the sink is not determinable because of the data type (ContentType C0<sub>16</sub> to EF<sub>16</sub>). The IUT processes repeated Connect.StartResultAck commands correctly.  
The UT shall use Disconnect before processing the next sink to avoid resource overflow.

Figure 35 — CTC\_3.1-4 - TEST\_GSI\_Connect\_Repeat

8.8.2.4 CTC\_3.1-5 - TEST\_GSI\_DisConnect

Table 42 specifies the CTC\_3.1-5 - TEST\_GSI\_DisConnect.

Table 42 — CTC\_3.1-5 - TEST\_GSI\_DisConnect

| Item          | Content   |
|---------------|---|
| CTC # - Title | CTC_3.1-5 - TEST_GSI_DisConnect   |
| Purpose       | This CTC verifies that a sink handles Disconnect.StartResultAck correctly.<br><br>This CTC applies to all MOST devices that contain at least one sink that supports transmission class "synchronous".<br><br>This CTC is only applicable if the node that contains the IUT does not contain the connection manager. |