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Foreword

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This document was prepared by Joint Technical Committee ISO/IEC JTC 1, *Information technology*, Subcommittee SC 27, *Information security, cybersecurity and privacy protection*.

A list of all parts in the ISO/IEC 29192 series can be found on the ISO and IEC websites.

Any feedback or questions on this document should be directed to the user's national standards body. A complete listing of these bodies can be found at www.iso.org/members.html and www.iec.ch/national-committees.

Introduction

This document specifies authenticated encryption tailored for implementation in constrained environments. Data transmitted from one party to another is often vulnerable against various attacks such as eavesdropping or malicious alterations. Similarly, data at rest usually requires protection.

Encryption mechanisms as specified in the ISO/IEC 18033 series and ISO/IEC 10116 provide solutions against eavesdropping. Integrity protection is usually guaranteed with a message authentication code (MAC) algorithm, such as those defined in the ISO/IEC 9797 series. In addition, ISO/IEC 19772 describes several authenticated encryption mechanisms, that is to say mechanisms that efficiently combine the encryption and MAC operations.

Nonetheless, some applications including radiofrequency identification (RFID) tags, smart cards, secure batteries, health-care systems and sensor networks, encounter several constraints. Chip area, energy consumption, execution time, program code, RAM size and communication bandwidth are typically critical for the applications listed above. The ISO/IEC 29192 series specifies lightweight cryptography suitable for these constrained environments. ISO/IEC 29192-2 and ISO/IEC 29192-3 respectively define lightweight block ciphers and stream ciphers. Both can be used to provide confidentiality. Regarding protection against alteration, lightweight MAC algorithms are defined in ISO/IEC 29192-6.

In this document, lightweight authenticated encryption mechanisms are defined. Similar to ISO/IEC 19772, they provide confidentiality, integrity and optionally data origin authentication. They differ from those specified in the aforementioned document, in that they have been specifically designed for constrained environments.

This document specifies a unique method. In the future, other methods may be added to this document, including lightweight authenticated encryption with additional data (AEAD) methods, based either on block ciphers or stream ciphers.

Information security — Lightweight cryptography —

Part 8: Authenticated encryption

1 Scope

This document specifies one method for authenticated encryption suitable for applications requiring lightweight cryptographic mechanisms.

This method processes a data string with the following security objectives:

a) data confidentiality, i.e. protection against unauthorized disclosure of data,

b) data integrity, i.e. protection that enables the recipient of data to verify that it has not been modified.

Optionally, this method can provide data origin authentication, i.e. protection that enables the recipient of data to verify the identity of the data originator.

The method specified in this document is based on a lightweight stream cipher, and requires the parties of the protected data to share a secret key for this algorithm. Key management is outside the scope of this document.

NOTE Key management techniques are defined in the ISO/IEC 11770 series.

2 Normative references

There are no normative references for this document.

3 Terms and definitions

For the purposes of this document, the following terms and definitions apply.

ISO and IEC maintain terminology databases for use in standardization at the following addresses:

— ISO Online browsing platform: available at https://www.iso.org/obp

— IEC Electropedia: available at https://www.electropedia.org/

3.1
authenticated encryption
(reversible) transformation of data by a cryptographic algorithm to produce ciphertext that cannot be altered by an unauthorized entity without detection, i.e. it provides data confidentiality, data integrity, and optionally data origin authentication

[SOURCE: ISO/IEC 19772:2020, 3.2, modified — The definition was slightly modified to make the data origin authentication optional.]

3.2
authenticated encryption mechanism
cryptographic technique used to protect the confidentiality, guarantee the integrity of data and optionally the data origin and which consists of two component processes: an *encryption* (3.6) algorithm and a *decryption* (3.5) algorithm

[SOURCE: ISO/IEC 19772:2020, 3.3, modified — The definition was slightly modified to make the data origin authentication optional.]

3.3
ciphertext
data which has been transformed to hide its information content

[SOURCE: ISO/IEC 18033-1:2021, 3.7]

3.4
data integrity
property that data has not been altered or destroyed in an unauthorized manner

[SOURCE: ISO/IEC 9797-1:2011, 3.4]

3.5
decryption
reversal of a corresponding *encryption* (3.6)

[SOURCE: ISO/IEC 9797-1:2011, 3.5]

3.6
encryption
reversible operation by a cryptographic algorithm converting data into *ciphertext* (3.3) so as to hide the information content of the data

[SOURCE: ISO/IEC 9797-1:2011, 3.6]

3.7
initialization value
value used in defining the starting point of an *encryption* (3.6) process

[SOURCE: ISO/IEC 18033-4:2011, 3.7]

3.8
key
sequence of symbols that controls the operation of a cryptographic transformation

[SOURCE: ISO/IEC 9797-1:2011, 3.7, modified — Note was removed.]

3.9
keystream function
function that takes as input, the current *state* (3.17) of the *keystream generator* (3.10) and (optionally) part of the previously generated *ciphertext* (3.3), and gives as output the next part of the keystream

[SOURCE: ISO/IEC 18033-4:2011, 3.9]

3.10
keystream generator
state-based process (i.e. as a finite state machine) that takes as input, a *key* (3.8), an *initialization value* (3.7), and if necessary the *ciphertext* (3.3), and gives as output a keystream (i.e. a sequence of bits or blocks of bits) of arbitrary length

[SOURCE: ISO/IEC 18033-4:2011, 3.10, modified —"initialization vector" changed to "initialization value".]

3.11
message authentication code
MAC
string of bits which is the output of a *MAC algorithm* (3.12)

[SOURCE: ISO/IEC 9797-1:2011, 3.9, modified — Note was removed.]

3.12
message authentication code algorithm
MAC algorithm
algorithm for computing a function which maps strings of bits and a *secret key* (3.16) to fixed-length strings of bits, satisfying the following two properties:

— for any key and any input string, the function can be computed efficiently;

— for any fixed key, and given no prior knowledge of the key, it is computationally infeasible to compute the function value on any new input string, even given knowledge of a set of input strings and corresponding function values, where the value of the ith input string might have been chosen after observing the value of the first i-1 function values (for integers $i > 1$)

[SOURCE: ISO/IEC 9797-1:2011, 3.10, modified — Notes were removed]

3.13
next-state function
function that takes as input, the current *state* (3.17) of the *keystream generator* (3.10) and (optionally) part of the previously generated *ciphertext* (3.3), and gives as output a new *state* (3.17) for the *keystream generator* (3.10)

[SOURCE: ISO/IEC 18033-4:2011, 3.12]

3.14
plaintext
cleartext
unencrypted information

[SOURCE: ISO/IEC 18033-1:2021, 3.20]

3.15
pre-output stream
pseudo-random bits, which are used for the *encryption* (3.6) and the *decryption* (3.5) of the message, and the generation of the *message authentication code* (3.11)

3.16
secret key
key (3.8) used with symmetric cryptographic techniques by a specified set of entities

[SOURCE: ISO/IEC 18033-1:2021, 3.25]

3.17
state
internal state of *a keystream generator* (3.10)

[SOURCE: ISO/IEC 29192-3:2012, 3.12]

4 Symbols and abbreviated terms

For the purposes of this document, the following symbols and abbreviated terms apply.

ACCU Dedicated accumulator register for the MAC (t bits).

AM	Authenticated message, the concatenation of the ciphertext *C* and the *MAC*. *AM = C || MAC.*
AND	Bitwise logical AND operation.
$AUTH^{(i)}$	Dedicated register for the MAC computation.
a_i	Variable forming part of the internal state of a keystream generator.
b_i	Variable forming part of the internal state of a keystream generator.
C	Ciphertext.
C_i	Ciphertext bit.
Fmac	Function which finalizes the MAC computation.
Imac	Function which initializes the MAC registers.
Init	Function which generates the initial internal state of a keystream generator.
IV	Initialization value.
K	Key.
l	Length of a plaintext or ciphertext block (in bits).
Len	Function that returns the number of bits in a string.
LFSR	Linear feedback shift register.
MAC	Message authentication code. MAC is a *t*-bit string.
M_i	Message bit.
n	Length of the authenticated message (AM) (in bits).
Next	Next-state function of a keystream generator.
NLFSR	Nonlinear feedback shift register.
OR	Bitwise logical OR operation.
P	Plaintext.
P_i	Plaintext bit.
Prt	Function that generates a pre-output of the stream cipher.
r_i	Variable forming part of the internal state of a keystream generator.
SHIFT	Dedicated shift register for the MAC (*t* bits).
s_i	Variable forming part of the internal state of a keystream generator.
Strm	Keystream function of a keystream generator.
$S^{(i)}$	Internal state of keystream generator.
t	MAC length (in bits).
Upmac	Function which updates the MAC registers.

Y	Pre-output stream.
$Y^{(i)}$	Pre-output bit.
Z	Keystream.
$Z^{(i)}$	Keystream bit.
0^i	Block of i zero bits.
1^i	Block of i one bits.
$\oplus$	Bitwise XOR (eXclusive OR) operation.
$\|\|$	Concatenation of strings, i.e. if A and B are blocks of data, then $A\|\|B$ is the block obtained concatenating A and B in the order specified.
$X\|_s$	Left-truncation of the block of bits X: if X has a bit-length greater than or equal to s, then $X\|_s$ is the s-bit block consisting of the left-most s bits of X.
$X\|^s$	Right-truncation of the block of bits X: if X has a bit-length greater than or equal to s, then $X\|^s$ is the s-bit block consisting of the right-most s bits of X.

5 Grain-128A

5.1 Introduction to Grain-128A

Grain-128A is a synchronous stream cipher with an add-on module that generates a message authentication code (MAC).

It is composed of two sub-modules that work conjointly:

a) a stream cipher module that generates the key stream for the encryption/decryption of the message;

b) a MAC module that constitutes the MAC algorithm.

Grain-128A has a 128-bit long key, K, and a 96-bit initialization value, IV. It generates a t-bit MAC.

As a precondition, the recipients will have received K and IV in a secure way as pre-shared parameters.

For this mechanism, t shall be at least equal to 32 and the MAC shall apply to the entire plaintext.

After the initialization of the system with the K and the IV, the cipher generates a pre-output stream. This pre-output stream is split into two parts:

a) the even bits compose the keystream to encrypt/decrypt the message;

b) the odd bits are used to generate the MAC.

NOTE 1 Grain-128A document [12] defines two modes of operation: with or without a MAC. The MAC is disabled when $IV_0 = 0$ and conversely enabled when $IV_0 = 1$. This document only specifies the authenticated mode, i.e. the MAC is always supported. Accordingly, the value of IV_0 is fixed to '1'.

As a synchronous keystream generator, Grain-128A follows the general models of stream ciphers defined in ISO/IEC 18033-4, with supplementary functions to take the MAC generation into consideration.

Grain-128A finite-state machine is defined by:

a) an initialization function, **Init**, which takes as input a key, K, and an initialization value, IV, and outputs an initial state $S^{(-2*t)}$ for the keystream generator;

NOTE 2 As for any stream cipher, the uniqueness of the *IV* for a given key, *K*, is a crucial requirement. The reuse of an *IV* under the same key leads to trivial attacks causing plaintext recovery and impersonation.

b) a MAC initialization function, **Imac**, which initializes the MAC registers;

c) a next-state function, **Next**, which takes as input the current state of the pre-output stream generator $S^{(i)}$, and outputs the next state of the pre-output stream generator $S^{(i+1)}$;

d) a pre-output function, **Prt**, which takes as input a state of the pre-output generator $S^{(i)}$, and outputs pre-output bits $Y^{(i)}$;

e) a keystream function, **Strm**, that outputs a key stream bit $Z^{(i)}$;

f) an update MAC function, **Upmac**, which takes as input the current MAC registers, a pre-output bit $Y^{(i)}$, a bit of the message M_i and outputs the next state of the MAC registers;

g) a final MAC function, **Fmac**, which completes the MAC computation and outputs the MAC of the message.

The output function is the binary-additive function. That is to say, the operation to combine the plaintext with the key stream is the bitwise XOR.

The encryption of plaintext bit P_i by a keystream bit $Z^{(i)}$ is given by:

$$C_i = P_i \oplus Z^{(i)}$$

Inversely, the decryption of a ciphertext C_i by a keystream bit $Z^{(i)}$ is given by:

$$P_i = C_i \oplus Z^{(i)}$$

Annex B provides some numerical examples for $t = 32$ and $t = 64$. Annex A defines object identifiers that shall be used to identify the lightweight authenticated encryption algorithms specified in this document. Annex C exposes some security considerations on Grain-128A.

5.2 Internal state

Grain-128A state is composed of two sub-modules.

a) The state variable $S^{(i)}$ which is subdivided into two 128-bit registers:

$$S^{(i)} = (NLFSR^{(i)}, LFSR^{(i)})$$

where

$$NLFSR^{(i)} = (b_0^{(i)}, b_1^{(i)}, \ldots, b_{127}^{(i)})$$

$$LFSR^{(i)} = (s_0^{(i)}, s_1^{(i)}, \ldots, s_{127}^{(i)})$$

where b_j and s_j are bits (for $j = 0, 1, 2, \ldots, 127$).

b) The MAC variable $AUTH^{(i)}$ which is subdivided into two t-bit registers:

$$AUTH^{(i)} = (ACCU^{(i)}, SHIFT^{(i)})$$

where

$$ACCU^{(i)} = (a_0^{(i)}, a_1^{(i)}, \ldots, a_{t-1}^{(i)})$$

$$SHIFT^{(i)} = (r_0^{(i)}, r_1^{(i)}, \ldots, r_{t-1}^{(i)})$$

where a_j and r_j are bits (for j = 0, 1, 2, ..., t-1)

Figure 1 illustrates the building blocks of Grain-128A. The algorithm is composed of four registers: NLFSR, LFSR, SHIFT and ACCU registers.

On the top left of Figure 1, the feedbacks of the registers LFSR and NLFSR are outlined together with the generation of the pre-output stream. The small figures on top of and below the LFSR and NLFSR registers indicate the number of bits extracted from the registers. The operations are fully described in the sub-functions **Next**, **Strm** and **Prt.**

Figure 1 also shows how the pre-output stream is split in two sub-streams. Even bits compose the keystream to encrypt the data, while odd bits update the MAC module.

On the right of Figure 1, the SHIFT and the ACCU registers compose the module dedicated to the MAC generation.

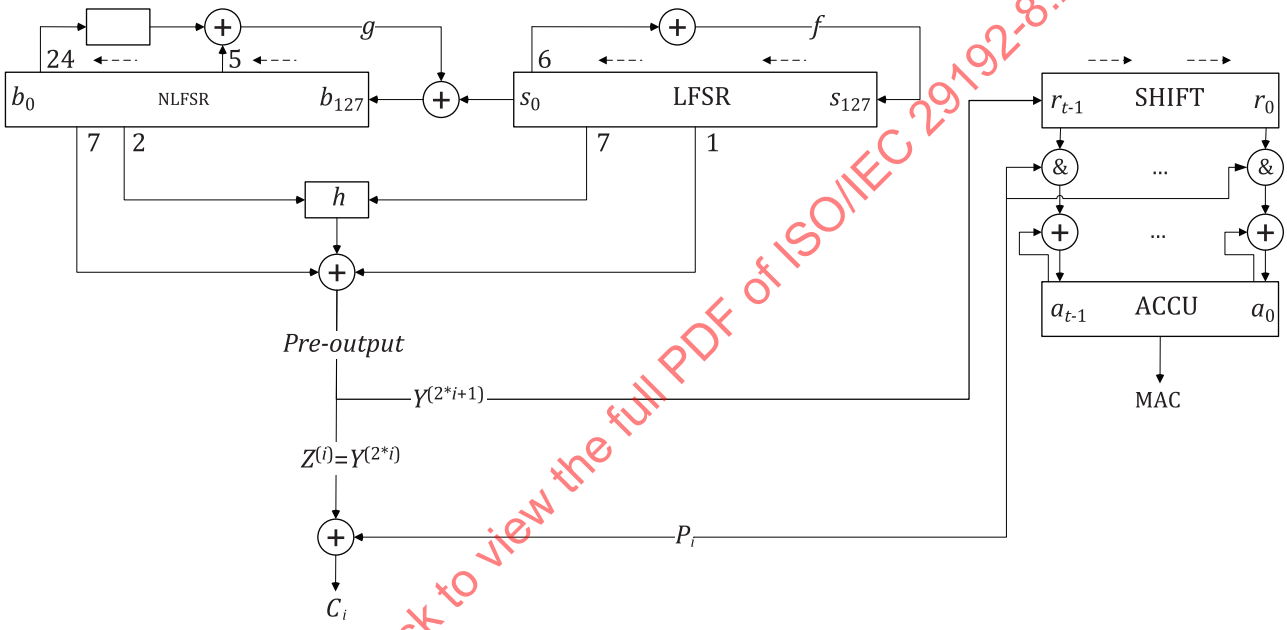

Figure 1 — An overview of the building blocks of Grain-128A

5.3 Encryption and MAC generation procedure

The originator shall perform the following steps to protect a data string P.

Inputs:

— $P = P_0\, P_1 \ldots P_{l-1}$, an l-bit-string to encrypt and authenticate

— IV is a 96-bit string that was pre-shared between recipients

— The 128-bit key, K, that was securely shared beforehand.

Output: $AM = C_0\, C_1 \ldots C_{l-1} \,||\, MAC$, the authenticated encryption $(l+t)$-bit string of P.

a) Initialize the state variable with key, K, and the initialization value, IV:

— $S^{(-2*t)}$ = **Init**(K,IV)

b) Initialize the MAC variable:

— $(AUTH^{(0)},S^{(0)})$ = **Imac**($S^{(-2*t)}$)

c) For every bit of the message P_i, generate a keystream bit $Z^{(i)}$ and update the MAC state.

— For $i = 0, 1, ..., l\text{-}1$:

— Encrypt the message bit:

— $Y^{(2*i)} = \mathbf{Prt}(S^{(2*i)})$

— $Z^{(i)} = \mathbf{Strm}(Y^{(2*i)})$

— $C_i = P_i \oplus Z^{(i)}$

— $S^{(2*i+1)} = \mathbf{Next}(S^{(2*i)})$

— Update the MAC variable:

— $Y^{(2*i+1)} = \mathbf{Prt}(S^{(2*i+1)})$

— $AUTH^{(i+1)} = \mathbf{Upmac}(AUTH^{(i)}, Y^{(2*i+1)}, P_i)$

— $S^{(2*i+2)} = \mathbf{Next}(S^{(2*i+1)})$

d) Finalize the MAC computation:

— $MAC = \mathbf{Fmac}(AUTH^{(i)})$

e) Concatenate the encrypted message C and the MAC to produce the $(l+t)$-bit string AM, the authenticated-encrypted version of P.

— $AM = C \,||\, MAC$, where $C = C_0\, C_1 \ldots C_{l-1}$ is an l-bit-string.

f) Output AM.

5.4 Decryption and MAC verification procedure

The recipient shall perform the following steps to decrypt and verify an $(l+t)$-bit authenticated-encrypted string AM.

Inputs:

— $AM = C_0\, C_1 \ldots C_{l-1} \,||\, MAC$, where AM is an $l+t$-bit string.

— IV is a 96-bit string that was pre-shared between recipients.

— The 128-bit key, K, that was securely shared beforehand.

Output: $P = P_0\, P_1 \ldots P_{l-1}$, the decryption of C or INVALID in case of incorrect MAC

a) If the length of AM is less than t bits then halt and output INVALID. Otherwise let:

— $n = \mathrm{Len}(AM)$

— $l = n\text{-}t$

— $C = AM|_l$, where $C = C_0\, C_1 \ldots C_{l-1}$ and where C_i are bits for $i = 0, \ldots, l\text{-}1$

— $MAC' = AM|^t$

b) Initialize the state variable with key, K, and the initialization value, IV:

— $S^{(-2*t)} = \mathbf{Init}(K, IV)$

c) Initialize the MAC variable:

— $(AUTH^{(0)}, S^{(0)}) = \mathbf{Imac}(S^{(-2*t)})$

d) For every bit of the message C_i, generate a key stream bit $Z^{(i)}$ and update the MAC state.

- For 0, 1, …, l-1:
  - Decrypt the message bit:
    - $Y^{(2*i)} = \mathbf{Prt}(S^{(2*i)})$
    - $Z^{(i)} = \mathbf{Strm}(Y^{(2*i)})$
    - $P_i = C_i \oplus Z^{(i)}$
    - $S^{(2*i+1)} = \mathbf{Next}(S^{(2*i)})$
  - Update the MAC variable:
    - $Y^{(2*i+1)} = \mathbf{Prt}(S^{(2*i+1)})$
    - $AUTH^{(i+1)} = \mathbf{Upmac}(AUTH^{(i)}, Y^{(2*i+1)}, P_i)$
    - $S^{(2*i+2)} = \mathbf{Next}(S^{(2*i+1)})$

e) Finalize the MAC computation:

- $MAC = \mathbf{Fmac}(AUTH^{(i)})$

f) Compare *MAC'* and the computed *MAC*:

- If *MAC* = *MAC'* continue, otherwise halt and output INVALID.

g) Output the l-bit string $P = P_0\, P_1 \ldots P_{l-1}$.

5.5 Sub-functions

5.5.1 Initialization function Init

The internal state $S^{(i)}$ of Grain-128A is initialized using the following **Init** function. The NLFSR is fed with the key. The LFSR is fed with the initialization value *IV* appended with a 32-bit fixed padding value $1^{31}0^1$. The first bit of the *IV* is forced to 1 to indicate the authenticated mode ($IV_0 = 1$). As a warm-up, the state is clocked 256 times where the pre-output is reinjected into the two shift registers LFSR and NLFSR.

Inputs: 128-bit key *K*, 96-bit initialization value, *IV*.

Output: The state variable $S^{(-2*t)}$

a) Set the $NLFSR^{(-256-2*t)}$ registers as follows:

- For i = 0, …, 127, set $b_i^{(-256-2*t)} = K_i$

b) Set the $LFSR^{(-256-2*t)}$ register as follows:

- For i = 0, …,95, set $s_i^{(-256-2*t)} = IV_i$
- For i = 96, …,126, set $s_i^{(-256-2*t)} = 1$
- $s_{127}^{(-256-2*t)} = 0$

c) Clock 256 times

- For i = 0, …, 255:

$Y^{(-256-2*t+i)} = \mathbf{Prt}(S^{(-256-2*t+i)})$

$S^{(-255-2*t+i)} = \mathbf{Next}(S^{(-256-2*t+i)})$

$b_{127}^{(-255-2*t+i)} = b_{127}^{(-255-2*t+i)} \oplus Y^{(-256-2*t+i)}$

$s_{127}^{(-255-2*t+i)} = s_{127}^{(-255-2*t+i)} \oplus Y^{(-256-2*t+i)}$

d) Output: $S^{(-2*t)}$

Figure 2 illustrates the function **Init**. It shows how the two registers LFSR and NLFSR are updated and how the pre-output stream is generated. The double lines indicate that the pre-output stream bits are reinjected into the LSFR and the NLFSR during the **Init** procedure.

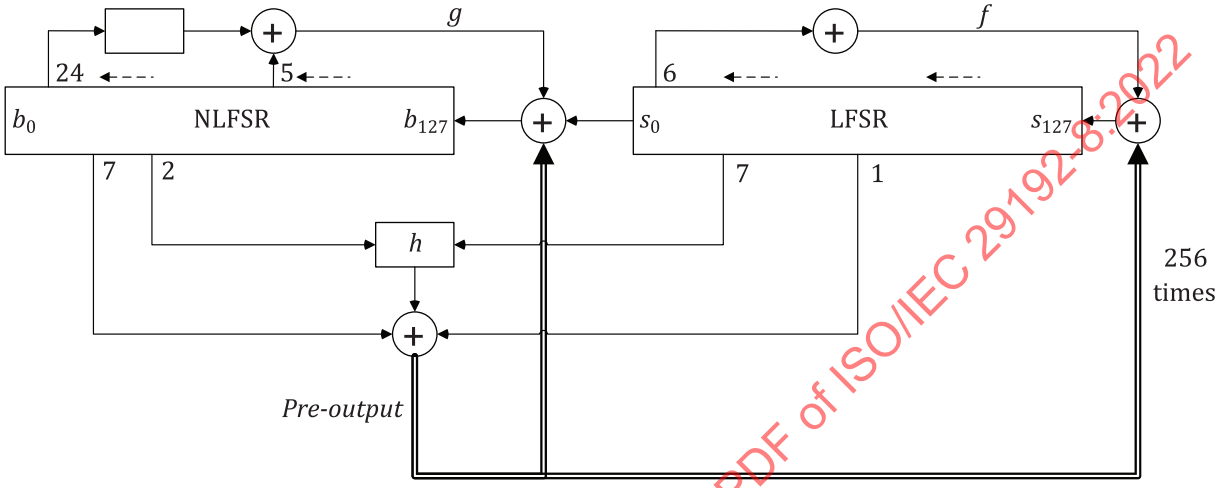

Figure 2 — Initialization of Grain-128A

5.5.2 MAC Initialization function Imac

The MAC state $AUTH^{(i)}$ is initialized using the following **Imac** function. The ACCU buffer is set with the t first pre-output bits $Y^{(i)}$ generated after the initialization. The SHIFT register is set with the next t pre-output bits $Y^{(i)}$. It also updates the state S.

Input: $S^{(-2*t)}$

Outputs: $AUTH^{(0)}$, $S^{(0)}$

a) Set the $ACCU^{(0)}$ register as follows:

— For $i = 0, \ldots, t\text{-}1$:

$a_i^{(0)} = \mathbf{Prt}(S^{(-2*t+i)})$

$S^{(-2*t+i+1)} = \mathbf{Next}(S^{(-2*t+i)})$

b) Set the $SHIFT^{(0)}$ register as follows:

— For $i = 0, \ldots, t\text{-}1$:

$r_i^{(0)} = \mathbf{Prt}(S^{(-t+i)})$

$S^{(-t+i+1)} = \mathbf{Next}(S^{(-t+i)})$

Output: $AUTH^{(0)}$ and $S^{(0)}$.

5.5.3 Next-state function Next

LFSR is a linear shift register with the feedback polynomial f.

$$f(X) = 1 + X^{32} + X^{47} + X^{58} + X^{90} + X^{121} + X^{128}$$

NLFSR is a nonlinear shift register with the feedback polynomial g.

$$g(X) = 1 + X^{32} + X^{37} + X^{72} + X^{102} + X^{128} + X^{44}X^{60} + X^{61}X^{125} + X^{63}X^{67} + X^{69}X^{101} + X^{80}X^{88} + X^{110}X^{111} + X^{115}X^{117} + X^{46}X^{50}X^{58} + X^{103}X^{104}X^{106} + X^{33}X^{35}X^{36}X^{40}$$

In addition, $s_0^{(i)}$ bit of the LFSR is injected into the NLFSR adding $s_0^{(i)}$ to the NLFSR feedback g.

Input: $S^{(i)}$.

Output: $S^{(i+1)}$.

a) Compute the feedback g of NLFSR$^{(i)}$ as follows:

— $b_{127}^{(i+1)} = s_0^{(i)} \oplus b_0^{(i)} \oplus b_{26}^{(i)} \oplus b_{56}^{(i)} \oplus b_{91}^{(i)} \oplus b_{96}^{(i)} \oplus (b_3^{(i)}$ AND $b_{67}^{(i)}) \oplus (b_{11}^{(i)}$ AND $b_{13}^{(i)}) \oplus (b_{17}^{(i)}$ AND $b_{18}^{(i)}) \oplus (b_{27}^{(i)}$ AND $b_{59}^{(i)}) \oplus (b_{40}^{(i)}$ AND $b_{48}^{(i)}) \oplus (b_{61}^{(i)}$ AND $b_{65}^{(i)}) \oplus (b_{68}^{(i)}$ AND $b_{84}^{(i)}) \oplus (b_{88}^{(i)}$ AND $b_{92}^{(i)}$ AND $b_{93}^{(i)}$ AND $b_{95}^{(i)}) \oplus (b_{22}^{(i)}$ AND $b_{24}^{(i)}$ AND $b_{25}^{(i)}) \oplus (b_{70}^{(i)}$ AND $b_{78}^{(i)}$ AND $b_{82}^{(i)})$

b) Compute the feedback f of LFSR$^{(i)}$ as follows:

— $s_{127}^{(i+1)} = s_0^{(i)} \oplus s_7^{(i)} \oplus s_{38}^{(i)} \oplus s_{70}^{(i)} \oplus s_{81}^{(i)} \oplus s_{96}^{(i)}$

c) Shift the two registers:

— For $j = 0$ to 126, set $b_j^{(i+1)} = b_{j+1}^{(i)}$

— For $j = 0$ to 126, set $s_j^{(i+1)} = s_{j+1}^{(i)}$

d) Output $S^{(i+1)}$.

5.5.4 Pre-output function Prt

The **Prt** function generates a pre-output bit $Y^{(i)}$ from the state $S^{(i)}$ using a Boolean function.

Input: $S^{(i)}$.

Output: Pre-output bit $Y^{(i)}$.

a) Compute h as follows:

— $h = (b_{12}^{(i)}$ AND $s_8^{(i)}) \oplus (s_{13}^{(i)}$ AND $s_{20}^{(i)}) \oplus (b_{95}^{(i)}$ AND $s_{42}^{(i)}) \oplus (s_{60}^{(i)}$ AND $s_{79}^{(i)}) \oplus (b_{12}^{(i)}$ AND $b_{95}^{(i)}$ AND $s_{94}^{(i)})$

b) Compute $Y^{(i)}$ as follows:

— $Y^{(i)} = h \oplus b_2^{(i)} \oplus b_{15}^{(i)} \oplus b_{36}^{(i)} \oplus b_{45}^{(i)} \oplus b_{64}^{(i)} \oplus b_{73}^{(i)} \oplus b_{89}^{(i)} \oplus s_{93}^{(i)}$

c) Output $Y^{(i)}$.

5.5.5 Keystream function Strm

The **Strm** function generates the keystream bit $Z^{(i)}$ to add with the message to encrypt or decrypt.

Input: $Y^{(2*i)}$.

Output: $Z^{(i)}$.

a) Compute the keystream bit $Z^{(i)}$

— $Z^{(i)} = Y^{(2*i)}$

b) Output $Z^{(i)}$.

5.5.6 Function Upmac

The MAC registers are updated as a function of the message bit M_i and the pre-output bit $Y^{(2*i+1)}$.

Inputs: $AUTH^{(i)}$, $Y^{(2*i+1)}$, M_i.

Output: $AUTH^{(i+1)}$.

a) Update the $ACCU^{(i)}$ register:

— For $j = 0$ to t-1, set $a_j^{(i+1)} = a_j^{(i)} \oplus (M_i \text{ AND } r_j^{(i)})$

b) Update the $SHIFT^{(i)}$ register:

— For $j = 0$ to t-2, set $r_j^{(i+1)} = r_{j+1}^{(i)}$

— $r_{t-1}^{(i+1)} = Y^{(2*i+1)}$

c) Output $AUTH^{(i+1)}$.

5.5.7 Function Fmac

The **Fmac** function completes the MAC computation. It pads the message with an additional bit set to 1, which is equivalent to add bitwise the SHIFT and the ACCU registers. The final MAC is the new value of the ACCU register.

Input: $AUTH^{(i)}$.

Output: *MAC*

a) Update the $ACCU^{(i)}$ register as follows:

— For $j = 0$ to t-1, set $a_j^{(i+1)} = a_j^{(i)} \oplus r_j^{(i)}$

b) Output $MAC = ACCU^{(i+1)}$.

Annex A
(normative)

Object identifiers

This annex lists the object identifiers assigned to the lightweight authenticated encryption algorithms specified in this document.

```
LightweightCryptography-8 {
iso(1) standard(0) lightweight-cryptography(29192) part8(8)
asn1-module(0) algorithm-object-identifiers(0)}

DEFINITIONS EXPLICIT TAGS::= BEGIN
-- EXPORTS All; --
-- IMPORTS None; --

OID::= OBJECT IDENTIFIER -- Alias

-- Synonyms --
is29192-8 OID::= {iso(1) standard(0) lightweight-cryptography(29192) part8(8)}

-- Lightweight authenticated encryption mechanisms

lightweight-Authenticated-Encryption OID::= {is29192-8 mechanism(1)}

grain-128A OID::= {lightweight-Authenticated-Encryption 1}

LightweightCryptographyIdentifier::= SEQUENCE {
   algorithm ALGORITHM.&id({AuthenticatedEncryptionAlgorithms}),
   parameters
ALGORITHM.&Type({AuthenticatedEncryptionAlgorithms }{@algorithm})
    OPTIONAL
}

authenticatedEncryption ALGORITHM::= {
  PARMS  MacLengthID
  DYN-PARMS InitializationVector
  IDENTIFIED BY grain-128A }

AuthenticatedEncryptionAlgorithms ALGORITHM::= {
  authenticatedEncryption, ... }

MacLength::= INTEGER

MacLengthID::= CHOICE {
  int MacLength,
  oid OID
}

InitializationVector::= OCTET STRING (SIZE (12))

-- ALGORITHM information object class
ALGORITHM::= CLASS {
  &Type  OPTIONAL,
  &DynParms OPTIONAL,
  &id OBJECT IDENTIFIER UNIQUE }
WITH SYNTAX {
  [PARMS &Type]
  [DYN-PARMS&DynParms ]
  IDENTIFIED BY &id }

END
```