

INTERNATIONAL STANDARD



**Fixed capacitors for use in electronic equipment –
Part 22: Sectional specification – Fixed surface mount multilayer capacitors of
ceramic dielectric, Class 2**

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**Fixed capacitors for use in electronic equipment –
Part 22: Sectional specification – Fixed surface mount multilayer capacitors of
ceramic dielectric, Class 2**

INTERNATIONAL
ELECTROTECHNICAL
COMMISSION

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INTERNATIONAL ELECTROTECHNICAL COMMISSION

FIXED CAPACITORS FOR USE IN ELECTRONIC EQUIPMENT –**Part 22: Sectional specification –
Fixed surface mount multilayer capacitors of ceramic dielectric, Class 2**

FOREWORD

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International Standard IEC 60384-22 has been prepared by IEC technical committee 40: Capacitors and resistors for electronic equipment.

This third edition cancels and replaces the second edition published in 2011. This edition constitutes a technical revision.

This edition includes the following significant technical changes with respect to the previous edition:

- a) revision of the structure in accordance with ISO/IEC Directives, Part 2:2016 (seventh edition) to the extent practicable, and for harmonizing with IEC 60384-21;
- b) deletion of the description on the permissible reactive power in 6.2.2 because it is not appropriate for the purposes of this document;
- c) the dimensions of 0201M in Annex A have been added.

The text of this International Standard is based on the following documents:

FDIS	Report on voting
40/2640/FDIS	40/2652/RVD

Full information on the voting for the approval of this International Standard can be found in the report on voting indicated in the above table.

This document has been drafted in accordance with the ISO/IEC Directives, Part 2.

A list of all parts in the IEC 60384 series, published under the general title *Fixed capacitors for use in electronic equipment*, can be found on the IEC website.

The committee has decided that the contents of this document will remain unchanged until the stability date indicated on the IEC website under "<http://webstore.iec.ch>" in the data related to the specific document. At this date, the document will be

- reconfirmed,
- withdrawn,
- replaced by a revised edition, or
- amended.

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FIXED CAPACITORS FOR USE IN ELECTRONIC EQUIPMENT –

Part 22: Sectional specification – Fixed surface mount multilayer capacitors of ceramic dielectric, Class 2

1 ~~General~~

1 Scope

This part of IEC 60384 is applicable to fixed unencapsulated surface mount multilayer capacitors of ceramic dielectric, Class 2, for use in electronic equipment. These capacitors have metallized connecting pads or soldering strips and are intended to be mounted on printed boards, or directly onto substrates for hybrid circuits.

Capacitors for electromagnetic interference suppression are not included, but are covered by IEC 60384-14.

1.2 ~~Object~~

The object of this document is to prescribe preferred ratings and characteristics and to select from IEC 60384-1 the appropriate quality assessment procedures, tests and measuring methods and to give general performance requirements for this type of capacitor. Test severities and requirements prescribed in detail specifications referring to this sectional specification ~~should be~~ are of equal or higher performance levels; lower performance levels are not permitted.

2 Normative references

The following documents are referred to in the text in such a way that some or all of their content constitutes requirements of this document. For dated references, only the edition cited applies. For undated references, the latest edition of the referenced document (including any amendments) applies.

IEC 60063:~~1963~~, Preferred number series for resistors and capacitors
~~Amendment 1 (1967)~~
~~Amendment 2 (1977)~~

IEC 60068-1:~~1988~~ 2013, Environmental testing – Part 1: General and guidance
~~Amendment 1 (1992)~~

IEC 60068-2-58:~~2004~~ 2015, Environmental testing – Part 2-58: Tests – Test Td – Test methods for solderability, resistance to dissolution of metallization and to soldering heat of surface mounting devices (SMD)
IEC 60068-2-58:2015/AMD1:2017

IEC 60384-1:~~2008~~ 2016, Fixed capacitors for use in electronic equipment – Part 1: Generic specification

IEC 61193-2:2007, Quality assessment systems – Part 2: Selection and use of sampling plans for inspection of electronic components and packages

ISO 3:1973, Preferred numbers – Series of preferred numbers

3 Terms and definitions

For the purposes of this document, the terms and definitions given in IEC 60384-1 and the following apply.

ISO and IEC maintain terminological databases for use in standardization at the following addresses:

- IEC Electropedia: available at <http://www.electropedia.org/>
- ISO Online browsing platform: available at <http://www.iso.org/obp>

3.1

surface mount multilayer capacitor

multilayer capacitor whose small dimensions and nature or shape of terminations make it suitable for surface mounting in hybrid circuits and on printed boards

3.2

fixed capacitors, capacitor of ceramic dielectric, Class 2

capacitor that has a dielectric with a high permittivity and is suitable for by-pass and coupling applications or for frequency-discriminating circuits where low losses and high stability of capacitance are not of major importance

Note 1 to entry: The ceramic dielectric is characterized by a non linear change of capacitance over the category temperature range (see Table 3).

3.3

subclass

maximum percentage change of capacitance within the category temperature range with respect to the capacitance at 20 °C

Note 1 to entry: The subclass may be expressed in code form (see Table 3).

3.4

category temperature range

~~range of~~ ambient temperatures range for which the capacitor has been designed to operate continuously

Note 1 to entry: This is given by the lower and upper category temperature.

3.5

rated temperature

T_R

maximum ambient temperature at which the rated voltage may be continuously applied

3.6

rated d.c. voltage

U_R

maximum DC voltage that may be applied continuously to a capacitor at any temperature between the lower category temperature and the rated temperature

Note 1 to entry: The maximum DC voltage is the sum of the DC voltage and peak AC voltage or peak pulse voltage applied to the capacitor.

3.7

category voltage

U_C

maximum voltage that ~~may~~ can be applied continuously to a capacitor at its upper category temperature

4 Information to be given in a detail specification

4.1 General

The detail specifications shall be derived from the relevant blank detail specification.

Detail specifications shall not specify requirements inferior to those of the generic, sectional or blank detail specification. When more severe requirements are included, they shall be listed in 1.9 of the detail specification and indicated in the test schedules, for example by an asterisk.

NOTE The information given in 4.2 may ~~for convenience~~, be presented in tabular form if more convenient.

The information in 4.2 to 4.5 shall be given in each detail specification and the values quoted ~~shall preferably~~ should be selected from those given in the appropriate clause of this sectional specification.

4.2 Outline drawing and dimensions

There shall be an illustration of the capacitors as an aid to easy recognition and for comparison of the capacitors with others.

Dimensions and their associated tolerances, which affect interchangeability and mounting, shall be given in the detail specification. All dimensions shall ~~preferably~~ be stated in millimetres, however, when the original dimensions are given in inches, the converted metric dimensions in millimetres shall be added.

Normally the numerical values shall be given for the length, width and height of the body. When necessary, for example when a number of items (sizes and capacitance/voltage ranges) are covered by a detail specification, the dimensions and their associated tolerances shall be placed in a table below the drawing.

When the configuration is other than described above, the detail specification shall state such dimensional information as will adequately describe the capacitors.

4.3 Mounting

The detail specification shall give guidance on methods of mounting for normal use. Mounting for test and measurement purposes (when required) shall be in accordance with 8.4 of this sectional specification.

4.4 Rating and characteristics

4.4.1 General

The ratings and characteristics shall be in accordance with the relevant clauses of this sectional specification, together with 4.4.2, 4.4.3 and 4.4.4.

4.4.2 Nominal capacitance range

See 6.2.4.1.

NOTE When products approved to the detail specification have different ranges, the following statement should be added: "The range of capacitance values available in each voltage range is given in the register of approvals, available for example on the IECQ on-line certificate system website www.iecq.org".

4.4.3 Particular characteristics

Additional characteristics may be listed, when they are considered necessary to specify adequately the component for design and application purposes.

4.4.4 Soldering

The detail specification shall prescribe the test methods, severities and requirements applicable for the solderability and the resistance to soldering heat tests.

4.5 Marking

The detail specification shall specify the content of the marking on the capacitor and on the packaging. Deviations from Clause 5 of this sectional specification shall be specifically stated.

5 Marking

5.1 General

See IEC 60384-1:2016, 2.4, with the details of 5.2 to 5.6.

5.2 Information for marking

The information given in the marking is normally selected from the following list; the relative importance of each item is indicated by its position in the list:

- nominal capacitance;
- rated voltage (DC voltage may be indicated by the symbol: $\overline{\quad}$ [IEC 60417-5031(2002-10)] or —);
- tolerance on nominal capacitance;
- dielectric subclass as applicable (in accordance with 6.2.5);
- year and month (or week) of manufacture;
- manufacturer's name or trade mark;
- climatic category;
- manufacturer's type designation;
- reference to the detail specification.

5.3 Marking on the body

These capacitors are generally not marked on the body. If some marking can be applied, they shall be clearly marked with as many as possible of the items stated in 5.2 as is considered useful. Any duplication of information in the marking on the capacitor should be avoided.

5.4 Requirements for marking

Any marking shall be legible and not easily smeared or removed by rubbing with fingers.

5.5 Marking of the packaging

The packaging containing the capacitor(s) shall be clearly marked with all the information listed in 5.2.

5.6 Additional marking

Any additional marking shall be so applied that no confusion can arise.

6 Preferred ratings and characteristics

6.1 Preferred characteristics

~~The values given in the detail specification shall preferably be selected from the following.~~

Preferred climatic categories only shall be given in the preferred characteristics.

The capacitors covered by this document are classified into climatic categories in accordance with the general rules given in IEC 60068-1:2013, Annex A.

The lower and upper category temperatures and the duration of the damp heat, steady state test shall be chosen from the following:

- lower category temperature: –55 °C, –40 °C, –25 °C, –10 °C and +10 °C;
- upper category temperature: +70 °C, +85 °C, +100 °C, +125 °C and +150 °C;
- duration of the damp heat,
steady state test (40 °C, 93 % RH): 4, 10, 21 and 56 days.

The severities of the cold and dry heat tests are the lower and upper category temperatures respectively.

NOTE The resistance to humidity resulting from the above climatic category is for the capacitors in their unmounted state. The climatic performance of the capacitors after mounting is greatly influenced by the mounting substrate, the mounting method (see 8.4) and the final coating.

6.2 Preferred values of ratings

6.2.1 Rated temperature (T_R)

The rated temperature is equal to the upper category temperature for capacitors with the upper category temperature not exceeding 125 °C, unless otherwise stated in the detail specification.

6.2.2 Rated voltage (U_R)

The preferred values of the rated voltage are the values of the R5 series of ISO 3. If other values are needed they shall be chosen from the R10 series.

The sum of the DC voltage and the peak AC voltage or the peak to peak AC voltage, whichever is the greater, applied to the capacitor shall not exceed the rated voltage. ~~The value of the peak a.c. voltage shall not exceed the value determined by the permissible reactive power.~~

6.2.3 Category voltage (U_C)

The category voltage is equal to the rated voltage for capacitors with the upper category temperature not exceeding 125 °C. Any category voltages which are different from the rated voltage, for capacitors with the upper category temperature exceeding 125 °C or for high-voltage capacitors with rated voltages ~~above~~ about 500 V, shall be given in the detail specification.

The preferred values of the category voltage at 125 °C upper category temperature for high volumetric capacitors with a rated voltage of 16 V and less and a rated temperature of 85 °C are given in Table 1.

Table 1 – Preferred values of category voltages

U_R	V	2,5	4	6,3	10	16
U_C	V	1,6	2,5	4	6,3	10
NOTE The numeric values of U_C are calculated by the following: $U_C = 0,63 \times U_R$						

6.2.4 Preferred values of nominal capacitance and associated tolerance values**6.2.4.1 Preferred values of nominal capacitance**

Nominal capacitance values shall be taken from the number series of IEC 60063; the E3, E6 and E12 series are preferred.

6.2.4.2 Preferred tolerances on nominal capacitance

See Table 2.

Table 2 – Preferred tolerances

Preferred series	Tolerance %	Letter code
E3 and E6	-20/+80	Z
	-20/+50	S
E6	± 20	M
E6 and E12	± 10	K

6.2.5 Temperature characteristic of capacitance

Table 3 denotes with a cross the preferred values of the temperature characteristic with and without a DC voltage applied. The method of coding the subclass is also given; for example a dielectric with a percentage change of ±20 % without DC voltage applied over the temperature range from -55 °C to +125 °C will be defined as a dielectric of subclass 2C1.

The temperature range for which the temperature characteristic of the dielectric is defined is the same as the category temperature range.

Table 3 – Temperature characteristic of capacitance

Sub-class letter code	Maximum capacitance change within the category temperature range with respect to the capacitance at 20 °C measured with and without a DC voltage applied %		Category temperature range and corresponding number code					
			-55/+150 °C	-55/+125 °C	-55/+85 °C	-40/+85 °C	-25/+85 °C	+10/+85 °C
	without DC voltage applied	with DC voltage applied (NOTE 1)	0	1	2	3	4	6
2B	±10	Requirements specified in the detail specification			x	x	x	
2C	±20			x	x	x		
2D	+20/-30			x			x	
2E	+22/-56				x	x	x	x
2F	+30/-80				x	x	x	x
2R	±15			x	x		x	
				x	x	x		
<p>NOTE When the upper category temperature is above 125 °C, the limits of capacitance change, both with and without DC voltage applied, should be given in the detail specification.</p> <p>NOTE 1 DC voltage applied is either rated voltage or the voltage specified in the detail specification.</p> <p>NOTE 2 "x" indicates preferred.</p>								

NOTE See Annex C for the reference temperature of 25 °C as an informative guidance.

6.2.6 Dimensions

Suggested rules for the specification and coding of dimensions are given in Annex A.

Specific dimensions shall be given in the detail specification.

7 Quality assessment procedures

7.1 Primary stage of manufacture

The primary stage of manufacture is the first common firing of the dielectric-electrode assembly.

7.2 Structurally similar components

Capacitors considered as being structurally similar are capacitors produced with similar processes and materials, though they may be of different case sizes and values.

7.3 Certified records of released lots

The information required in IEC 60384-1:2016, ~~Q.9~~ Q.1.5, shall be made available when prescribed in the detail specification and when requested by a purchaser. After the endurance test, the parameters for which variables information is required are the capacitance change, tan δ and the insulation resistance.

7.4 Qualification approval

7.4.1 General

The procedures for qualification approval testing are given in IEC 60384-1:2016, Clause ~~Q.5~~ Q.2.

The schedule to be used for qualification approval testing on the basis of lot-by-lot and periodic tests is given in 7.5. The procedure using a fixed sample size schedule is given in 7.4.2 and 7.4.3.

7.4.2 Qualification approval on the basis of the fixed sample size procedures

The fixed sample size procedure is described in IEC 60384-1:2016, ~~Q.5.3, b)~~ Q.2.4. The sample shall be representative of the range of capacitors for which approval is sought. This may or may not be the complete range covered by the detail specification.

For each temperature characteristic, the sample shall consist of specimens of capacitors of maximum and minimum size and for each of these sizes, the maximum capacitance value for the highest rated voltage and minimum rated voltage of the voltage ranges for which approval is sought. When there are more than four rated voltages, an intermediate voltage shall also be tested. Thus, for the approval of a range, testing is required of either four or six values (capacitance/voltage combinations) for each temperature characteristic. Where the total range consists of ~~less~~ fewer than four values, the number of specimens to be tested shall be that required for four values.

In case assessment level EZ is used, spare specimens are permitted as follows:

Two (for six values) or three (for four values) per value may be used as replacements for specimens that are non-conforming because of incidents not attributable to the manufacturer.

The numbers given in Group 0 assume that all groups are applicable. If this is not so, the numbers may be reduced accordingly.

When additional groups are introduced into the qualification approval test schedule, the number of specimens required for Group 0 shall be increased by the same number as that required for the additional groups.

Table 4 gives the number of samples to be tested in each group or subgroup together with the number of permissible non-conformances for the qualification approval test.

7.4.3 Tests

The complete series of tests specified in Table 4 and Table 5 are required for the approval of capacitors covered by one detail specification. The tests of each group shall be carried out in the order given.

The whole sample shall be subjected to the tests of Group 0 and then divided for the other groups.

Non-conforming specimens found during the tests of Group 0 ~~(according to Table 4)~~ shall not be used for the other groups.

"One non-conforming item" is counted when a capacitor has not satisfied the whole or a part of the tests of a group.

The approval is granted when the number of non-conforming items ~~do not exceed the specified number of permissible non-conforming items for each group or subgroup and the total number of permissible non-conformances~~ is zero.

NOTE Table 4 and Table 5 together form the fixed sample size test schedule. Table 4 includes the details for the sampling and permissible non-conforming items for the different tests or groups of tests. Table 5 together with the details of the test contained in Clause 8 gives a complete summary of test conditions and performance requirements and indicates where, for

example for the test method or conditions of test, a choice ~~has to~~ shall be made in the detail specification.

The conditions of test and performance requirements for the fixed sample size test schedule ~~should~~ shall be identical to those prescribed in the detail specification for quality conformance inspection.

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Table 4 – Fixed sample size test plan for qualification approval – Assessment level EZ

Group No.	Test	Subclause of this publication	Number of specimens <i>n</i> ^e	Permissible number of nonconforming items <i>c</i>
0	Visual examination	8.5	132 + 24 ^f	0
	Dimensions	8.5		
	Capacitance	8.6.1		
	Tangent of loss angle	8.6.2		
	Insulation resistance	8.6.3		
	Voltage proof	8.6.4		
	Spare specimens		12	
1A	Robustness of termination ^g	8.16	12	0
	Resistance to soldering heat	8.10		
	Component solvent resistance ^b	8.17		
1B	Impedance ^b	8.6.5	12	0
	Equivalent series resistance [ESR] ^b	8.6.6		
	Solderability	8.11		
	Solvent resistance of marking ^b	8.18		
2	Substrate bending test ^d	8.9	12	0
3 ^a	Mounting	8.4	84 + 24 ^f	0 ^c
	Visual examination	8.5		
	Capacitance	8.6.1		
	Tangent of loss angle	8.6.2		
	Insulation resistance	8.6.3		
	Voltage proof	8.6.4		
3.1	Shear test ^h	8.8	24	0
	Rapid change of temperature	8.12		
	Climatic sequence	8.13		
3.2	Damp heat, steady state	8.14	24	0
3.3	Endurance	8.15	36	0
3.4	Accelerated damp heat, steady state ^b	8.19	24 ^f	0
4	Temperature characteristic of capacitance	8.7	12	0
^a	The values of these measurements serve as initial measurements for the tests of Group 3.			
^b	If required in the detail specification.			
^c	The capacitors found non-conforming items after mounting shall not be taken into account when calculating the permissible non-conforming for the following tests. They shall be replaced by spare capacitors.			
^d	Not applicable to capacitors, which, in accordance with their detail specification, shall only be mounted on alumina substrates.			
^e	Capacitance/voltage combinations, see 7.4.2.			
^f	Additional capacitors, if Group 3.4 is tested.			
^g	Applicable to capacitors with strip terminations.			
^h	Not applicable to capacitors with strip terminations.			

Table 5 – Tests schedule for qualification approval

Subclause number and test (see NOTE 1)	D or ND	Conditions of test (see NOTE 1)	Number of specimens (n) and number of non-conforming items (c)	Performance requirements (see NOTE 1)
GROUP 0 8.5 Visual examination 8.5 Dimension (detail) 8.6.1 Capacitance 8.6.2 Tangent of loss angle (tan δ) 8.6.3 Insulation resistance 8.6.4 Voltage proof	ND	Frequency: ... Hz Measuring voltage: ... V RMS Frequency and Measuring voltage same as in 8.6.1 See detail specification for the method See detail specification for the method	See Table 4	As in 8.5.3 Legible marking and as specified in the detail specification See the detail specification Within specified tolerance As in 8.6.2.3 As in 8.6.3.4 No breakdown or flashover
GROUP 1A 8.16 Robustness of termination (if applicable) 8.10.3 Initial measurement 8.10 Resistance to soldering heat 8.10.6 Final measurement 8.17 Component solvent resistance (if required)	D	Test U _{a1} , Force: 2,5 N Test U _b , Method 1, Force: 2,5 N, Number of bends: 1 Visual examination Capacitance Special preconditioning as in 8.2 See detail specification for the method Recovery: (24 ± 2) h Visual examination Capacitance Solvent: ... Solvent temperature: ... Method 2 Recovery: ...	See Table 4	No visible damage As in 8.10.6 As in 8.10.6 See detail specification
GROUP 1B 8.6.5 Impedance (if required) 8.6.6 ESR (if required) 8.11 Solderability 8.11.4 Final measurements 8.18 Solvent resistance of the marking ^a (if required)	D	Frequency: 100 kHz Frequency: 100 kHz See detail specification for the method Visual examination Solvent: ... Solvent temperature: ... Method 1 Rubbing material: cotton wool Recovery: ...	See Table 4	See detail specification See detail specification As in 8.11.4 Legible marking

Subclause number and test (see NOTE 1)	D or ND	Conditions of test (see NOTE 1)	Number of specimens (n) and number of non-conforming items (c)	Performance requirements (see NOTE 1)
GROUP 2 8.9 Substrate bending test 8.9.2 Initial measurement 8.9.3 Final inspection	D	Deflection: ... Number of bends: ... Capacitance Capacitance (with printed board in bent position) Visual examination	See Table 4	See detail specification $ \Delta C/C \leq 10\%$ No visible damage
GROUP 3 8.4 Mounting	D	Substrate material: ... ^b Visual examination Capacitance Tangent of loss angle Insulation resistance Voltage proof	See Table 4	As in 8.5.3 Within specified tolerance As in 8.6.2.3 As in 8.6.3.4 No breakdown or flashover
GROUP 3.1 8.8 Shear test 8.12.3 Initial measurement 8.12 Rapid change of temperature 8.12.6 Final measurements 8.13 Climatic sequence 8.13.3 Initial Measurement 8.13.4 Dry heat 8.13.5 Damp heat, cyclic, test Db, first cycle 8.13.6 Cold 8.13.7 Damp heat, cyclic, test Db, remaining cycles	D	Visual examination Capacitance Special preconditioning as in 8.2 T_A = Lower category temperature T_B = Upper category temperature Five cycles Duration $t_1 = 30$ min Recovery: (24 ± 2) h Visual examination Capacitance Special preconditioning as in 8.2 Capacitance Temperature: upper category temperature Duration: 16 h Temperature: lower category temperature Duration: 2 h Visual examination Recovery: (24 ± 2) h	See Table 4	No visible damage No visible damage $\Delta C/C$: as in 8.12.6 No visible damage
8.13.8 Final measurements		Visual examination Capacitance Tangent of loss angle Insulation resistance		No visible damage Legible marking $\Delta C/C$: as in 8.13.8 As in 8.13.8 As in 8.13.8

Subclause number and test (see NOTE 1)	D or ND	Conditions of test (see NOTE 1)	Number of specimens (n) and number of non-conforming items (c)	Performance requirements (see NOTE 1)
GROUP 3.2 8.14 Damp heat, steady state 8.14.3 Initial measurement 8.14.6 Final measurements	D	Special preconditioning as in 8.2 Capacitance Recovery: (24 ± 2) h Visual examination Capacitance Tangent of loss angle Insulation resistance	See Table 4	No visible damage Legible marking ΔC/C: as in 8.14.6 As in 8.14.6 As in 8.14.6
GROUP 3.3 8.15 Endurance 8.15.3 Initial measurement 8.15.6 Final measurements	D	Special preconditioning as in 8.2 Duration: ... h Temperature: ...°C Voltage: ...V Capacitance Recovery: (24 ± 2) h Visual examination Capacitance Tangent of loss angle Insulation resistance	See Table 4	No visible damage Legible marking ΔC/C: as in 8.15.6 As in 8.15.6 As in 8.15.6
Group 3.4 8.19 Accelerated damp heat, steady state (if required) 8.19.2 Initial measurement 8.19.5 Final measurement	D	Duration: ... h Temperature: (85 ± 2) °C Humidity: (85 ± 3) % RH Insulation resistance Recovery: (24 ± 2) h Insulation resistance	See Table 4	As in 8.19.2 As in 8.19.5
Group 4 8.7 Temperature characteristic of capacitance	ND	Special preconditioning as in 8.2	See Table 4	ΔC/C: as in 8.7.3
NOTE 1 Subclause numbers of test and performance requirements refer to Clause 8.				
NOTE 2 In this table: D = destructive, ND= non-destructive.				
^a This test may be carried out on capacitors mounted on a substrate.				
^b When different substrate materials are used for the individual subgroup, the detail specification shall indicate which substrate material is used in each subgroup.				

7.5 Quality conformance inspection

7.5.1 Formation of inspection lots

7.5.1.1 Groups A and B inspection

These tests shall be carried out on a lot-by-lot basis.

A manufacturer may aggregate the current production into inspection lots subject to the following safeguards.

- 1) The inspection lot shall consist of structurally similar capacitors (see 7.2).
- 2a) The sample tested shall be representative of the values and the dimensions contained in the inspection lot:
 - in relation to their number;
 - with a minimum of five of any one value.
- 2b) If there are ~~less~~ fewer than five of any one value in the sample the basis for the drawing of samples shall be agreed between the manufacturer and the ~~National Supervising Inspectorate~~¹ certification body (CB).

7.5.1.2 Group C inspection

These tests shall be carried out on a periodic basis.

Samples shall be representative of the current production of the specified periods and shall be divided into small, medium and large sizes. In order to cover the range of approvals in any period, one voltage shall be tested from each group of sizes. In subsequent periods, other sizes and/or voltage ratings in production shall be tested with the aim of covering the whole range.

7.5.2 Test schedule

The schedule for the lot-by-lot and periodic tests for quality conformance inspection is given in Clause 2, ~~Table 4~~ of the blank detail specification.

7.5.3 Delayed delivery

When, in accordance with the procedures of IEC 60384-1:2016, ~~Q.10~~ Q.1.7, re-inspection ~~has to~~ shall be made, solderability and capacitance shall be checked as specified in Groups A and B inspection.

7.5.4 Assessment levels

The assessment level(s) given in the blank detail specification ~~shall preferably~~ should be selected from Table 6 and Table 7.

¹ The term Certification Body (CB) replaces the term National Supervising Inspectorate (NSI), see IECQ 01.

Table 6 – Lot-by-lot inspection

Inspection subgroup ^d	EZ		
	IL ^a	n ^a	c ^a
A0	100 % ^b		
A1	S-4	c	0
A2	S-3	c	0
B1	S-3	c	0
B2	S-2	c	0

^a IL = inspection level
n = sample size
c = permissible number of non-conforming items

^b The inspection shall be performed after removal of nonconforming items by 100 % testing during the manufacturing process. Whether the lot was accepted or not, all samples for sampling inspection shall be ~~performed~~ inspected in order to monitor outgoing quality level by nonconforming items per million ($\times 10^{-6}$).
The sampling level shall be established by the manufacturer, preferably in accordance with IEC 61193-2:2007, Annex A.
In the case where one or more nonconforming items occur in a sample, this lot shall be rejected, but ~~the whole sample shall be inspected and~~ all nonconforming items shall be counted for the calculation of quality level values. Outgoing quality level by nonconforming items per million ($\times 10^{-6}$) values shall be calculated by accumulating inspection data in accordance with the method given in IEC 61193-2:2007, 6.2.

^c Number to be tested: sample size shall be determined in accordance with IEC 61193-2:2007, 4.3.2.

^d The content of the inspection subgroup is described in Clause 2 of the relevant blank detail specification.

Table 7 – Periodic test

Inspection subgroup ^b	EZ		
	p ^a	n ^a	c ^a
C1	3	12	0
C2	3	12	0
C3.1	6	27	0
C3.2	6	15	0
C3.3	3	15	0
C3.4 ^c	6	15	0
C4	6	9	0

^a p = periodicity in months
n = sample size
c = permissible number of non-conforming items

^b ~~Additional capacitors if Subgroup C3.4 is tested.~~

^{e b} The content of the inspection subgroup is described in Clause 2 of the relevant blank details specification.

^c If required.

8 Test and measurement procedures

8.1 General

This clause supplements the information given in IEC 60384-1:2016, Clause 4.

8.2 Special preconditioning

Unless otherwise specified in the detail specification, the special preconditioning, when specified in this sectional specification before a test or a sequence of test, shall be carried out under the following conditions.

Exposure at upper category temperature or at such higher temperature as may be specified in the detail specification during 1 h, followed by recovery during (24 ± 1) h under standard atmospheric conditions for testing.

NOTE Capacitors lose capacitance continuously with time in accordance with a logarithmic law (this is called ageing). However, if the capacitor is heated to a temperature above the Curie point of its dielectric, then "de-ageing" takes place, i.e. the capacitance lost through "ageing" is regained, and "ageing" recommences from the time when the capacitor recools. The purpose of special preconditioning is to bring the capacitor to a defined state regardless of its previous history (see Clause B.4 for further information).

8.3 Measuring conditions

See IEC 60384-1:2016, 4.2.1.

8.4 Mounting

See IEC 60384-1:2016, 4.33.

8.5 Visual examination and check of dimensions

8.5.1 General

See IEC 60384-1:2016, 4.4, with the details of 8.5.2 and 8.5.3.

8.5.2 Visual examination

A visual examination shall be carried out with suitable equipment with approximately 10× magnification and lighting appropriate to the specimen under test and the quality level required.

NOTE The operator should have available facilities for incident or transmitted illumination as well as an appropriate measuring facility.

8.5.3 Requirements

8.5.3.1 General

Quantitative values for the requirements below may be given in the detail or in the manufacturer's specification.

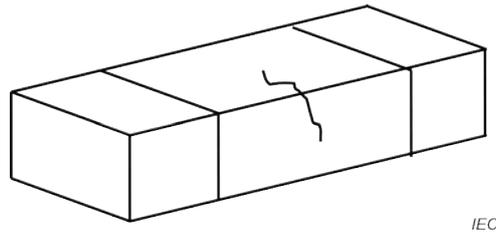
8.5.3.2 Requirements for the ceramic

Requirements for the ceramic are as follows:

- a) Be free of cracks or fissures, except small damages on the surface, that do not deteriorate the performance of the capacitor (examples: see Figure 1 and Figure 2).



Figure 1 – Fault: crack or fissure

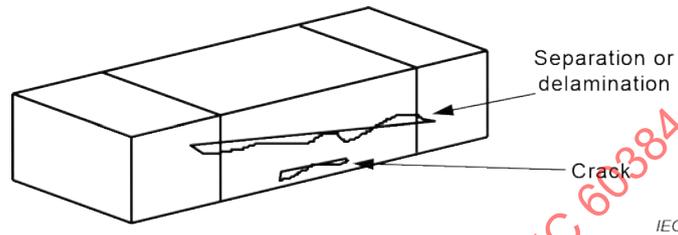


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NOTE Crack or fissure on one side or extending from one face to another over a corner.

Figure 2 – Fault: crack or fissure

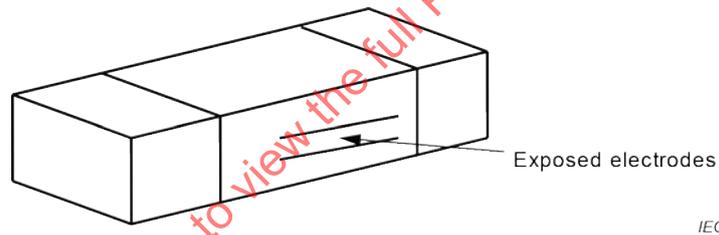
- b) Not exhibit visible separation or delamination between the layers of the capacitor (see Figure 3).



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Figure 3 – Separation or delamination

- c) Not exhibit exposed electrodes between the two terminations (see Figure 4).



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Figure 4 – Exposed electrodes

- d) The ceramic body shall be free of any conducting smears (metallization, tinning, etc.) on a central zone between two adjacent terminations which is equal to the minimum distance between those (Annex A, dimension L_4).

8.5.3.3 Requirements for the metallization

Requirements for the metallization are as follows:

- a) Not exhibit any visible detachment of the metallized terminations and not exhibit any exposed electrodes (see Figure 4).
- b) The principal faces (see Figure 5) are those noted A, B and C.

In the case of capacitors of square section, the faces D and E are also considered principal.

The maximum area of gaps in metallization on each principal face shall not be greater than 15 % of the area of that face; these gaps shall not be concentrated in the same area. The gaps in metallization shall not affect the two principal edges of each extremity of the block (or four edges for square section capacitors). Dissolution of the end face plating (leaching) shall not exceed 25 % of the length of the edge concerned.

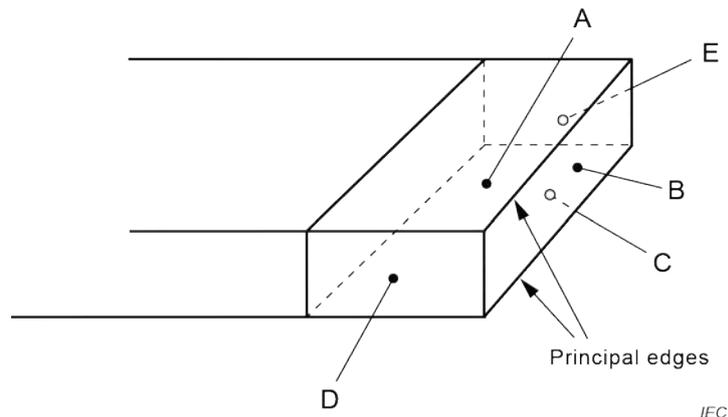


Figure 5 – Principal faces

8.6 Electrical tests

8.6.1 Capacitance

8.6.1.1 General

See IEC 60384-1:2016, 4.7, with the details of 8.6.1.2 and 8.6.1.3.

8.6.1.2 Measuring conditions

See Table 8, unless otherwise specified in the detail specification.

Table 8 – Measuring conditions

Nominal capacitance	Rated voltage U_R	Frequency	Measuring voltage V RMS	Referee voltage V RMS
$C_N < 100 \text{ pF}$	^a	1 MHz	$1,0 \pm 0,2$	$1,0 \pm 0,02$
$100 \text{ pF} \leq C_N \leq 10 \text{ } \mu\text{F}$	$U_R > 6,3 \text{ V}$	1 kHz	$1,0 \pm 0,2$	$1,0 \pm 0,02$
	$U_R \leq 6,3 \text{ V}$	1 kHz	$0,5 \pm 0,2$	$0,5 \pm 0,02$
$C_N > 10 \text{ } \mu\text{F}$	^a	100 Hz or 120 Hz	$0,5 \pm 0,2$	$0,5 \pm 0,02$

^a All rated voltages (U_R).

8.6.1.3 Requirements

The capacitance value as measured in the unmounted state, shall correspond with the rated value taking into account the specified tolerance.

The capacitance as measured in the mounted state in accordance with Group 3 is for reference purposes only in further tests.

For referee measurements, the capacitance value shall be the value extrapolated to an ageing time of 1 000 h, unless otherwise specified in the detail specification (see Annex B for explanations). If applying the ageing time other than 1 000 h, that may be specified in the detail specification.

8.6.2 Tangent of loss angle ($\tan \delta$)

8.6.2.1 General

See IEC 60384-1:2016, 4.8, with the details of 8.6.2.2 and 8.6.2.3.

8.6.2.2 Measuring conditions

The measuring conditions are the same as those of 8.6.1. The inaccuracy of the measuring instruments equipment shall not exceed 1×10^{-3} .

8.6.2.3 Requirements

The tangent of loss angle as measured in the unmounted state shall not exceed the limit given in Table 9.

Table 9 – Tangent of loss angle limits

Rated voltage U_R	Subclass	Tangent of loss angle %
$U_R \geq 10 \text{ V}$	All subclass codes	Not exceed 0,035 or value as may be given in the detail specification
$U_R < 10 \text{ V}$	2B, 2C, 2R	0,1
	2D, 2E	0,15
	2F	0,2
NOTE See 6.2.5 for an explanation of the subclass codes.		

The tangent of loss angle as measured in the mounted state in accordance with Group 3 is for reference purposes only in further tests.

8.6.3 Insulation resistance

8.6.3.1 General

See IEC 60384-1:2016, 4.5, with the details of 8.6.3.2 to 8.6.3.4.

8.6.3.2 Preparation for test

Prior to the test, capacitors shall be carefully cleaned to remove any contamination.

Care shall be taken to maintain cleanliness in the test chambers and during post test measurements. Before the measurement, the capacitors shall be fully discharged. The insulation resistance shall be measured between the terminations.

8.6.3.3 Measuring conditions

See IEC 60384-1:2016, 4.5.2, with the following details.

The measuring voltage may be of any value not greater than U_R , the referee voltage being U_R , for a capacitors with a rated voltages below or equal to 1 kV. For $U_R > 1 \text{ kV}$ the referee voltage shall be 1 kV.

The insulation resistance (R_i) shall be measured after the voltage has been applied for $(60 \pm 5) \text{ s}$.

For lot-by-lot testing (Group A), the test may be terminated in a shorter time, if the required value of insulation resistance is reached.

The product of the internal resistance of the voltage source and the nominal capacitance of the capacitor shall not exceed 1 s, unless otherwise prescribed in the detail specification.

The charge current shall not exceed 0,05 A. For capacitors with rated voltages of 1 kV and above, a lower limit (value) may be given in the detail specification.

8.6.3.4 Requirements

The insulation resistance shall meet the following requirements.

$C_N \leq 25 \text{ nF}$	$R_i \geq 4\,000 \text{ M}\Omega$
$C_N > 25 \text{ nF}$	$R_i \times \epsilon_R C_N \geq 100 \text{ s}$

8.6.4 Voltage proof

8.6.4.1 General

See IEC 60384-1:2016, 4.6, with the details of 8.6.4.2 to 8.6.4.4.

8.6.4.2 Test conditions

The product of R_1 and the nominal capacitance C_X shall be smaller than or equal to 1 s.

NOTE R_1 is a charging resistor that includes the internal resistance of the voltage source. See IEC 60384-1:2016, 4.6.42.

The charge current shall not exceed 0,05 A.

For capacitors with rated voltages of 1 kV and above, a lower charge current limit value may be given in the detail specification. To protect the capacitors against flashover, the test may be performed in a suitable insulating medium.

8.6.4.3 Test voltages

The test voltages in accordance with Table 10 shall be applied between the measuring points of 8.6.3 and Table 3 in IEC 60384-1:2016, for a period of 1 min for qualification approval testing and for a period of 1 s for the lot-by-lot quality conformance testing.

Table 10 – Test voltages

Rated voltage V	Test voltage V
$U_R \leq 100$	$2,5 U_R$
$100 < U_R \leq 200$	$1,5 U_R + 100$
$200 < U_R \leq 500$	$1,3 U_R + 100$
$500 < U_R < 1\,000$	$1,3 U_R$
$U_R \geq 1\,000$	$1,2 U_R$

8.6.4.4 Requirement

There shall be no breakdown or flashover during the test.

8.6.5 Impedance (if required by the detail specification)

8.6.5.1 General

See IEC 60384-1:2016, 4.10, with the details of 8.6.5.2 and 8.6.5.3.

8.6.5.2 Measuring conditions

The frequency of measurement: 100 kHz, with a relative tolerance of ± 10 %.

8.6.5.3 Requirements

Impedance shall be specified in the detail specification.

8.6.6 Equivalent series resistance [ESR] (if required by the detail specification)

8.6.6.1 General

See IEC 60384-1:2016, 4.8.2, with the details of 8.6.6.2 and 8.6.6.3.

8.6.6.2 Measuring conditions

The frequency of measurement: 100 kHz, with a relative tolerance of ± 10 %.

8.6.6.3 Requirements

The ESR shall be specified in the detail specification.

8.7 Temperature characteristic of capacitance

8.7.1 Special preconditioning

See 8.2.

8.7.2 Measuring conditions

See IEC 60384-1:2016, 4.24.1, with the following details.

The capacitors shall be measured in the unmounted state as well as the conditions of Table 11.

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Table 11 – Details of measuring conditions

Measuring step	Temperature °C	DC voltage applied
1	20 ± 2	–
2	$T_A^a \pm 3$	–
3	20 ± 2	–
4	$T_B^b \pm 2$	–
5	$T_B \pm 2$	×
6	20 ± 2	×
7	$T_A \pm 3$	×
8	20 ± 2	–
<p>NOTE Measurements may be made at such intermediate temperatures as to ensure that the requirements of 6.2.5 are met.</p>		
<p>NOTE 1 "–" indicates: no DC voltage applied "×" indicates: DC voltage applied (if specified in the detail specification)</p>		
<p>NOTE 2 Reference capacitance is the capacitance measured at Step 3.</p>		
<p>NOTE 3 Because of the effects described in the Note in 8.2, the capacitance values measured at temperature reference, Steps 5 to 7, with DC voltage applied, are time dependent. This time dependency is included in the given limits for capacitance change. The capacitance change between the first and the last measurements at temperature reference, Steps 1 and 8, indicates the amount of ageing involved. In the case of a dispute about the results of measurements with DC voltage applied, it is advisable to agree upon a fixed time interval between measurements at temperature reference, Steps 5 and 7 with DC voltage applied (see IEC 60384-1:2016, 4.24.1.3).</p>		
<p>^a T_A = Lower category temperature.</p>		
<p>^b T_B = Upper category temperature.</p>		

8.7.3 Requirements

Temperature characteristic with and without DC voltage applied shall not exceed the values given in Table 3. The variation of capacitance shall be calculated in accordance with IEC 60384-1:2016, 4.24.3.1.

8.8 Shear test

See IEC 60384-1:2016, 4.34.

A force shall be selected from 1 N, 2 N, 5 N or 10 N and specified in the detail specification.

8.9 Substrate bending test

8.9.1 General

See IEC 60384-1:2016, 4.35.

Unless otherwise specified in the detail specification,

- the deflection D shall be selected from 1 mm, 2 mm or 3 mm,
- the number of bends shall be 1 time,
- the radius of the bending tool shall be 5 mm.

NOTE When the deflection D is 2 mm or less, the radius may be 230 mm.

- the duration in the bent state shall be 5 s.

For 1005M or smaller size, the thickness of substrate should be 0,8 mm.

8.9.2 Initial measurement

The capacitance shall be measured as specified in 8.6.1 and in the detail specification.

8.9.3 Final inspection

The capacitors shall be visually examined and there shall be no visible damage.

The change of capacitance with board in bent position shall not exceed 10 %.

8.10 Resistance to soldering heat

8.10.1 General

See IEC 60068-2-58 with the details of 8.10.2 to 8.10.6.

8.10.2 Special preconditioning

See 8.2.

8.10.3 Initial measurement

The capacitance shall be measured in accordance with 8.6.1.

8.10.4 Test conditions

8.10.4.1 Solder bath method (applicable to 1608M, 2012M and 3216M)

NOTE See Table A.1 for explanation of the size code.

See IEC 60068-2-58:2015, ~~Clauses 6 and 8~~ Test Td₂, Method 1, with the following details, if not otherwise specified in the detail specification.

The specimen shall be preheated to a temperature of 110 °C to 140 °C and maintained for 30 s to 60 s.

Solder alloy:	Sn-Pb or Sn-Ag-Cu
Temperature:	260 °C ± 5 °C
Duration of immersion:	10 s ± 1 s
Depth of immersion:	10 mm
Number of immersions:	1

8.10.4.2 Infrared and forced gas convection soldering system

See IEC 60068-2-58:2015, ~~Clauses 7 and 8~~ Test Td₂, Method 2, with the following details:

- the solder paste shall be applied to the test substrate;
- the thickness of solder deposit shall be specified in the detail specification;
- the terminations of the specimen shall be placed on the solder paste;
- solder alloy: Sn-Pb;

unless otherwise specified in the detail specification, the specimen and test substrate shall be preheated to a temperature of (150 ± 10) °C and maintained for 60 s to 120 s in infrared and forced gas convection soldering system;

the temperature of the reflow system shall be quickly raised until the specimen has reached $(235 \pm 5) ^\circ\text{C}$ and maintained at this temperature for $(10 \pm 1) \text{ s}$; ~~Number of each test: 1, unless otherwise specified in the detail specification.~~

e) solder alloy: Sn-Ag-Cu;

unless otherwise specified in the detail specification, the reflow temperature profile shall be selected from Table 12 and Figure 6;

Table 12 – Reflow temperature profiles for Sn-Ag-Cu alloy

Alloy composition		T_1 °C	T_2 °C	t_1 s	T_3 °C	t_2 s	T_4 °C	t_3 s
Lead-free solder (Sn-Ag-Cu)	Test 1	150 ± 5	180 ± 5	120 ± 5	220	60 to 90	250	20 to 40 at $T_4 - 5 \text{ K}$
	Test 2	150 ± 5	180 ± 5	120 ± 5	220	≤ 60	255	≤ 20 at $T_4 - 10 \text{ K}$

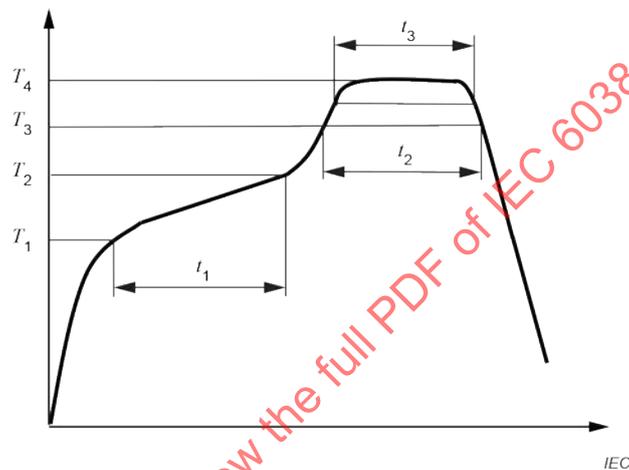


Figure 6 – Reflow temperature profile

- f) number of each test: 1, unless otherwise specified in the detail specification;
g) the temperature profile of d) or e) shall be specified in the detail specification.

8.10.5 Recovery

The capacitors shall recover for $24 \text{ h} \pm 2 \text{ h}$.

The flux residues shall be removed with a suitable solvent.

8.10.6 Final inspection, measurements and requirements

After recovery, the capacitors shall be visually examined and measured and shall meet the following requirements.

Under normal lighting and approximately $10\times$ magnification, there shall be no signs of damage such as cracks.

Dissolution of the end face plating (leaching) shall not exceed 25 % of the length of the edge concerned. The detail specification may prescribe further details.

The capacitance shall be measured in accordance with 8.6.1 and the change shall not exceed the values in Table 13.

Table 13 – Maximum capacitance change

Subclass	Requirements
2B and 2C	±10 %
2D and 2R	±15 %
2E and 2F	±20 %
NOTE See 6.2.5 for an explanation of the subclass codes.	

8.11 Solderability

8.11.1 General

See IEC 60068-2-58 with the details of 8.11.2 to 8.11.4.

8.11.2 Test conditions

~~See IEC 60068-2-58, with the following details.~~

8.11.2.1 Solder bath method (applicable to 1608M, 2012M and 3216M)

NOTE See Table A.1 for explanation of the size code.

See IEC 60068-2-58:2015, ~~Clauses 6 and 8~~ Test Td₁, Method 1, with the following details, if not otherwise specified in the detail specification.

The specimen shall be preheated to a temperature of 80 °C to 140 °C and maintained for 30 s to 60 s.

Solder alloy:	Sn-Pb	Sn-Ag-Cu
Temperature:	(235 ± 5) °C	(245 ± 5) °C
Duration of immersion:	(2 ± 0,2) s	(3 ± 0,3) s
Depth of immersion:	10 mm	10 mm
Number of immersions:	1	1

8.11.2.2 Infrared and forced gas convection soldering system

See IEC 60068-2-58:2015, ~~Clauses 7 and 8~~ Test Td₁, Method 2, with the following details:

- a) the solder paste shall be applied to the test substrate;
- b) the thickness of solder deposit shall be specified in the detail specification;
- c) the terminations of the specimen shall be placed on the solder paste;
- d) solder alloy: Sn-Pb;

unless otherwise specified in the detail specification, the specimen and test substrate shall be preheated to a temperature of (150 ± 10) °C and maintained for 60 s to 120 s in the infrared and forced gas convection soldering system;

the temperature of the reflow system shall be quickly raised until the specimen has reached (215 ± 3) °C and maintained at this temperature for (10 ± 1) s;

- e) solder alloy: Sn-Ag-Cu;

unless otherwise specified in the detail specification, the specimen and test substrate shall be preheated to a temperature of (150 ± 5) °C to (180 ± 5) °C for 60 s to 120 s in the infrared and forced gas convection soldering system;

the temperature of the reflow system shall be quickly raised until the specimen has reached (235 ± 3) °C. The time above 225 °C shall be (20 ± 5) s;

f) the temperature profile of d) or e) shall be specified in the detail specification.

8.11.3 Recovery

The flux residues shall be removed with a suitable solvent.

8.11.4 Final inspection, measurements and requirements

The capacitors shall be visually examined under normal lighting and approximately 10× magnification. There shall be no signs of damage.

Both end face and the contact areas shall be covered with a smooth and bright solder coating with no more than a small amount of scattered imperfections such as pinholes or unwetted or de-wetted areas. These imperfections shall not be concentrated in one area.

The detail specification may prescribe further requirements.

8.12 Rapid change of temperature

8.12.1 General

This test shall be applied only to capacitors for which the category temperature is greater 110 °C.

See IEC 60384-1:2016, 4.16, with the details of 8.12.2 to 8.12.6.

The capacitors shall be mounted in accordance with 8.4.

8.12.2 Special preconditioning

See 8.2.

8.12.3 Initial measurement

The capacitance shall be measured in accordance with 8.6.1.

8.12.4 Number of cycles

The number of cycles: 5

Duration of exposure at the temperature limits: 30 min.

8.12.5 Recovery

The capacitors shall recover for $24 \text{ h} \pm 2 \text{ h}$.

8.12.6 Final inspection, measurements and requirements

The capacitors shall be visually examined. There shall be no visible damage.

The capacitance shall be measured in accordance with 8.6.1 and the change shall not exceed the value in Table 14.

Table 14 – Maximum capacitance change

Subclass	Requirements
2B and 2C	±10 %
2D and 2R	±15 %
2E and 2F	±20 %
NOTE See 6.2.5 for an explanation of the subclass codes.	

8.13 Climatic sequence

8.13.1 General

See IEC 60384-1:2016, 4.21, with the details of 8.13.2 to 8.13.8.

8.13.2 Special preconditioning

See 8.2.

8.13.3 Initial measurement

The capacitance shall be measured in accordance with 8.6.1.

8.13.4 Dry heat

See IEC 60384-1:2016, 4.21.3.

8.13.5 Damp heat, cyclic, Test Db, first cycle

See IEC 60384-1:2016, 4.21.4.

8.13.6 Cold

See IEC 60384-1:2016, 4.21.45, with the following details.

~~4.12.5.1 Final inspection and requirements~~

The capacitors shall be visually examined. There shall be no visible damage.

8.13.7 Damp heat, cyclic, Test Db, remaining cycles

8.13.7.1 General

See IEC 60384-1:2016, 4.21.67, with the details of 8.13.7.2 and 8.13.7.3.

8.13.7.2 Test conditions

No voltage applied.

The remaining cycles shall be tested in accordance with Table 15.

Table 15 – Number of damp heat cycles

Category	No. of cycles of 24 h
- / - /56	5
- / - /21	1
- / - /10	1
- / - /04	0

8.13.7.3 Recovery

The capacitors shall recover for 24 h \pm 2 h.

8.13.8 Final inspection, measurements and requirements

The capacitors shall be visually examined. There shall be no visible damage.

The capacitors shall be measured and shall meet the requirements in Table 16.

If the capacitance value is less than the minimum value permitted, then after the other measurements have been made the capacitor shall be preconditioned in accordance with 8.2 and then the requirement in Table 16 shall be met.

Table 16 – Final inspection, measurements and requirements

Measurement	Measuring conditions	Requirements			
		Subclasses 2B and 2C	Subclasses 2D and 2R	Subclasses 2E	Subclasses 2F
Capacitance	8.6.1	$\Delta C/C \leq \pm 10 \%$	$\Delta C/C \leq \pm 15 \%$	$\Delta C/C \leq \pm 20 \%$	$\Delta C/C \leq \pm 30 \%$
Tangent of loss angle	8.6.2	$\leq 2 \times$ value of 8.6.2			
Insulation resistance	8.6.3	$R_i \geq 1\,000\text{ M}\Omega$ or $R_i \times C_N \geq 25\text{ s}$ (whichever is less of the two values)			
NOTE See 6.2.5 for an explanation of the subclass codes.					

8.14 Damp heat, steady state**8.14.1 General**

See IEC 60384-1:2016, 4.22, with the details of 8.14.2 to 8.14.6.

The capacitors shall be mounted in accordance with 8.4.

8.14.2 Special preconditioning

See 8.2.

8.14.3 Initial measurement

The capacitance shall be measured in accordance with 8.6.1.

8.14.4 Test conditions

No voltage shall be applied, unless otherwise specified in the detail specification.

The severity of the test should be selected from the test conditions as shown in Table 17 and be specified in the detail specification.

The duration time should be selected in accordance with 6.1 and shall be specified in the detail specification.

Table 17 – Test conditions for damp heat, steady state

Severity	Temperature °C	Relative humidity %
1	+85 ± 2	85 ± 3
2	+60 ± 2	93 ± 3
3	+40 ± 2	93 ± 3

When the application of voltage is prescribed, U_R shall be applied to one half of the lot and no voltage shall be applied to the other half of the lot.

Within 15 min after removal from the damp heat test, the voltage proof test in accordance with 8.6.4 shall be carried out, but with the rated voltage applied.

NOTE ~~Due to~~ For safety reasons, different conditions for the application of voltage to capacitors with rated voltages of 1 kV ~~and~~ or above may be given in the detail specification.

8.14.5 Recovery

The capacitors shall recover for 24 h ± 2 h.

8.14.6 Final inspection, measurements and requirements

The capacitors shall be visually examined. There shall be no visible damage.

The capacitors shall be measured and shall meet the requirements in Table 18.

If the capacitance value is less than the minimum value permitted, then after the other measurements have been made, the capacitors shall be preconditioned in accordance with 8.2 and then the requirement in Table 18 shall be met.

Table 18 – Final inspection, measurements and requirements

Measurement	Measuring conditions	Requirements			
		Subclasses 2B and 2C	Subclasses 2D and 2R	Subclasses 2E	Subclasses 2F
Capacitance	8.6.1	$\Delta C/C \leq \pm 10 \%$	$\Delta C/C \leq \pm 15 \%$	$\Delta C/C \leq \pm 20 \%$	$\Delta C/C \leq \pm 30 \%$
Tangent of loss angle	8.6.2	$\leq 2 \times$ value of 8.6.2			
Insulation resistance	8.6.3	$R_i \geq 1\,000\text{ M}\Omega$ or $R_i \times C_N \geq 25\text{ s}$ (whichever is less of the two values)			
NOTE See 6.2.5 for an explanation of the subclass codes.					

8.15 Endurance

8.15.1 General

See IEC 60384-1:2016, 4.23, with the details of 8.15.2 to 8.15.6.

The capacitors shall be mounted in accordance with 8.4.

8.15.2 Special preconditioning

See 8.2.

8.15.3 Initial measurement

The capacitance shall be measured in accordance with 8.6.1.

8.15.4 Test conditions

~~The capacitors shall be tested as follows.~~

If the category voltage is equal to the rated voltage, the capacitors shall be tested as in Table 19.

Table 19 – Endurance test conditions ($U_C = U_R$)

U_R	$U_R \leq 200$	$200 < U_R \leq 500$	$U_R > 500$
Temperature	Upper category temperature		
Voltage (DC)	$1,5 U_R$	$1,3 U_R$	$1,2 U_R$
Duration	1 000 h	1 500 h	2 000 h

If the category voltage is not equal to the rated voltage, the capacitors shall be tested as in Table 20.

Table 20 – Endurance test conditions ($U_C \neq U_R$)

U_R	$U_R \leq 200$		$200 < U_R \leq 500$		$U_R > 500$	
Temperature	T_R	T_B	T_R	T_B	T_R	T_B
Voltage (DC)	$1,5 U_R$	$1,5 U_C$	$1,3 U_R$	$1,3 U_C$	$1,2 U_R$	$1,2 U_C$
Duration	1 000 h		1 500 h		2 000 h	
Sample	Divided into two parts		Divided into two parts		Divided into two parts	
T_R = Rated temperature.						
T_B = Upper category temperatures > 85 °C, such as 100 °C.						

8.15.5 Recovery

The capacitors shall recover for 24 h \pm 2 h.

8.15.6 Final inspection, measurements and requirements

The capacitors shall be visually examined. There shall be no visible damage.

The capacitors shall be measured and shall meet the requirements in Table 21.

If the capacitance value is less than the minimum value permitted, then after the other measurements have been made the capacitor shall be preconditioned in accordance with 8.2 and then the requirement in Table 21 shall be met.

Table 21 – Final inspection, measurements and requirements of endurance test

Measurement	Measuring conditions	Requirements			
		Subclasses 2B and 2C	Subclasses 2D and 2R	Subclasses 2E	Subclasses 2F
Capacitance	8.6.1	$\Delta C/C \leq \pm 10 \%$	$\Delta C/C \leq \pm 15 \%$	$\Delta C/C \leq \pm 20 \%$	$\Delta C/C \leq \pm 30 \%$
Tangent of loss angle	8.6.2	$\leq 2 \times$ value of 8.6.2			
Insulation resistance	8.6.3	$R_i \geq 2\,000\text{ M}\Omega$ or $R_i \times C_N \geq 50\text{ s}$ (whichever is less of the two values)			
NOTE See 6.2.5 for an explanation of the subclass codes.					

8.16 Robustness of terminations (only for capacitors with strip termination)

8.16.1 General

See IEC 60384-1:2016, 4.13, with the details of 8.16.2 and 8.16.3.

8.16.2 Test conditions

Unless otherwise specified in the detail specification, the conditions of the tests are as follows:

- Test U_{a1} : force: 2,5 N;
- Test U_b , Method 1: force: 2,5 N;
- number of bends: 1.

8.16.3 Final inspection and requirements

The capacitors shall be visually examined. There shall be no visible damage.

8.17 Component solvent resistance (if required)

See IEC 60384-1:2016, 4.31.

8.18 Solvent resistance of the marking (if required)

See IEC 60384-1:2016, 4.32.

8.19 Accelerated damp heat, steady state (if required)

8.19.1 General

See IEC 60384-1:2016, ~~4.37~~ Annex H, with the details of 8.19.2 to 8.19.5.

The capacitors shall be mounted in accordance with 8.4 and IEC 60384-1:2016, Clause H.1.

Half the capacitors shall be connected in series with resistors of 100 k Ω , with a relative tolerance of $\pm 10 \%$ and half in series with resistors of 6,8 k Ω , with a relative tolerance of $\pm 10 \%$.

8.19.2 Initial measurement

The capacitors shall be measured for insulation resistance with a voltage of 1,5 V \pm 0,1 V applied across the capacitor and resistor in series.

The insulation resistance, including the series resistor, shall meet the requirements given in Table 22.

Table 22 – Initial requirements

Measurement	Measuring conditions	Requirements	
Insulation resistance	$(1,5 \pm 0,1) \text{ V}$	Connected to 100 k Ω resistors	$C_N \leq 25 \text{ nF: } R_i \geq 4\,000 \text{ M}\Omega$ $C_N > 25 \text{ nF: } (R_i - 100 \text{ k}\Omega) \times C_N \geq 100 \text{ s}$
		Connected to 6,8 k Ω resistors	$C_N \leq 25 \text{ nF: } R_i \geq 4\,000 \text{ M}\Omega$ $C_N > 25 \text{ nF: } (R_i - 6,8 \text{ k}\Omega) \times C_N \geq 100 \text{ s}$

8.19.3 Conditioning

The capacitors with associated resistors shall be subjected to conditioning at $(85 \pm 2) \text{ }^\circ\text{C}$, $(85 \pm 3) \%$ relative humidity for the test duration given in Table 23. The voltage given in Table 23 shall be applied to the capacitors connected to 100 k Ω resistors and those connected to 6,8 k Ω resistors ~~shall be applied to voltage given in Table 22~~. In both cases, the voltage shall be applied across the capacitor/resistor combination.

Care shall be taken to avoid condensation of water on the capacitors or substrates. This may happen if the door is opened during the test before the humidity is lowered.

Table 23 – Conditioning

Connected resistors k Ω	Applied voltage	Duration
100	$(1,5 \pm 0,1) \text{ V}$ or the voltage specified in the detail specification	168 h, 500 h or 1 000 h; as given by specified in the detail specification
6,8	$(50 \pm 0,1) \text{ V}$ or U_R , whichever is the lower, applied , or the voltage specified in the detail specification	

8.19.4 Recovery

The applied voltage shall be disconnected and the capacitors and resistors shall be removed from the test chamber and allowed to recover for ~~respectively~~ 22 h to 26 h in standard atmospheric conditions for testing.

8.19.5 Final measurements

The capacitors shall be measured for insulation resistance, as in 8.19.2.

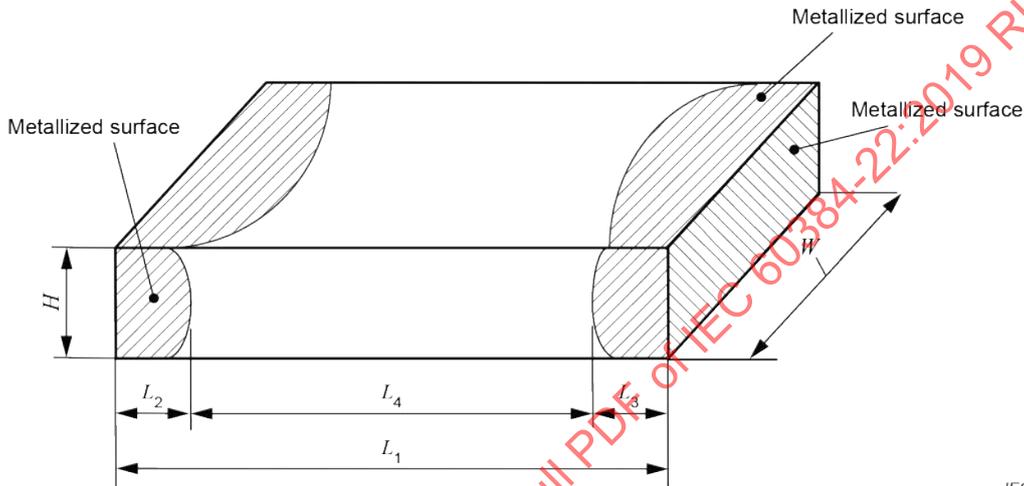
The insulation resistance, including the series resistor, shall be greater than 0,1 times the values given in 8.19.2.

Annex A
(normative)

Guidance for the specification and coding of dimensions of fixed surface mount multilayer capacitors of ceramic dielectric, Class 2

The principles given in Figure A.1 should be considered in the dimensioning of ~~surface mount~~ the capacitors.

Dimensions are specified in Table A.1.



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Dimension W should not exceed dimension L_1 .

Dimension H should not exceed dimension W .

If necessary, the thickness of tinning should be specified.

Figure A.1 – Dimensions

Table A.1 – Dimensions

Code	Length L_1	Width W	$L_2; L_3$ Minimum	L_4 Minimum
0201M	$0,25 \pm 0,013$	$0,125 \pm 0,013$	0,04	0,06
0402M	$0,4 \pm 0,02$	$0,2 \pm 0,02$	0,05	0,1
0603M	$0,6 \pm 0,03$	$0,3 \pm 0,03$	0,1	0,2
1005M	$1,0 \pm 0,05$	$0,5 \pm 0,05$	0,1	0,3
1608M	$1,6 \pm 0,1$	$0,8 \pm 0,1$	0,2	0,5
2012M	$2,0 \pm 0,1$	$1,25 \pm 0,1$	0,2	0,7
3216M	$3,2 \pm 0,2$	$1,6 \pm 0,15$	0,3	1,4
3225M	$3,2 \pm 0,2$	$2,5 \pm 0,2$	0,3	1,4
4532M	$4,5 \pm 0,3$	$3,2 \pm 0,2$	0,3	2,0
5750M	$5,7 \pm 0,4$	$5,0 \pm 0,4$	0,3	2,5

NOTE Dimension in millimetres.

Other case sizes and dimensions may be specified in the detail specification.

Annex B (informative)

Capacitance ageing of fixed capacitors of ceramic dielectric, Class 2

B.1 General

Most Class 2 dielectrics used for ceramic capacitors have ferroelectric properties and exhibit a Curie temperature.

Above this temperature the dielectric has the highly symmetric cubic crystal structure whereas below the Curie temperature the crystal structure is less symmetrical. Although in single crystals this phase transition is very sharp, in practical ceramics, it is often spread over a finite temperature range, but in all cases it is linked with a peak in the capacitance/temperature curve.

Under the influence of thermal vibration, the ions in the crystal lattice continue to move to positions of lower potential energy for a long time after the dielectric has cooled through the Curie temperature. This gives rise to the phenomenon of capacitance ageing, whereby the capacitor continually decreases its capacitance.

However, if the capacitor is heated to a temperature above the Curie temperature, then de-ageing takes place; i.e. the capacitance lost through ageing is regained, and ageing recommences from the time when the capacitor recools.

B.2 Law of capacitance ageing

During the first hour after cooling through the Curie temperature, the loss of capacitance is not well defined, but after this time it follows a logarithmic law (see K.W. Plessner, Proc. Phys. Soc., vol. 69B, P1261, 1956) which can be expressed in terms of an ageing constant.

The ageing constant k is defined as the percentage loss of capacitance due to the ageing process of the dielectric which occurs during a "decade", i.e. a time in which the capacitor increases its age tenfold for example from 1 h to 10 h.

As the law of decrease of capacitance is logarithmic, the percentage loss of capacitance will be $2 \times k$ between 1 h and 100 h age and $3 \times k$ between 1 h and 1 000 h. This may be expressed mathematically by the following equation:

$$C_t = C_1 \left(1 - \frac{k}{100} \times \lg t \right)$$

where

C_t is the capacitance t h after the start of the ageing process;

C_1 is the capacitance 1 h after the start of the ageing process;

k is the ageing constant in percent per decade (as defined above);

t is the time in h from the start of the ageing process.

The ageing constant may be declared by the manufacturer for a particular ceramic dielectric, or it may be defined by de-ageing the capacitor and measuring the capacitance at two known times thereafter.

k is then given by the following equation:

$$k = \frac{100 \times (C_{t_1} - C_{t_2})}{C_{t_1} \times \lg t_2 - C_{t_2} \times \lg t_1}$$

If capacitance measurements are made three or more times, then it is possible to derive k from the slope of a graph where C_t is plotted against $\lg t$.

It is also possible to plot $\log C$ against $\lg t$.

During measurements of ageing, the capacitor should be maintained at a constant temperature so that capacitance variations due to the temperature characteristic do not mask those due to ageing.

B.3 Capacitance measurements and capacitance tolerance

Because of ageing, it is necessary to specify a reference age at which the capacitance shall be within the prescribed tolerance. This is fixed at 1 000 h, since for practical purposes there is not much further loss of capacitance after this time.

In order to calculate the capacitance $C_{1\,000}$ after 1 000 h, the ageing constant shall be known or determined as in Clause B.2, when the following formula may be used:

$$C_{1\,000} = C_t \left[1 - \frac{k}{100} (3 - \lg t) \right]$$

For factory measurements, the loss of capacitance from the age at time of measurement to 1 000 h age will be known and can be offset by using asymmetric inspection tolerances.

For example, if it is known that the capacitance loss will be 5 %, then the capacitors may be inspected to limits of +25/-15 % instead of 20 %.

Capacitance is normally declared at 20 °C, and it may be necessary to measure at this temperature or correct the results to this temperature. Errors can also arise from heat from the hands, and capacitors should therefore always be handled with tweezers.

B.4 Special preconditioning (see 8.2)

In many of the tests in this document, it is required to measure the capacitance change which results from a given conditioning (for example climatic sequence). In order to avoid the interfering effect of ageing, the capacitor is specially preconditioned before these tests by maintaining it for 1 h at the upper category temperature followed by 24 h at standard atmospheric conditions for testing.

For those capacitors with a Curie temperature below the upper category temperature, this results in de-ageing and the conditioning is also arranged, if possible, to bring the capacitors to an age of 24 h, so that capacitance changes due to ageing are minimized.

If the Curie temperature of the dielectric is above the upper category temperature, then the special preconditioning will not completely de-age the capacitor, but it will nevertheless bring it into a state where its capacitance is not so dependent on its previous history, and the same effect will be achieved, though completely de-aged. In order to de-age such capacitors completely, temperature up to 160 °C may be required, and this temperature could be deleterious to the encapsulation. Therefore, in the few cases where complete de-ageing of such capacitors may be required, the detail specification shall be consulted for details and any necessary precautions.

Annex C (informative)

Temperature characteristics of capacitance for the reference temperature of 25 °C

The temperature characteristics of capacitance for the reference temperature of 25 °C have often been used due to marketing needs and because of their actual performance. These temperature characteristics and codes are shown in Table C.1 and the exact conditions of temperature characteristics of capacitance are shown in Table C.2.

**Table C.1 – Temperature characteristics of capacitance
for the reference temperature of 25 °C**

Code of temperature characteristics of capacitance	Maximum capacitance change %	Temperature range °C
X5R	± 15	-55 to +85
X7R	± 15	-55 to +125
X8R	± 15	-55 to +150
X6S	± 22	-55 to +105
X7S	± 22	-55 to +125
Y5V	-82 to +22	-30 to +85

**Table C.2 – Measuring conditions of temperature characteristic
of capacitance for the reference temperature of 25 °C**

Measuring step	Temperature °C
1	25 ± 2
2	$T_A^a \pm 3$
3	25 ± 2
4	$T_B^b \pm 2$
5	25 ± 2
<p>NOTE Measurements may be made at such intermediate temperatures as to ensure that the requirements of Table C.1 are met.</p>	
<p>NOTE Reference capacitance is the capacitance measured at Step 3.</p>	
<p>^a T_A = Lower category temperature.</p>	
<p>^b T_B = Upper category temperature.</p>	

Annex X
(informative)

Cross-reference for reference to IEC 60384-22:2011

The drafting of this document has resulted in a new structure. Table X.1 indicates the new clause and subclause numbers with respect to IEC 60384-22:2011.

Table X.1 – Reference to IEC 60384-22 for clause/subclause

IEC 60384-22:2011 2 nd edition Clause/Subclause	IEC 60384-22:20xx 3 rd edition Clause/Subclause	Notes
1 1.1 1.2	1	Scope and Object are merged into one in accordance with the ISO/IEC Directives Part 2
1.3	2	In accordance with ISO/IEC Directives Part 2
1.4	4	In accordance with the change of clause numbers
1.5	3	In accordance with ISO/IEC Directives Part 2
1.6	5	In accordance with the change of clause numbers
2	6	In accordance with the change of clause numbers
3	7	In accordance with the change of clause numbers
4	8	In accordance with the change of clause numbers

Table X.2 indicates the new figure and table numbers with respect to IEC 60384-22:2011.

Table X.2 – Reference to IEC 60384-22 for figure/table

IEC 60384-22:2011 2 nd edition Figure/Table	IEC 60384-22:20xx 3 rd edition Figure/Table	Notes
Table 6a	Table 6	In accordance with the ISO/IEC directives, Part 2 and the change of table numbers
Table 6b	Table 7	
Table 7 to Table 22	Table 8 to Table 23	In accordance with the change of table numbers
For the figure numbers, there was no change.		

Bibliography

IEC 60384-14, *Fixed capacitors for use in electronic equipment – Part 14: Sectional specification: Fixed capacitors for electromagnetic interference suppression and connection to the supply mains*

~~IEC 60384-22-1, *Fixed capacitors for use in electronic equipment – Part 22-1: Blank detail specification: Fixed surface mount multilayer capacitors of ceramic dielectric, Class 2 – Assessment level EZ*~~

IEC 60417, *Graphical symbols for use on equipment* (available at <http://www.graphical-symbols.info/equipment>)

K.W. Plessner: *Ageing of the Dielectric Properties of Barium Titanate Ceramics*, Proceedings of the Physical Society, Section B, Volume 69, Issue 12, pp. 1261 to 1268 (1956)

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INTERNATIONAL STANDARD

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**Fixed capacitors for use in electronic equipment –
Part 22: Sectional specification – Fixed surface mount multilayer capacitors of
ceramic dielectric, Class 2**

**Condensateurs fixes utilisés dans les équipements électroniques –
Partie 22: Spécification intermédiaire – Condensateurs multicouches fixes à
diélectriques en céramique pour montage en surface, de Classe 2**

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INTERNATIONAL ELECTROTECHNICAL COMMISSION

FIXED CAPACITORS FOR USE IN ELECTRONIC EQUIPMENT –**Part 22: Sectional specification –
Fixed surface mount multilayer capacitors of ceramic dielectric, Class 2**

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International Standard IEC 60384-22 has been prepared by IEC technical committee 40: Capacitors and resistors for electronic equipment.

This third edition cancels and replaces the second edition published in 2011. This edition constitutes a technical revision.

This edition includes the following significant technical changes with respect to the previous edition:

- a) revision of the structure in accordance with ISO/IEC Directives, Part 2:2016 (seventh edition) to the extent practicable, and for harmonizing with IEC 60384-21;
- b) deletion of the description on the permissible reactive power in 6.2.2 because it is not appropriate for the purposes of this document;
- c) the dimensions of 0201M in Annex A have been added.

The text of this International Standard is based on the following documents:

FDIS	Report on voting
40/2640/FDIS	40/2652/RVD

Full information on the voting for the approval of this International Standard can be found in the report on voting indicated in the above table.

This document has been drafted in accordance with the ISO/IEC Directives, Part 2.

A list of all parts in the IEC 60384 series, published under the general title *Fixed capacitors for use in electronic equipment*, can be found on the IEC website.

The committee has decided that the contents of this document will remain unchanged until the stability date indicated on the IEC website under "<http://webstore.iec.ch>" in the data related to the specific document. At this date, the document will be

- reconfirmed,
- withdrawn,
- replaced by a revised edition, or
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FIXED CAPACITORS FOR USE IN ELECTRONIC EQUIPMENT –

Part 22: Sectional specification – Fixed surface mount multilayer capacitors of ceramic dielectric, Class 2

1 Scope

This part of IEC 60384 is applicable to fixed unencapsulated surface mount multilayer capacitors of ceramic dielectric, Class 2, for use in electronic equipment. These capacitors have metallized connecting pads or soldering strips and are intended to be mounted on printed boards, or directly onto substrates for hybrid circuits.

Capacitors for electromagnetic interference suppression are not included, but are covered by IEC 60384-14.

The object of this document is to prescribe preferred ratings and characteristics and to select from IEC 60384-1 the appropriate quality assessment procedures, tests and measuring methods and to give general performance requirements for this type of capacitor. Test severities and requirements prescribed in detail specifications referring to this sectional specification are of equal or higher performance levels, lower performance levels are not permitted.

2 Normative references

The following documents are referred to in the text in such a way that some or all of their content constitutes requirements of this document. For dated references, only the edition cited applies. For undated references, the latest edition of the referenced document (including any amendments) applies.

IEC 60063, *Preferred number series for resistors and capacitors*

IEC 60068-1:2013, *Environmental testing – Part 1: General and guidance*

IEC 60068-2-58:2015, *Environmental testing – Part 2-58: Tests – Test Td – Test methods for solderability, resistance to dissolution of metallization and to soldering heat of surface mounting devices (SMD)*

IEC 60068-2-58:2015/AMD1:2017

IEC 60384-1:2016, *Fixed capacitors for use in electronic equipment – Part 1: Generic specification*

IEC 61193-2:2007, *Quality assessment system – Part 2: Selection and use of sampling plans for inspection of electronic components and packages*

ISO 3:1973, *Preferred numbers – Series of preferred numbers*

3 Terms and definitions

For the purposes of this document, the terms and definitions given in IEC 60384-1 and the following apply.

ISO and IEC maintain terminological databases for use in standardization at the following addresses:

- IEC Electropedia: available at <http://www.electropedia.org/>
- ISO Online browsing platform: available at <http://www.iso.org/obp>

3.1

surface mount multilayer capacitor

multilayer capacitor whose small dimensions and nature or shape of terminations make it suitable for surface mounting in hybrid circuits and on printed boards

3.2

capacitor of ceramic dielectric, Class 2

capacitor that has a dielectric with a high permittivity and is suitable for by-pass and coupling applications or for frequency-discriminating circuits where low losses and high stability of capacitance are not of major importance

Note 1 to entry: The ceramic dielectric is characterized by a non linear change of capacitance over the category temperature range (see Table 3).

3.3

subclass

maximum percentage change of capacitance within the category temperature range with respect to the capacitance at 20 °C

Note 1 to entry: The subclass may be expressed in code form (see Table 3).

3.4

category temperature range

ambient temperature range for which the capacitor has been designed to operate continuously

Note 1 to entry: This is given by the lower and upper category temperature.

3.5

rated temperature

T_R

maximum ambient temperature at which the rated voltage may be continuously applied

3.6

rated voltage

U_R

maximum DC voltage that may be applied continuously to a capacitor at any temperature between the lower category temperature and the rated temperature

Note 1 to entry: The maximum DC voltage is the sum of the DC voltage and peak AC voltage or peak pulse voltage applied to the capacitor.

3.7

category voltage

U_C

maximum voltage that can be applied continuously to a capacitor at its upper category temperature

4 Information to be given in a detail specification

4.1 General

The detail specifications shall be derived from the relevant blank detail specification.

Detail specifications shall not specify requirements inferior to those of the generic, sectional or blank detail specification. When more severe requirements are included, they shall be listed in 1.9 of the detail specification and indicated in the test schedules, for example by an asterisk.

The information given in 4.2 may be presented in tabular form if more convenient.

The information in 4.2 to 4.5 shall be given in each detail specification and the values quoted should be selected from those given in the appropriate clause of this sectional specification.

4.2 Outline drawing and dimensions

There shall be an illustration of the capacitors as an aid to easy recognition and for comparison of the capacitors with others.

Dimensions and their associated tolerances, which affect interchangeability and mounting, shall be given in the detail specification. All dimensions shall be stated in millimetres, however, when the original dimensions are given in inches, the converted metric dimensions in millimetres shall be added.

Normally the numerical values shall be given for the length, width and height of the body. When necessary, for example when a number of items (sizes and capacitance/voltage ranges) are covered by a detail specification, the dimensions and their associated tolerances shall be placed in a table below the drawing.

When the configuration is other than described above, the detail specification shall state such dimensional information as will adequately describe the capacitors.

4.3 Mounting

The detail specification shall give guidance on methods of mounting for normal use. Mounting for test and measurement purposes (when required) shall be in accordance with 8.4 of this sectional specification.

4.4 Rating and characteristics

4.4.1 General

The ratings and characteristics shall be in accordance with the relevant clauses of this sectional specification, together with 4.4.2, 4.4.3 and 4.4.4.

4.4.2 Nominal capacitance range

See 6.2.4.1.

When products approved to the detail specification have different ranges, the following statement should be added: "The range of capacitance values available in each voltage range is given in the register of approvals, available for example on the IECQ on-line certificate system website www.iecq.org".

4.4.3 Particular characteristics

Additional characteristics may be listed, when they are considered necessary to specify adequately the component for design and application purposes.

4.4.4 Soldering

The detail specification shall prescribe the test methods, severities and requirements applicable for the solderability and the resistance to soldering heat tests.

4.5 Marking

The detail specification shall specify the content of the marking on the capacitor and on the packaging. Deviations from Clause 5 of this sectional specification shall be specifically stated.

5 Marking

5.1 General

See IEC 60384-1:2016, 2.4, with the details of 5.2 to 5.6.

5.2 Information for marking

The information given in the marking is normally selected from the following list, the relative importance of each item is indicated by its position in the list:

- nominal capacitance;
- rated voltage (DC voltage may be indicated by the symbol: $\overline{\text{---}}$ [IEC 60417-5031(2002-10)] or ---);
- tolerance on nominal capacitance;
- dielectric subclass as applicable (in accordance with 6.2.5);
- year and month (or week) of manufacture;
- manufacturer's name or trade mark;
- climatic category;
- manufacturer's type designation;
- reference to the detail specification.

5.3 Marking on the body

These capacitors are generally not marked on the body. If some marking can be applied, they shall be clearly marked with as many as possible of the items stated in 5.2 as is considered useful. Any duplication of information in the marking on the capacitor should be avoided.

5.4 Requirements for marking

Any marking shall be legible and not easily smeared or removed by rubbing with fingers.

5.5 Marking of the packaging

The packaging containing the capacitor(s) shall be clearly marked with all the information listed in 5.2.

5.6 Additional marking

Any additional marking shall be so applied that no confusion can arise.

6 Preferred ratings and characteristics

6.1 Preferred characteristics

Preferred climatic categories only shall be given in the preferred characteristics.

The capacitors covered by this document are classified into climatic categories in accordance with the general rules given in IEC 60068-1:2013, Annex A.

The lower and upper category temperatures and the duration of the damp heat, steady state test shall be chosen from the following:

- lower category temperature: -55 °C, -40 °C, -25 °C, -10 °C and +10 °C;
- upper category temperature: +70 °C, +85 °C, +100 °C, +125 °C and +150 °C;
- duration of the damp heat,
 steady state test (40 °C, 93 % RH): 4, 10, 21 and 56 days.

The severities of the cold and dry heat tests are the lower and upper category temperatures respectively.

NOTE The resistance to humidity resulting from the above climatic category is for the capacitors in their unmounted state. The climatic performance of the capacitors after mounting is greatly influenced by the mounting substrate, the mounting method (see 8.4) and the final coating.

6.2 Preferred values of ratings

6.2.1 Rated temperature (T_R)

The rated temperature is equal to the upper category temperature for capacitors with the upper category temperature not exceeding 125 °C, unless otherwise stated in the detail specification.

6.2.2 Rated voltage (U_R)

The preferred values of the rated voltage are the values of the R5 series of ISO 3. If other values are needed they shall be chosen from the R10 series.

The sum of the DC voltage and the peak AC voltage or the peak to peak AC voltage, whichever is the greater, applied to the capacitor shall not exceed the rated voltage.

6.2.3 Category voltage (U_C)

The category voltage is equal to the rated voltage for capacitors with the upper category temperature not exceeding 125 °C. Any category voltages which are different from the rated voltage, for capacitors with the upper category temperature exceeding 125 °C or for high-voltage capacitors with rated voltages about 500 V, shall be given in the detail specification.

The preferred values of the category voltage at 125 °C upper category temperature for high volumetric capacitors with a rated voltage of 16 V and less and a rated temperature of 85 °C are given in Table 1.

Table 1 – Preferred values of category voltages

U_R	V	2,5	4	6,3	10	16
U_C	V	1,6	2,5	4	6,3	10
NOTE The numeric values of U_C are calculated by the following: $U_C = 0,63 \times U_R$						

6.2.4 Preferred values of nominal capacitance and associated tolerance values

6.2.4.1 Preferred values of nominal capacitance

Nominal capacitance values shall be taken from the number series of IEC 60063; the E3, E6 and E12 series are preferred.

6.2.4.2 Preferred tolerances on nominal capacitance

See Table 2.

Table 2 – Preferred tolerances

Preferred series	Tolerance %	Letter code
E3 and E6	-20/+80	Z
	-20/+50	S
E6	± 20	M
E6 and E12	± 10	K

6.2.5 Temperature characteristic of capacitance

Table 3 denotes with a cross the preferred values of the temperature characteristic with and without a DC voltage applied. The method of coding the subclass is also given; for example a dielectric with a percentage change of ±20 % without DC voltage applied over the temperature range from -55 °C to +125 °C will be defined as a dielectric of subclass 2C1.

The temperature range for which the temperature characteristic of the dielectric is defined is the same as the category temperature range.

Table 3 – Temperature characteristic of capacitance

Sub-class letter code	Maximum capacitance change within the category temperature range with respect to the capacitance at 20 °C measured with and without a DC voltage applied		Category temperature range and corresponding number code					
	%		-55/+150	-55/+125	-55/+85	-40/+85	-25/+85	+10/+85
	without DC voltage applied	with DC voltage applied (NOTE 1)	°C	°C	°C	°C	°C	°C
			0	1	2	3	4	6
2B	±10	Requirements specified in the detail specification			x	x	x	
2C	±20			x	x	x		
2D	+20/-30			x			x	
2E	+22/-56				x	x	x	x
2F	+30/-80				x	x	x	x
2R	±15		x	x	x		x	
When the upper category temperature is above 125 °C, the limits of capacitance change, both with and without DC voltage applied, should be given in the detail specification.								
NOTE 1 DC voltage applied is either rated voltage or the voltage specified in the detail specification.								
NOTE 2 "x" indicates preferred.								

NOTE See Annex C for the reference temperature of 25 °C as an informative guidance.

6.2.6 Dimensions

Suggested rules for the specification and coding of dimensions are given in Annex A.

Specific dimensions shall be given in the detail specification.

7 Quality assessment procedures

7.1 Primary stage of manufacture

The primary stage of manufacture is the first common firing of the dielectric-electrode assembly.

7.2 Structurally similar components

Capacitors considered as being structurally similar are capacitors produced with similar processes and materials, though they may be of different case sizes and values.

7.3 Certified records of released lots

The information required in IEC 60384-1:2016, Q.1.5, shall be made available when prescribed in the detail specification and when requested by a purchaser. After the endurance test, the parameters for which variables information is required are the capacitance change, $\tan \delta$ and the insulation resistance.

7.4 Qualification approval

7.4.1 General

The procedures for qualification approval testing are given in IEC 60384-1:2016, Clause Q.2.

The schedule to be used for qualification approval testing on the basis of lot-by-lot and periodic tests is given in 7.5. The procedure using a fixed sample size schedule is given in 7.4.2 and 7.4.3.

7.4.2 Qualification approval on the basis of the fixed sample size procedures

The fixed sample size procedure is described in IEC 60384-1:2016, Q.2.4. The sample shall be representative of the range of capacitors for which approval is sought. This may or may not be the complete range covered by the detail specification.

For each temperature characteristic, the sample shall consist of specimens of capacitors of maximum and minimum size and for each of these sizes, the maximum capacitance value for the highest rated voltage and minimum rated voltage of the voltage ranges for which approval is sought. When there are more than four rated voltages, an intermediate voltage shall also be tested. Thus, for the approval of a range, testing is required of either four or six values (capacitance/voltage combinations) for each temperature characteristic. Where the total range consists of fewer than four values, the number of specimens to be tested shall be that required for four values.

In case assessment level EZ is used, spare specimens are permitted as follows:

Two (for six values) or three (for four values) per value may be used as replacements for specimens that are non-conforming because of incidents not attributable to the manufacturer.

The numbers given in Group 0 assume that all groups are applicable. If this is not so, the numbers may be reduced accordingly.

When additional groups are introduced into the qualification approval test schedule, the number of specimens required for Group 0 shall be increased by the same number as that required for the additional groups.

Table 4 gives the number of samples to be tested in each group or subgroup together with the number of permissible non-conformances for the qualification approval test.

7.4.3 Tests

The complete series of tests specified in Table 4 and Table 5 are required for the approval of capacitors covered by one detail specification. The tests of each group shall be carried out in the order given.

The whole sample shall be subjected to the tests of Group 0 and then divided for the other groups.

Non-conforming specimens found during the tests of Group 0 shall not be used for the other groups.

"One non-conforming item" is counted when a capacitor has not satisfied the whole or a part of the tests of a group.

The approval is granted when the number of non-conforming items is zero.

Table 4 and Table 5 together form the fixed sample size test schedule. Table 4 includes the details for the sampling and permissible non-conforming items for the different tests or groups of tests. Table 5 together with the details of the test contained in Clause 8 gives a complete summary of test conditions and performance requirements and indicates where, for example for the test method or conditions of test, a choice shall be made in the detail specification.

The conditions of test and performance requirements for the fixed sample size test schedule shall be identical to those prescribed in the detail specification for quality conformance inspection.

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Table 4 – Fixed sample size test plan for qualification approval – Assessment level EZ

Group No.	Test	Subclause of this publication	Number of specimens <i>n</i> ^e	Permissible number of nonconforming items <i>c</i>
0	Visual examination	8.5	132 + 24 ^f	0
	Dimensions	8.5		
	Capacitance	8.6.1		
	Tangent of loss angle	8.6.2		
	Insulation resistance	8.6.3		
	Voltage proof	8.6.4		
	Spare specimens		12	
1A	Robustness of termination ^g	8.16	12	0
	Resistance to soldering heat	8.10		
	Component solvent resistance ^b	8.17		
1B	Impedance ^b	8.6.5	12	0
	Equivalent series resistance [ESR] ^b	8.6.6		
	Solderability	8.11		
	Solvent resistance of marking ^b	8.18		
2	Substrate bending test ^d	8.9	12	0
3 ^a	Mounting	8.4	84 + 24 ^f	0 ^c
	Visual examination	8.5		
	Capacitance	8.6.1		
	Tangent of loss angle	8.6.2		
	Insulation resistance	8.6.3		
	Voltage proof	8.6.4		
3.1	Shear test ^h	8.8	24	0
	Rapid change of temperature	8.12		
	Climatic sequence	8.13		
3.2	Damp heat, steady state	8.14	24	0
3.3	Endurance	8.15	36	0
3.4	Accelerated damp heat, steady state ^b	8.19	24 ^f	0
4	Temperature characteristic of capacitance	8.7	12	0
<p>^a The values of these measurements serve as initial measurements for the tests of Group 3.</p> <p>^b If required in the detail specification.</p> <p>^c The capacitors found non-conforming items after mounting shall not be taken into account when calculating the permissible non-conforming for the following tests. They shall be replaced by spare capacitors.</p> <p>^d Not applicable to capacitors, which, in accordance with their detail specification, shall only be mounted on alumina substrates.</p> <p>^e Capacitance/voltage combinations, see 7.4.2.</p> <p>^f Additional capacitors, if Group 3.4 is tested.</p> <p>^g Applicable to capacitors with strip terminations.</p> <p>^h Not applicable to capacitors with strip terminations.</p>				

Table 5 – Tests schedule for qualification approval

Subclause number and test (see NOTE 1)	D or ND	Conditions of test (see NOTE 1)	Number of specimens (<i>n</i>) and number of non-conforming items (<i>c</i>)	Performance requirements (see NOTE 1)
GROUP 0	ND		See Table 4	
8.5 Visual examination				As in 8.5.3
8.5 Dimension (detail)				Legible marking and as specified in the detail specification
8.6.1 Capacitance		Frequency: ... Hz Measuring voltage:... V RMS		See the detail specification Within specified tolerance
8.6.2 Tangent of loss angle ($\tan \delta$)		Frequency and Measuring voltage same as in 8.6.1		As in 8.6.2.3
8.6.3 Insulation resistance		See detail specification for the method		As in 8.6.3.4
8.6.4 Voltage proof		See detail specification for the method		No breakdown or flashover
GROUP 1A	D		See Table 4	
8.16 Robustness of termination (if applicable)		Test U_{a1} , Force:2,5 N Test U_b , Method 1, Force:2,5 N Number of bends:1		No visible damage
8.10.3 Initial measurement		Visual examination Capacitance		
8.10 Resistance to soldering heat		Special preconditioning as in 8.2 See detail specification for the method Recovery: (24 ± 2) h		
8.10.6 Final measurement		Visual examination Capacitance		As in 8.10.6 As in 8.10.6
8.17 Component solvent resistance (if required)		Solvent: ... Solvent temperature:... Method 2 Recovery: ...		See detail specification
GROUP 1B	D		See Table 4	
8.6.5 Impedance (if required)		Frequency: 100 kHz		See detail specification
8.6.6 ESR (if required)		Frequency: 100 kHz		See detail specification
8.11 Solderability		See detail specification for the method		
8.11.4 Final measurements		Visual examination		As in 8.11.4
8.18 Solvent resistance of the marking ^a (if required)		Solvent: ... Solvent temperature: ... Method 1 Rubbing material: cotton wool Recovery: ...		Legible marking

Subclause number and test (see NOTE 1)	D or ND	Conditions of test (see NOTE 1)	Number of specimens (n) and number of non-conforming items (c)	Performance requirements (see NOTE 1)
GROUP 2 8.9 Substrate bending test 8.9.2 Initial measurement 8.9.3 Final inspection	D	Deflection: ... Number of bends: ... Capacitance Capacitance (with printed board in bent position) Visual examination	See Table 4	See detail specification $ \Delta C/C \leq 10\%$ No visible damage
GROUP 3 8.4 Mounting	D	Substrate material: ... ^b Visual examination Capacitance Tangent of loss angle Insulation resistance Voltage proof	See Table 4	As in 8.5.3 Within specified tolerance As in 8.6.2.3 As in 8.6.3.4 No breakdown or flashover
GROUP 3.1 8.8 Shear test 8.12.3 Initial measurement 8.12 Rapid change of temperature 8.12.6 Final measurements 8.13 Climatic sequence 8.13.3 Initial Measurement 8.13.4 Dry heat 8.13.5 Damp heat, cyclic, test Db, first cycle 8.13.6 Cold 8.13.7 Damp heat, cyclic, test Db, remaining cycles	D	Visual examination Capacitance Special preconditioning as in 8.2 T_A = Lower category temperature T_B = Upper category temperature Five cycles Duration $t_1 = 30$ min Recovery: (24 ± 2) h Visual examination Capacitance Special preconditioning as in 8.2 Capacitance Temperature: upper category temperature Duration: 16 h Temperature: lower category temperature Duration: 2 h Visual examination Recovery: (24 ± 2) h	See Table 4	No visible damage No visible damage $\Delta C/C$: as in 8.12.6 No visible damage
8.13.8 Final measurements		Visual examination Capacitance Tangent of loss angle Insulation resistance		No visible damage Legible marking $\Delta C/C$: as in 8.13.8 As in 8.13.8 As in 8.13.8

Subclause number and test (see NOTE 1)	D or ND	Conditions of test (see NOTE 1)	Number of specimens (n) and number of non-conforming items (c)	Performance requirements (see NOTE 1)
GROUP 3.2 8.14 Damp heat, steady state 8.14.3 Initial measurement 8.14.6 Final measurements	D	Special preconditioning as in 8.2 Capacitance Recovery: (24 ± 2) h Visual examination Capacitance Tangent of loss angle Insulation resistance	See Table 4	No visible damage Legible marking $\Delta C/C$: as in 8.14.6 As in 8.14.6 As in 8.14.6
GROUP 3.3 8.15 Endurance 8.15.3 Initial measurement 8.15.6 Final measurements	D	Special preconditioning as in 8.2 Duration: ... h Temperature: ... °C Voltage: ... V Capacitance Recovery: (24 ± 2) h Visual examination Capacitance Tangent of loss angle Insulation resistance	See Table 4	No visible damage Legible marking $\Delta C/C$: as in 8.15.6 As in 8.15.6 As in 8.15.6
Group 3.4 8.19 Accelerated damp heat, steady state (if required) 8.19.2 Initial measurement 8.19.5 Final measurement	D	Duration: ... h Temperature: (85 ± 2) °C Humidity: (85 ± 3) % RH Insulation resistance Recovery: (24 ± 2) h Insulation resistance	See Table 4	As in 8.19.2 As in 8.19.5
Group 4 8.7 Temperature characteristic of capacitance	ND	Special preconditioning as in 8.2	See Table 4	$\Delta C/C$: as in 8.7.3
NOTE 1 Subclause numbers of test and performance requirements refer to Clause 8.				
NOTE 2 In this table: D = destructive, ND= non-destructive.				
^a This test may be carried out on capacitors mounted on a substrate. ^b When different substrate materials are used for the individual subgroup, the detail specification shall indicate which substrate material is used in each subgroup.				

7.5 Quality conformance inspection

7.5.1 Formation of inspection lots

7.5.1.1 Groups A and B inspection

These tests shall be carried out on a lot-by-lot basis.

A manufacturer may aggregate the current production into inspection lots subject to the following safeguards.

- 1) The inspection lot shall consist of structurally similar capacitors (see 7.2).
- 2a) The sample tested shall be representative of the values and the dimensions contained in the inspection lot:
 - in relation to their number;
 - with a minimum of five of any one value.
- 2b) If there are fewer than five of any one value in the sample the basis for the drawing of samples shall be agreed between the manufacturer and the certification body (CB).

7.5.1.2 Group C inspection

These tests shall be carried out on a periodic basis.

Samples shall be representative of the current production of the specified periods and shall be divided into small, medium and large sizes. In order to cover the range of approvals in any period, one voltage shall be tested from each group of sizes. In subsequent periods, other sizes and/or voltage ratings in production shall be tested with the aim of covering the whole range.

7.5.2 Test schedule

The schedule for the lot-by-lot and periodic tests for quality conformance inspection is given in Clause 2 of the blank detail specification.

7.5.3 Delayed delivery

When, in accordance with the procedures of IEC 60384-1:2016, Q.1.7, re-inspection shall be made, solderability and capacitance shall be checked as specified in Groups A and B inspection.

7.5.4 Assessment levels

The assessment level(s) given in the blank detail specification should be selected from Table 6 and Table 7.

Table 6 – Lot-by-lot inspection

Inspection subgroup ^d	EZ		
	IL ^a	<i>n</i> ^a	<i>c</i> ^a
A0	100 % ^b		
A1	S-4	<i>c</i>	0
A2	S-3	<i>c</i>	0
B1	S-3	<i>c</i>	0
B2	S-2	<i>c</i>	0

^a IL = inspection level
n = sample size
c = permissible number of non-conforming items

^b The inspection shall be performed after removal of nonconforming items by 100 % testing during the manufacturing process. Whether the lot was accepted or not, all samples for sampling inspection shall be inspected in order to monitor outgoing quality level by nonconforming items per million ($\times 10^{-6}$).
 The sampling level shall be established by the manufacturer, preferably in accordance with IEC 61193-2:2007, Annex A.
 In the case where one or more nonconforming items occur in a sample, this lot shall be rejected, but all nonconforming items shall be counted for the calculation of quality level values. Outgoing quality level by nonconforming items per million ($\times 10^{-6}$) values shall be calculated by accumulating inspection data in accordance with the method given in IEC 61193-2:2007, 6.2.

^c Number to be tested: sample size shall be determined in accordance with IEC 61193-2:2007, 4.3.2.

^d The content of the inspection subgroup is described in Clause 2 of the relevant blank detail specification.

Table 7 – Periodic test

Inspection subgroup ^b	EZ		
	<i>p</i> ^a	<i>n</i> ^a	<i>c</i> ^a
C1	3	12	0
C2	3	12	0
C3.1	6	27	0
C3.2	6	15	0
C3.3	3	15	0
C3.4 ^c	6	15	0
C4	6	9	0

^a *p* = periodicity in months
n = sample size
c = permissible number of non-conforming items

^b The content of the inspection subgroup is described in Clause 2 of the relevant blank details specification.

^c If required.

8 Test and measurement procedures

8.1 General

This clause supplements the information given in IEC 60384-1:2016, Clause 4.

8.2 Special preconditioning

Unless otherwise specified in the detail specification, the special preconditioning, when specified in this sectional specification before a test or a sequence of test, shall be carried out under the following conditions.

Exposure at upper category temperature or at such higher temperature as may be specified in the detail specification during 1 h, followed by recovery during (24 ± 1) h under standard atmospheric conditions for testing.

NOTE Capacitors lose capacitance continuously with time in accordance with a logarithmic law (this is called ageing). However, if the capacitor is heated to a temperature above the Curie point of its dielectric, then "de-ageing" takes place, i.e. the capacitance lost through "ageing" is regained, and "ageing" recommences from the time when the capacitor recools. The purpose of special preconditioning is to bring the capacitor to a defined state regardless of its previous history (see Clause B.4 for further information).

8.3 Measuring conditions

See IEC 60384-1:2016, 4.2.1.

8.4 Mounting

See IEC 60384-1:2016, 4.33.

8.5 Visual examination and check of dimensions

8.5.1 General

See IEC 60384-1:2016, 4.4, with the details of 8.5.2 and 8.5.3.

8.5.2 Visual examination

A visual examination shall be carried out with suitable equipment with approximately 10× magnification and lighting appropriate to the specimen under test and the quality level required.

The operator should have available facilities for incident or transmitted illumination as well as an appropriate measuring facility.

8.5.3 Requirements

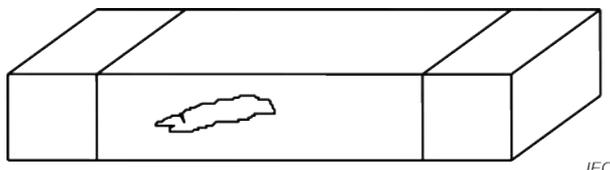
8.5.3.1 General

Quantitative values for the requirements below may be given in the detail or in the manufacturer's specification.

8.5.3.2 Requirements for the ceramic

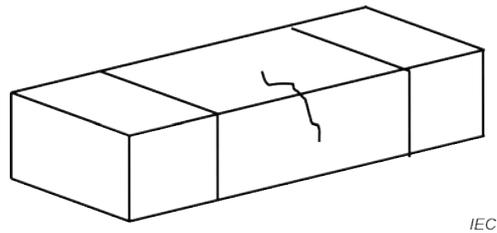
Requirements for the ceramic are as follows:

- a) Be free of cracks or fissures, except small damages on the surface, that do not deteriorate the performance of the capacitor (examples: see Figure 1 and Figure 2).



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Figure 1 – Fault: crack or fissure

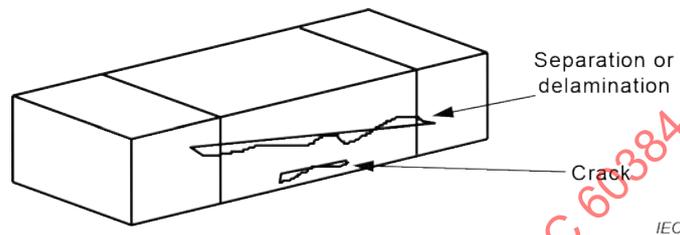


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NOTE Crack or fissure on one side or extending from one face to another over a corner.

Figure 2 – Fault: crack or fissure

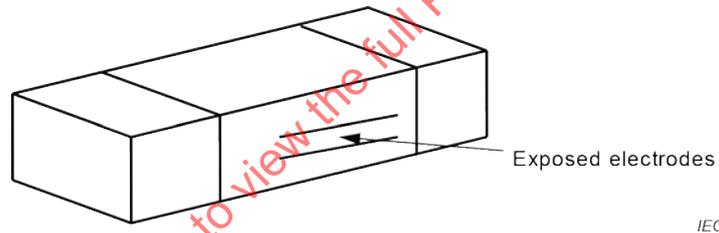
- b) Not exhibit visible separation or delamination between the layers of the capacitor (see Figure 3).



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Figure 3 – Separation or delamination

- c) Not exhibit exposed electrodes between the two terminations (see Figure 4).



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Figure 4 – Exposed electrodes

- d) The ceramic body shall be free of any conducting smears (metallization, tinning, etc.) on a central zone between two adjacent terminations which is equal to the minimum distance between those (Annex A, dimension L_4).

8.5.3.3 Requirements for the metallization

Requirements for the metallization are as follows:

- a) Not exhibit any visible detachment of the metallized terminations and not exhibit any exposed electrodes (see Figure 4).
 b) The principal faces (see Figure 5) are those noted A, B and C.

In the case of capacitors of square section, the faces D and E are also considered principal.

The maximum area of gaps in metallization on each principal face shall not be greater than 15 % of the area of that face; these gaps shall not be concentrated in the same area. The gaps in metallization shall not affect the two principal edges of each extremity of the block (or four edges for square section capacitors). Dissolution of the end face plating (leaching) shall not exceed 25 % of the length of the edge concerned.

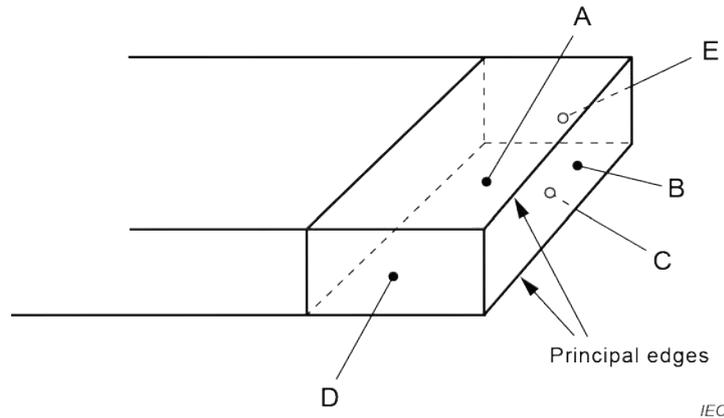


Figure 5 – Principal faces

8.6 Electrical tests

8.6.1 Capacitance

8.6.1.1 General

See IEC 60384-1:2016, 4.7, with the details of 8.6.1.2 and 8.6.1.3.

8.6.1.2 Measuring conditions

See Table 8, unless otherwise specified in the detail specification.

Table 8 – Measuring conditions

Nominal capacitance	Rated voltage U_R	Frequency	Measuring voltage V RMS	Referee voltage V RMS
$C_N < 100 \text{ pF}$	^a	1 MHz	$1,0 \pm 0,2$	$1,0 \pm 0,02$
$100 \text{ pF} \leq C_N \leq 10 \text{ }\mu\text{F}$	$U_R > 6,3 \text{ V}$	1 kHz	$1,0 \pm 0,2$	$1,0 \pm 0,02$
	$U_R \leq 6,3 \text{ V}$	1 kHz	$0,5 \pm 0,2$	$0,5 \pm 0,02$
$C_N > 10 \text{ }\mu\text{F}$	^a	100 Hz or 120 Hz	$0,5 \pm 0,2$	$0,5 \pm 0,02$

^a All rated voltages (U_R).

8.6.1.3 Requirements

The capacitance value as measured in the unmounted state, shall correspond with the rated value taking into account the specified tolerance.

The capacitance as measured in the mounted state in accordance with Group 3 is for reference purposes only in further tests.

For referee measurements, the capacitance value shall be the value extrapolated to an ageing time of 1 000 h, unless otherwise specified in the detail specification (see Annex B for explanations). If applying the ageing time other than 1 000 h, that may be specified in the detail specification.

8.6.2 Tangent of loss angle ($\tan \delta$)

8.6.2.1 General

See IEC 60384-1:2016, 4.8, with the details of 8.6.2.2 and 8.6.2.3.

8.6.2.2 Measuring conditions

The measuring conditions are the same as those of 8.6.1. The inaccuracy of the measuring equipment shall not exceed 1×10^{-3} .

8.6.2.3 Requirements

The tangent of loss angle as measured in the unmounted state shall not exceed the limit given in Table 9.

Table 9 – Tangent of loss angle limits

Rated voltage U_R	Subclass	Tangent of loss angle
$U_R \geq 10$ V	All subclass codes	Not exceed 0,035 or value as may be given in the detail specification
$U_R < 10$ V	2B, 2C, 2R	0,1
	2D, 2E	0,15
	2F	0,2
NOTE See 6.2.5 for an explanation of the subclass codes.		

The tangent of loss angle as measured in the mounted state in accordance with Group 3 is for reference purposes only in further tests.

8.6.3 Insulation resistance

8.6.3.1 General

See IEC 60384-1:2016, 4.5, with the details of 8.6.3.2 to 8.6.3.4.

8.6.3.2 Preparation for test

Prior to the test, capacitors shall be carefully cleaned to remove any contamination.

Care shall be taken to maintain cleanliness in the test chambers and during post test measurements. Before the measurement, the capacitors shall be fully discharged. The insulation resistance shall be measured between the terminations.

8.6.3.3 Measuring conditions

See IEC 60384-1:2016, 4.5.2, with the following details.

The measuring voltage may be of any value not greater than U_R , the referee voltage being U_R , for a capacitor with a rated voltage below or equal to 1 kV. For $U_R > 1$ kV the referee voltage shall be 1 kV.

The insulation resistance (R_i) shall be measured after the voltage has been applied for (60 ± 5) s.

For lot-by-lot testing (Group A), the test may be terminated in a shorter time, if the required value of insulation resistance is reached.

The product of the internal resistance of the voltage source and the nominal capacitance of the capacitor shall not exceed 1 s, unless otherwise prescribed in the detail specification.

The charge current shall not exceed 0,05 A. For capacitors with rated voltages of 1 kV and above, a lower limit (value) may be given in the detail specification.

8.6.3.4 Requirements

The insulation resistance shall meet the following requirements.

$C_N \leq 25 \text{ nF}$	$R_i \geq 4\,000 \text{ M}\Omega$
$C_N > 25 \text{ nF}$	$R_i \times C_N \geq 100 \text{ s}$

8.6.4 Voltage proof

8.6.4.1 General

See IEC 60384-1:2016, 4.6, with the details of 8.6.4.2 to 8.6.4.4.

8.6.4.2 Test conditions

The product of R_1 and the nominal capacitance C_X shall be smaller than or equal to 1 s.

NOTE R_1 is a charging resistor that includes the internal resistance of the voltage source. See IEC 60384-1:2016, 4.6.2.

The charge current shall not exceed 0,05 A.

For capacitors with rated voltages of 1 kV and above, a lower charge current limit value may be given in the detail specification. To protect the capacitors against flashover, the test may be performed in a suitable insulating medium.

8.6.4.3 Test voltages

The test voltages in accordance with Table 10 shall be applied between the measuring points of 8.6.3 and Table 3 in IEC 60384-1:2016, for a period of 1 min for qualification approval testing and for a period of 1 s for the lot-by-lot quality conformance testing.

Table 10 – Test voltages

Rated voltage V	Test voltage V
$U_R \leq 100$	$2,5 U_R$
$100 < U_R \leq 200$	$1,5 U_R + 100$
$200 < U_R \leq 500$	$1,3 U_R + 100$
$500 < U_R < 1\,000$	$1,3 U_R$
$U_R \geq 1\,000$	$1,2 U_R$

8.6.4.4 Requirement

There shall be no breakdown or flashover during the test.

8.6.5 Impedance (if required by the detail specification)

8.6.5.1 General

See IEC 60384-1:2016, 4.10, with the details of 8.6.5.2 and 8.6.5.3.

8.6.5.2 Measuring conditions

The frequency of measurement: 100 kHz, with a relative tolerance of ± 10 %.

8.6.5.3 Requirements

Impedance shall be specified in the detail specification.

8.6.6 Equivalent series resistance [ESR] (if required by the detail specification)

8.6.6.1 General

See IEC 60384-1:2016, 4.8.2, with the details of 8.6.6.2 and 8.6.6.3.

8.6.6.2 Measuring conditions

The frequency of measurement: 100 kHz, with a relative tolerance of ± 10 %.

8.6.6.3 Requirements

The ESR shall be specified in the detail specification.

8.7 Temperature characteristic of capacitance

8.7.1 Special preconditioning

See 8.2.

8.7.2 Measuring conditions

See IEC 60384-1:2016, 4.24.1, with the following details.

The capacitors shall be measured in the unmounted state as well as the conditions of Table 11.

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Table 11 – Details of measuring conditions

Measuring step	Temperature °C	DC voltage applied
1	20 ± 2	–
2	$T_A^a \pm 3$	–
3	20 ± 2	–
4	$T_B^b \pm 2$	–
5	$T_B \pm 2$	×
6	20 ± 2	×
7	$T_A \pm 3$	×
8	20 ± 2	–
Measurements may be made at such intermediate temperatures as to ensure that the requirements of 6.2.5 are met.		
NOTE 1 "–" indicates: no DC voltage applied "×" indicates: DC voltage applied (if specified in the detail specification)		
NOTE 2 Reference capacitance is the capacitance measured at Step 3.		
NOTE 3 Because of the effects described in the Note in 8.2, the capacitance values measured at temperature reference, Steps 5 to 7, with DC voltage applied, are time dependent. This time dependency is included in the given limits for capacitance change. The capacitance change between the first and the last measurements at temperature reference, Steps 1 and 8, indicates the amount of ageing involved. In the case of a dispute about the results of measurements with DC voltage applied, it is advisable to agree upon a fixed time interval between measurements at temperature reference, Steps 5 and 7 with DC voltage applied (see IEC 60384-1:2016, 4.24.1.3).		
^a T_A = Lower category temperature.		
^b T_B = Upper category temperature.		

8.7.3 Requirements

Temperature characteristic with and without DC voltage applied shall not exceed the values given in Table 3. The variation of capacitance shall be calculated in accordance with IEC 60384-1:2016, 4.24.3.1.

8.8 Shear test

See IEC 60384-1:2016, 4.34.

A force shall be selected from 1 N, 2 N, 5 N or 10 N and specified in the detail specification.

8.9 Substrate bending test

8.9.1 General

See IEC 60384-1:2016, 4.35.

Unless otherwise specified in the detail specification,

- the deflection D shall be selected from 1 mm, 2 mm or 3 mm,
- the number of bends shall be 1 time,
- the radius of the bending tool shall be 5 mm.

When the deflection D is 2 mm or less, the radius may be 230 mm.

- the duration in the bent state shall be 5 s.

For 1005M or smaller size, the thickness of substrate should be 0,8 mm.

8.9.2 Initial measurement

The capacitance shall be measured as specified in 8.6.1 and in the detail specification.

8.9.3 Final inspection

The capacitors shall be visually examined and there shall be no visible damage.

The change of capacitance with board in bent position shall not exceed 10 %.

8.10 Resistance to soldering heat

8.10.1 General

See IEC 60068-2-58 with the details of 8.10.2 to 8.10.6.

8.10.2 Special preconditioning

See 8.2.

8.10.3 Initial measurement

The capacitance shall be measured in accordance with 8.6.1.

8.10.4 Test conditions

8.10.4.1 Solder bath method (applicable to 1608M, 2012M and 3216M)

NOTE See Table A.1 for explanation of the size code.

See IEC 60068-2-58:2015, Test Td₂, Method 1, with the following details, if not otherwise specified in the detail specification

The specimen shall be preheated to a temperature of 110 °C to 140 °C and maintained for 30 s to 60 s.

Solder alloy: Sn-Pb or Sn-Ag-Cu

Temperature: 260 °C ± 5 °C

Duration of immersion: 10 s ± 1 s

Depth of immersion: 10 mm

Number of immersions: 1

8.10.4.2 Infrared and forced gas convection soldering system

See IEC 60068-2-58:2015, Test Td₂, Method 2, with the following details:

- the solder paste shall be applied to the test substrate;
- the thickness of solder deposit shall be specified in the detail specification;
- the terminations of the specimen shall be placed on the solder paste;
- solder alloy: Sn-Pb;

unless otherwise specified in the detail specification, the specimen and test substrate shall be preheated to a temperature of (150 ± 10) °C and maintained for 60 s to 120 s in infrared and forced gas convection soldering system;

the temperature of the reflow system shall be quickly raised until the specimen has reached $(235 \pm 5) \text{ }^\circ\text{C}$ and maintained at this temperature for $(10 \pm 1) \text{ s}$;

e) solder alloy: Sn-Ag-Cu;

unless otherwise specified in the detail specification, the reflow temperature profile shall be selected from Table 12 and Figure 6;

Table 12 – Reflow temperature profiles for Sn-Ag-Cu alloy

Alloy composition		T_1 °C	T_2 °C	t_1 s	T_3 °C	t_2 s	T_4 °C	t_3 s
Lead-free solder (Sn-Ag-Cu)	Test 1	150 ± 5	180 ± 5	120 ± 5	220	60 to 90	250	20 to 40 at $T_4 \pm 5 \text{ K}$
	Test 2	150 ± 5	180 ± 5	120 ± 5	220	≤ 60	255	≤ 20 at $T_4 \pm 10 \text{ K}$

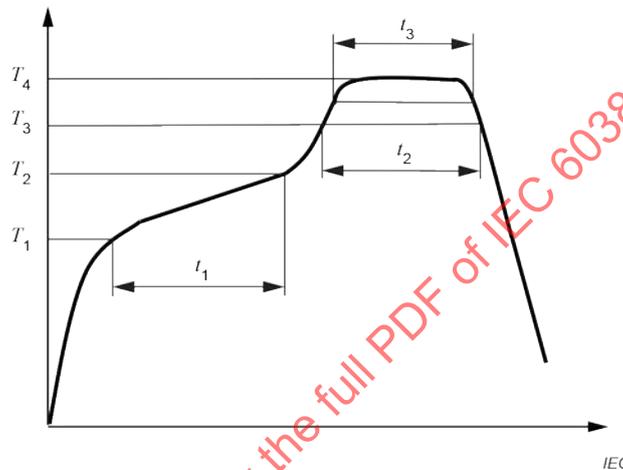


Figure 6 – Reflow temperature profile

- f) number of each test: 1, unless otherwise specified in the detail specification;
- g) the temperature profile of d) or e) shall be specified in the detail specification.

8.10.5 Recovery

The capacitors shall recover for $24 \text{ h} \pm 2 \text{ h}$.

The flux residues shall be removed with a suitable solvent.

8.10.6 Final inspection, measurements and requirements

After recovery, the capacitors shall be visually examined and measured and shall meet the following requirements.

Under normal lighting and approximately 10x magnification, there shall be no signs of damage such as cracks.

Dissolution of the end face plating (leaching) shall not exceed 25 % of the length of the edge concerned. The detail specification may prescribe further details.

The capacitance shall be measured in accordance with 8.6.1 and the change shall not exceed the values in Table 13.

Table 13 – Maximum capacitance change

Subclass	Requirements
2B and 2C	±10 %
2D and 2R	±15 %
2E and 2F	±20 %
NOTE See 6.2.5 for an explanation of the subclass codes.	

8.11 Solderability

8.11.1 General

See IEC 60068-2-58 with the details of 8.11.2 to 8.11.4.

8.11.2 Test conditions

8.11.2.1 Solder bath method (applicable to 1608M, 2012M and 3216M)

NOTE See Table A.1 for explanation of the size code.

See IEC 60068-2-58:2015, Test Td₁, Method 1, with the following details, if not otherwise specified in the detail specification.

The specimen shall be preheated to a temperature of 80 °C to 140 °C and maintained for 30 s to 60 s.

Solder alloy:	Sn-Pb	Sn-Ag-Cu
Temperature:	(235 ± 5) °C	(245 ± 5) °C
Duration of immersion:	(2 ± 0,2) s	(3 ± 0,3) s
Depth of immersion:	10 mm	10 mm
Number of immersions:	1	1

8.11.2.2 Infrared and forced gas convection soldering system

See IEC 60068-2-58:2015, Test Td₁, Method 2, with the following details:

- the solder paste shall be applied to the test substrate;
- the thickness of solder deposit shall be specified in the detail specification;
- the terminations of the specimen shall be placed on the solder paste;
- solder alloy: Sn-Pb;

unless otherwise specified in the detail specification, the specimen and test substrate shall be preheated to a temperature of (150 ± 10) °C and maintained for 60 s to 120 s in the infrared and forced gas convection soldering system;

the temperature of the reflow system shall be quickly raised until the specimen has reached (215 ± 3) °C and maintained at this temperature for (10 ± 1) s;

- solder alloy: Sn-Ag-Cu;

unless otherwise specified in the detail specification, the specimen and test substrate shall be preheated to a temperature of (150 ± 5) °C to (180 ± 5) °C for 60 s to 120 s in the infrared and forced gas convection soldering system;

the temperature of the reflow system shall be quickly raised until the specimen has reached (235 ± 3) °C. The time above 225 °C shall be (20 ± 5) s;

- the temperature profile of d) or e) shall be specified in the detail specification.

8.11.3 Recovery

The flux residues shall be removed with a suitable solvent.

8.11.4 Final inspection, measurements and requirements

The capacitors shall be visually examined under normal lighting and approximately 10x magnification. There shall be no signs of damage.

Both end face and the contact areas shall be covered with a smooth and bright solder coating with no more than a small amount of scattered imperfections such as pinholes or unwetted or de-wetted areas. These imperfections shall not be concentrated in one area.

The detail specification may prescribe further requirements.

8.12 Rapid change of temperature

8.12.1 General

This test shall be applied only to capacitors for which the category temperature is greater 110 °C.

See IEC 60384-1:2016, 4.16, with the details of 8.12.2 to 8.12.6.

The capacitors shall be mounted in accordance with 8.4.

8.12.2 Special preconditioning

See 8.2.

8.12.3 Initial measurement

The capacitance shall be measured in accordance with 8.6.1.

8.12.4 Number of cycles

The number of cycles: 5

Duration of exposure at the temperature limits: 30 min.

8.12.5 Recovery

The capacitors shall recover for 24 h ± 2 h.

8.12.6 Final inspection, measurements and requirements

The capacitors shall be visually examined. There shall be no visible damage.

The capacitance shall be measured in accordance with 8.6.1 and the change shall not exceed the value in Table 14.

Table 14 – Maximum capacitance change

Subclass	Requirements
2B and 2C	±10 %
2D and 2R	±15 %
2E and 2F	±20 %
NOTE See 6.2.5 for an explanation of the subclass codes.	

8.13 Climatic sequence**8.13.1 General**

See IEC 60384-1:2016, 4.21, with the details of 8.13.2 to 8.13.8.

8.13.2 Special preconditioning

See 8.2.

8.13.3 Initial measurement

The capacitance shall be measured in accordance with 8.6.1.

8.13.4 Dry heat

See IEC 60384-1:2016, 4.21.3.

8.13.5 Damp heat, cyclic, Test Db, first cycle

See IEC 60384-1:2016, 4.21.4.

8.13.6 Cold

See IEC 60384-1:2016, 4.21.5, with the following details.

The capacitors shall be visually examined. There shall be no visible damage.

8.13.7 Damp heat, cyclic, Test Db, remaining cycles**8.13.7.1 General**

See IEC 60384-1:2016, 4.21.7, with the details of 8.13.7.2 and 8.13.7.3.

8.13.7.2 Test conditions

No voltage applied.

The remaining cycles shall be tested in accordance with Table 15.

Table 15 – Number of damp heat cycles

Category	No. of cycles of 24 h
- / - /56	5
- / - /21	1
- / - /10	1
- / - /04	0

8.13.7.3 Recovery

The capacitors shall recover for 24 h ± 2 h.

8.13.8 Final inspection, measurements and requirements

The capacitors shall be visually examined. There shall be no visible damage.

The capacitors shall be measured and shall meet the requirements in Table 16.

If the capacitance value is less than the minimum value permitted, then after the other measurements have been made the capacitor shall be preconditioned in accordance with 8.2 and then the requirement in Table 16 shall be met.

Table 16 – Final inspection, measurements and requirements

Measurement	Measuring conditions	Requirements			
		Subclasses 2B and 2C	Subclasses 2D and 2R	Subclasses 2E	Subclasses 2F
Capacitance	8.6.1	$\Delta C/C \leq \pm 10 \%$	$\Delta C/C \leq \pm 15 \%$	$\Delta C/C \leq \pm 20 \%$	$\Delta C/C \leq \pm 30 \%$
Tangent of loss angle	8.6.2	$\leq 2 \times$ value of 8.6.2			
Insulation resistance	8.6.3	$R_i \geq 1\,000\text{ M}\Omega$ or $R_i \times C_N \geq 25\text{ s}$ (whichever is less of the two values)			
NOTE See 6.2.5 for an explanation of the subclass codes.					

8.14 Damp heat, steady state

8.14.1 General

See IEC 60384-1:2016, 4.22, with the details of 8.14.2 to 8.14.6.

The capacitors shall be mounted in accordance with 8.4.

8.14.2 Special preconditioning

See 8.2.

8.14.3 Initial measurement

The capacitance shall be measured in accordance with 8.6.1.

8.14.4 Test conditions

No voltage shall be applied, unless otherwise specified in the detail specification.

The severity of the test should be selected from the test conditions as shown in Table 17 and be specified in the detail specification.

The duration time should be selected in accordance with 6.1 and shall be specified in the detail specification.

Table 17 – Test conditions for damp heat, steady state

Severity	Temperature °C	Relative humidity %
1	+85 ± 2	85 ± 3
2	+60 ± 2	93 ± 3
3	+40 ± 2	93 ± 3

When the application of voltage is prescribed, U_R shall be applied to one half of the lot and no voltage shall be applied to the other half of the lot.

Within 15 min after removal from the damp heat test, the voltage proof test in accordance with 8.6.4 shall be carried out, but with the rated voltage applied.

For safety reasons, different conditions for the application of voltage to capacitors with rated voltages of 1 kV or above may be given in the detail specification.

8.14.5 Recovery

The capacitors shall recover for 24 h ± 2 h.

8.14.6 Final inspection, measurements and requirements

The capacitors shall be visually examined. There shall be no visible damage.

The capacitors shall be measured and shall meet the requirements in Table 18.

If the capacitance value is less than the minimum value permitted, then after the other measurements have been made, the capacitors shall be preconditioned in accordance with 8.2 and then the requirement in Table 18 shall be met.

Table 18 – Final inspection, measurements and requirements

Measurement	Measuring conditions	Requirements			
		Subclasses 2B and 2C	Subclasses 2D and 2R	Subclasses 2E	Subclasses 2F
Capacitance	8.6.1	$\Delta C/C \leq \pm 10 \%$	$\Delta C/C \leq \pm 15 \%$	$\Delta C/C \leq \pm 20 \%$	$\Delta C/C \leq \pm 30 \%$
Tangent of loss angle	8.6.2	$\leq 2 \times$ value of 8.6.2			
Insulation resistance	8.6.3	$R_i \geq 1\,000\text{ M}\Omega$ or $R_i \times C_N \geq 25\text{ s}$ (whichever is less of the two values)			
NOTE See 6.2.5 for an explanation of the subclass codes.					

8.15 Endurance

8.15.1 General

See IEC 60384-1:2016, 4.23, with the details of 8.15.2 to 8.15.6.

The capacitors shall be mounted in accordance with 8.4.

8.15.2 Special preconditioning

See 8.2.

8.15.3 Initial measurement

The capacitance shall be measured in accordance with 8.6.1.

8.15.4 Test conditions

If the category voltage is equal to the rated voltage, the capacitors shall be tested as in Table 19.

Table 19 – Endurance test conditions ($U_C = U_R$)

U_R	$U_R \leq 200$	$200 < U_R \leq 500$	$U_R > 500$
Temperature	Upper category temperature		
Voltage (DC)	$1,5 U_R$	$1,3 U_R$	$1,2 U_R$
Duration	1 000 h	1 500 h	2 000 h

If the category voltage is not equal to the rated voltage, the capacitors shall be tested as in Table 20.

Table 20 – Endurance test conditions ($U_C \neq U_R$)

U_R	$U_R \leq 200$		$200 < U_R \leq 500$		$U_R > 500$	
Temperature	T_R	T_B	T_R	T_B	T_R	T_B
Voltage (DC)	$1,5 U_R$	$1,5 U_C$	$1,3 U_R$	$1,3 U_C$	$1,2 U_R$	$1,2 U_C$
Duration	1 000 h		1 500 h		2 000 h	
Sample	Divided into two parts		Divided into two parts		Divided into two parts	
T_R = Rated temperature. T_B = Upper category temperatures > 85 °C, such as 100 °C.						

8.15.5 Recovery

The capacitors shall recover for 24 h ± 2 h.

8.15.6 Final inspection, measurements and requirements

The capacitors shall be visually examined. There shall be no visible damage.

The capacitors shall be measured and shall meet the requirements in Table 21.

If the capacitance value is less than the minimum value permitted, then after the other measurements have been made the capacitor shall be preconditioned in accordance with 8.2 and then the requirement in Table 21 shall be met.

Table 21 – Final inspection, measurements and requirements of endurance test

Measurement	Measuring conditions	Requirements			
		Subclasses 2B and 2C	Subclasses 2D and 2R	Subclasses 2E	Subclasses 2F
Capacitance	8.6.1	$\Delta C/C \leq \pm 10 \%$	$\Delta C/C \leq \pm 15 \%$	$\Delta C/C \leq \pm 20 \%$	$\Delta C/C \leq \pm 30 \%$
Tangent of loss angle	8.6.2	$\leq 2 \times$ value of 8.6.2			
Insulation resistance	8.6.3	$R_i \geq 2\,000\text{ M}\Omega$ or $R_i \times C_N \geq 50\text{ s}$ (whichever is less of the two values)			
NOTE See 6.2.5 for an explanation of the subclass codes.					

8.16 Robustness of terminations (only for capacitors with strip termination)

8.16.1 General

See IEC 60384-1:2016, 4.13, with the details of 8.16.2 and 8.16.3.

8.16.2 Test conditions

Unless otherwise specified in the detail specification, the conditions of the tests are as follows:

- Test U_{a1} : force: 2,5 N;
- Test U_b , Method 1: force: 2,5 N;
- number of bends: 1.

8.16.3 Final inspection and requirements

The capacitors shall be visually examined. There shall be no visible damage.

8.17 Component solvent resistance (if required)

See IEC 60384-1:2016, 4.31.

8.18 Solvent resistance of the marking (if required)

See IEC 60384-1:2016, 4.32.

8.19 Accelerated damp heat, steady state (if required)

8.19.1 General

See IEC 60384-1:2016, Annex H, with the details of 8.19.2 to 8.19.5.

The capacitors shall be mounted in accordance with 8.4 and IEC 60384-1:2016, Clause H.1.

Half the capacitors shall be connected in series with resistors of 100 k Ω , with a relative tolerance of $\pm 10 \%$ and half in series with resistors of 6,8 k Ω , with a relative tolerance of $\pm 10 \%$.

8.19.2 Initial measurement

The capacitors shall be measured for insulation resistance with a voltage of 1,5 V \pm 0,1 V applied across the capacitor and resistor in series.

The insulation resistance, including the series resistor, shall meet the requirements given in Table 22.

Table 22 – Initial requirements

Measurement	Measuring conditions	Requirements	
Insulation resistance	(1,5 ± 0,1) V	Connected to 100 kΩ resistors	$C_N \leq 25 \text{ nF}: R_i \geq 4\,000 \text{ M}\Omega$ $C_N > 25 \text{ nF}: (R_i - 100 \text{ k}\Omega) \times C_N \geq 100 \text{ s}$
		Connected to 6,8 kΩ resistors	$C_N \leq 25 \text{ nF}: R_i \geq 4\,000 \text{ M}\Omega$ $C_N > 25 \text{ nF}: (R_i - 6,8 \text{ k}\Omega) \times C_N \geq 100 \text{ s}$

8.19.3 Conditioning

The capacitors with associated resistors shall be subjected to conditioning at (85 ± 2) °C, (85 ± 3) % relative humidity for the test duration given in Table 23. The voltage given in Table 23 shall be applied to the capacitors connected to 100 kΩ resistors and those connected to 6,8 kΩ resistors. In both cases, the voltage shall be applied across the capacitor/resistor combination.

Care shall be taken to avoid condensation of water on the capacitors or substrates. This may happen if the door is opened during the test before the humidity is lowered.

Table 23 – Conditioning

Connected resistors kΩ	Applied voltage	Duration
100	(1,5 ± 0,1) V or the voltage specified in the detail specification	168 h, 500 h or 1 000 h; as specified in the detail specification
6,8	(50 ± 0,1) V or U_R , whichever is the lower, or the voltage specified in the detail specification	

8.19.4 Recovery

The applied voltage shall be disconnected and the capacitors and resistors shall be removed from the test chamber and allowed to recover for 22 h to 26 h in standard atmospheric conditions for testing.

8.19.5 Final measurements

The capacitors shall be measured for insulation resistance, as in 8.19.2.

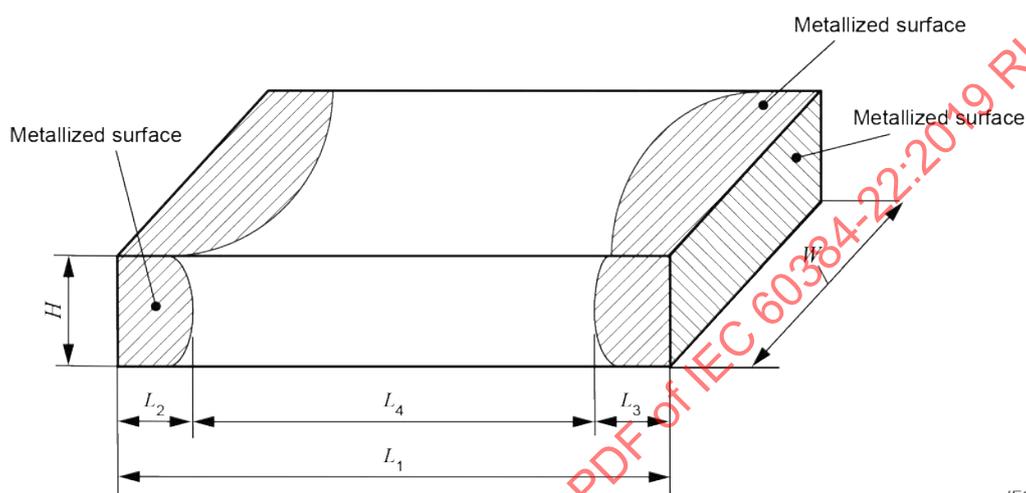
The insulation resistance, including the series resistor, shall be greater than 0,1 times the values given in 8.19.2.

Annex A (normative)

Guidance for the specification and coding of dimensions of fixed surface mount multilayer capacitors of ceramic dielectric, Class 2

The principles given in Figure A.1 should be considered in the dimensioning of the capacitors.

Dimensions are specified in Table A.1.



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Dimension W should not exceed dimension L_1 .

Dimension H should not exceed dimension W .

If necessary, the thickness of tinning should be specified.

Figure A.1 – Dimensions

Table A.1 – Dimensions

Code	Length L_1	Width W	$L_2; L_3$ Minimum	L_4 Minimum
0201M	$0,25 \pm 0,013$	$0,125 \pm 0,013$	0,04	0,06
0402M	$0,4 \pm 0,02$	$0,2 \pm 0,02$	0,05	0,1
0603M	$0,6 \pm 0,03$	$0,3 \pm 0,03$	0,1	0,2
1005M	$1,0 \pm 0,05$	$0,5 \pm 0,05$	0,1	0,3
1608M	$1,6 \pm 0,1$	$0,8 \pm 0,1$	0,2	0,5
2012M	$2,0 \pm 0,1$	$1,25 \pm 0,1$	0,2	0,7
3216M	$3,2 \pm 0,2$	$1,6 \pm 0,15$	0,3	1,4
3225M	$3,2 \pm 0,2$	$2,5 \pm 0,2$	0,3	1,4
4532M	$4,5 \pm 0,3$	$3,2 \pm 0,2$	0,3	2,0
5750M	$5,7 \pm 0,4$	$5,0 \pm 0,4$	0,3	2,5

NOTE Dimension in millimetres.

Other case sizes and dimensions may be specified in the detail specification.

Annex B (informative)

Capacitance ageing of fixed capacitors of ceramic dielectric, Class 2

B.1 General

Most Class 2 dielectrics used for ceramic capacitors have ferroelectric properties and exhibit a Curie temperature.

Above this temperature the dielectric has the highly symmetric cubic crystal structure whereas below the Curie temperature the crystal structure is less symmetrical. Although in single crystals this phase transition is very sharp, in practical ceramics, it is often spread over a finite temperature range, but in all cases it is linked with a peak in the capacitance/temperature curve.

Under the influence of thermal vibration, the ions in the crystal lattice continue to move to positions of lower potential energy for a long time after the dielectric has cooled through the Curie temperature. This gives rise to the phenomenon of capacitance ageing, whereby the capacitor continually decreases its capacitance.

However, if the capacitor is heated to a temperature above the Curie temperature, then de-ageing takes place; i.e. the capacitance lost through ageing is regained, and ageing recommences from the time when the capacitor recools.

B.2 Law of capacitance ageing

During the first hour after cooling through the Curie temperature, the loss of capacitance is not well defined, but after this time it follows a logarithmic law (see K.W. Plessner, Proc. Phys. Soc., vol. 69B, P1261, 1956) which can be expressed in terms of an ageing constant.

The ageing constant k is defined as the percentage loss of capacitance due to the ageing process of the dielectric which occurs during a "decade", i.e. a time in which the capacitor increases its age tenfold for example from 1 h to 10 h.

As the law of decrease of capacitance is logarithmic, the percentage loss of capacitance will be $2 \times k$ between 1 h and 100 h age and $3 \times k$ between 1 h and 1 000 h. This may be expressed mathematically by the following equation:

$$C_t = C_1 \left(1 - \frac{k}{100} \times \lg t \right)$$

where

C_t is the capacitance t h after the start of the ageing process;

C_1 is the capacitance 1 h after the start of the ageing process;

k is the ageing constant in percent per decade (as defined above);

t is the time in h from the start of the ageing process.

The ageing constant may be declared by the manufacturer for a particular ceramic dielectric, or it may be defined by de-ageing the capacitor and measuring the capacitance at two known times thereafter.

k is then given by the following equation:

$$k = \frac{100 \times (C_{t_1} - C_{t_2})}{C_{t_1} \times \lg t_2 - C_{t_2} \times \lg t_1}$$

If capacitance measurements are made three or more times, then it is possible to derive k from the slope of a graph where C_t is plotted against $\lg t$.

It is also possible to plot $\log C$ against $\lg t$.

During measurements of ageing, the capacitor should be maintained at a constant temperature so that capacitance variations due to the temperature characteristic do not mask those due to ageing.

B.3 Capacitance measurements and capacitance tolerance

Because of ageing, it is necessary to specify a reference age at which the capacitance shall be within the prescribed tolerance. This is fixed at 1 000 h, since for practical purposes there is not much further loss of capacitance after this time.

In order to calculate the capacitance $C_{1\,000}$ after 1 000 h, the ageing constant shall be known or determined as in Clause B.2, when the following formula may be used:

$$C_{1000} = C_t \left[1 - \frac{k}{100} (3 - \lg t) \right]$$

For factory measurements, the loss of capacitance from the age at time of measurement to 1 000 h age will be known and can be off-set by using asymmetric inspection tolerances.

For example, if it is known that the capacitance loss will be 5 %, then the capacitors may be inspected to limits of +25/–15 % instead of 20 %.

Capacitance is normally declared at 20 °C, and it may be necessary to measure at this temperature or correct the results to this temperature. Errors can also arise from heat from the hands, and capacitors should therefore always be handled with tweezers.

B.4 Special preconditioning (see 8.2)

In many of the tests in this document, it is required to measure the capacitance change which results from a given conditioning (for example climatic sequence). In order to avoid the interfering effect of ageing, the capacitor is specially preconditioned before these tests by maintaining it for 1 h at the upper category temperature followed by 24 h at standard atmospheric conditions for testing.

For those capacitors with a Curie temperature below the upper category temperature, this results in de-ageing and the conditioning is also arranged, if possible, to bring the capacitors to an age of 24 h, so that capacitance changes due to ageing are minimized.

If the Curie temperature of the dielectric is above the upper category temperature, then the special preconditioning will not completely de-age the capacitor, but it will nevertheless bring it into a state where its capacitance is not so dependent on its previous history, and the same effect will be achieved, though completely de-aged. In order to de-age such capacitors completely, temperature up to 160 °C may be required, and this temperature could be deleterious to the encapsulation. Therefore, in the few cases where complete de-ageing of such capacitors may be required, the detail specification shall be consulted for details and any necessary precautions.

Annex C
(informative)

**Temperature characteristics of capacitance
for the reference temperature of 25 °C**

The temperature characteristics of capacitance for the reference temperature of 25 °C have often been used due to marketing needs and because of their actual performance. These temperature characteristics and codes are shown in Table C.1 and the exact conditions of temperature characteristics of capacitance are shown in Table C.2.

**Table C.1 – Temperature characteristics of capacitance
for the reference temperature of 25 °C**

Code of temperature characteristics of capacitance	Maximum capacitance change %	Temperature range °C
X5R	± 15	-55 to +85
X7R	± 15	-55 to +125
X8R	± 15	-55 to +150
X6S	± 22	-55 to +105
X7S	± 22	-55 to +125
Y5V	-82 to +22	-30 to +85

**Table C.2 – Measuring conditions of temperature characteristic
of capacitance for the reference temperature of 25 °C**

Measuring step	Temperature °C
1	25 ± 2
2	$T_A^a \pm 3$
3	25 ± 2
4	$T_B^b \pm 2$
5	25 ± 2
Measurements may be made at such intermediate temperatures as to ensure that the requirements of Table C.1 are met.	
NOTE Reference capacitance is the capacitance measured at Step 3.	
^a T_A = Lower category temperature.	
^b T_B = Upper category temperature.	

Annex X (informative)

Cross-reference for reference to IEC 60384-22:2011

The drafting of this document has resulted in a new structure. Table X.1 indicates the new clause and subclause numbers with respect to IEC 60384-22:2011.

Table X.1 – Reference to IEC 60384-22 for clause/subclause

IEC 60384-22:2011 2 nd edition Clause/Subclause	IEC 60384-22:20xx 3 rd edition Clause/Subclause	Notes
1 1.1 1.2	1	Scope and Object are merged into one in accordance with the ISO/IEC Directives Part 2
1.3	2	In accordance with ISO/IEC Directives Part 2
1.4	4	In accordance with the change of clause numbers
1.5	3	In accordance with ISO/IEC Directives Part 2
1.6	5	In accordance with the change of clause numbers
2	6	In accordance with the change of clause numbers
3	7	In accordance with the change of clause numbers
4	8	In accordance with the change of clause numbers

Table X.2 indicates the new figure and table numbers with respect to IEC 60384-22:2011.

Table X.2 – Reference to IEC 60384-22 for figure/table

IEC 60384-22:2011 2 nd edition Figure/Table	IEC 60384-22:20xx 3 rd edition Figure/Table	Notes
Table 6a	Table 6	In accordance with the ISO/IEC directives, Part 2 and the change of table numbers
Table 6b	Table 7	
Table 7 to Table 22	Table 8 to Table 23	In accordance with the change of table numbers
For the figure numbers, there was no change.		

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COMMISSION ÉLECTROTECHNIQUE INTERNATIONALE

**CONDENSATEURS FIXES UTILISÉS
DANS LES ÉQUIPEMENTS ÉLECTRONIQUES –****Partie 22: Spécification intermédiaire –
Condensateurs multicouches fixes à diélectriques
en céramique pour montage en surface, de Classe 2**

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La Norme internationale IEC 60384-22 a été établie par le comité d'études 40 de l'IEC: Condensateurs et résistances pour équipements électroniques.

Cette troisième édition annule et remplace la deuxième édition parue en 2011. Cette édition constitue une révision technique.

La présente édition inclut les modifications techniques majeures suivantes par rapport à l'édition précédente:

- a) révision de la structure conformément aux directives ISO/IEC, Partie 2:2016 (septième édition), dans la mesure du possible, et pour l'harmonisation avec l'IEC 60384-21;

- b) suppression de la description de la puissance réactive admissible en 6.2.2, parce qu'elle n'est pas adaptée aux besoins du présent document;
- c) les dimensions de 0201M à l'Annexe A ont été ajoutées.

Le texte de cette Norme internationale est issu des documents suivants:

FDIS	Rapport de vote
40/2640FDIS	40/2652RVD

Le rapport de vote indiqué dans le tableau ci-dessus donne toute information sur le vote ayant abouti à l'approbation de cette Norme internationale.

Ce document a été rédigé selon les Directives ISO/IEC, Partie 2.

Une liste de toutes les parties de la série IEC 60384, publiées sous le titre général *Condensateurs fixes utilisés dans les équipements électroniques*, peut être consultée sur le site web de l'IEC.

Le comité a décidé que le contenu de ce document ne sera pas modifié avant la date de stabilité indiquée sur le site web de l'IEC sous "<http://webstore.iec.ch>" dans les données relatives à la publication recherchée. A cette date, le document sera

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CONDENSATEURS FIXES UTILISÉS DANS LES ÉQUIPEMENTS ÉLECTRONIQUES –

Partie 22: Spécification intermédiaire – Condensateurs multicouches fixes à diélectriques en céramique pour montage en surface, de Classe 2

1 Domaine d'application

La présente partie de l'IEC 60384 est applicable aux condensateurs multicouches fixes à diélectriques en céramique pour montage en surface non encapsulés, Classe 2, utilisés dans les équipements électroniques. Ces condensateurs possèdent des pastilles de connexion métallisées ou des bandes de brasure et sont destinés à être montés sur des cartes imprimées ou directement sur des substrats de circuits hybrides.

Les condensateurs d'antiparasitage ne sont pas inclus, mais ils sont couverts par l'IEC 60384-14.

L'objet du présent document est de prescrire des caractéristiques et des valeurs assignées préférentielles et de sélectionner à partir de l'IEC 60384-1 les procédures d'assurance de la qualité, les essais et les méthodes de mesure appropriées et de donner les exigences de performance générales pour ce type de condensateur. Les exigences et les sévérités des essais prescrits dans les spécifications particulières se référant à la présente spécification intermédiaire sont d'un niveau de performance supérieur ou égal; des niveaux de performance inférieurs ne sont pas admis.

2 Références normatives

Les documents suivants cités dans le texte constituent, pour tout ou partie de leur contenu, des exigences du présent document. Pour les références datées, seule l'édition citée s'applique. Pour les références non datées, la dernière édition du document de référence s'applique (y compris les éventuels amendements).

IEC 60063, *Séries de valeurs normales pour résistances et condensateurs*

IEC 60068-1:2013, *Essais d'environnement – Partie 1: Généralités et lignes directrices*

IEC 60068-2-58:2015, *Essais d'environnement – Partie 2-58: Essais – Essai Td – Méthodes d'essai de la soudabilité, résistance de la métallisation à la dissolution et résistance à la chaleur de brasage des composants pour montage en surface (CMS)*
IEC 60068-2-58:2015/AMD1:2017

IEC 60384-1:2016, *Condensateurs fixes utilisés dans les équipements électroniques – Partie 1: Spécification générique*

IEC 61193-2:2007, *Quality assessment system – Part 2: Selection and use of sampling plans for inspection of electronic components and packages* (disponible en anglais seulement)

ISO 3:1973, *Nombres normaux – Séries de nombres normaux*

3 Termes et définitions

Pour les besoins du présent document, les termes et les définitions de l'IEC 60384-1, ainsi que les suivants, s'appliquent.

L'ISO et l'IEC tiennent à jour des bases de données terminologiques destinées à être utilisées en normalisation, consultables aux adresses suivantes:

- IEC Electropedia: disponible à l'adresse <http://www.electropedia.org/>
- ISO Online browsing platform: disponible à l'adresse <http://www.iso.org/obp>

3.1

condensateur multicouche pour montage en surface

condensateur multicouche dont les petites dimensions et la nature ou la forme des connexions de sortie en font un condensateur pouvant être monté en surface dans des circuits hybrides et sur des cartes imprimées

3.2

condensateur à diélectrique en céramique, Classe 2

condensateur doté d'un diélectrique à permittivité élevée et adapté pour des applications de contournement et de couplage ou pour des circuits discriminateurs de fréquence lorsque de faibles pertes et une stabilité élevée de capacité ne sont pas de première importance

Note 1 à l'article: Le diélectrique en céramique est caractérisé par la variation non linéaire de capacité sur la plage de température de catégorie (voir Tableau 2).

3.3

sous-classe

variation maximale en pourcentage de capacité à l'intérieur de la plage de température de catégorie par rapport à la capacité à 20 °C

Note 1 à l'article: La sous-classe peut être exprimée sous forme de code (voir le Tableau 3).

3.4

plage de températures de catégorie

plage des températures ambiantes pour laquelle le condensateur est conçu en vue d'un fonctionnement permanent

Note 1 à l'article: Cette plage est donnée par la température minimale de catégorie et la température maximale de catégorie.

3.5

température assignée

T_R

température ambiante maximale à laquelle la tension assignée peut être appliquée de manière continue

3.6

tension assignée

U_R

tension continue maximale qui peut être appliquée de manière continue à un condensateur à n'importe quelle température entre la température minimale de catégorie et la température assignée

Note 1 à l'article: La tension continue maximale est la somme de la tension continue et de la valeur de crête de la tension alternative ou de la valeur de crête de la tension d'impulsion appliquées au condensateur.

3.7

tension de catégorie

U_C

tension maximale pouvant être appliquée de manière continue à un condensateur à sa température maximale de catégorie

4 Informations devant figurer dans une spécification particulière

4.1 Généralités

Les spécifications particulières doivent être établies à partir de la spécification particulière-cadre applicable.

Les spécifications particulières ne doivent pas indiquer d'exigences inférieures à celles de la spécification générique, intermédiaire ou particulière-cadre. Lorsque des exigences plus sévères sont incluses, elles doivent être indiquées en 1.9 de la spécification particulière et dans les programmes d'essais, par exemple par un astérisque.

Par commodité, les informations données en 4.2 peuvent être présentées sous forme de tableau.

Les informations présentées aux paragraphes 4.2 à 4.5 doivent être données dans chaque spécification particulière et il convient de choisir les valeurs citées parmi celles données dans l'article approprié de la présente spécification intermédiaire.

4.2 Dessin d'encombrement et dimensions

Une illustration des condensateurs doit être incluse pour permettre de reconnaître et de comparer facilement des condensateurs avec d'autres.

Les dimensions et les tolérances qui leur sont associées, qui affectent l'interchangeabilité et le montage, doivent être indiquées dans la spécification particulière. Toutes les dimensions doivent être indiquées en millimètres, toutefois, lorsque les dimensions originales sont indiquées en pouces, les dimensions métriques converties en millimètres doivent être ajoutées.

Normalement, les valeurs numériques de la longueur, de la largeur et de la hauteur du corps doivent être indiquées. Si nécessaire, par exemple lorsqu'un certain nombre d'éléments (tailles et plages de capacités/tensions) sont couverts par une spécification particulière, les dimensions et les tolérances associées doivent être placées dans un tableau sous le dessin.

Lorsque la configuration est différente de celle décrite ci-dessus, la spécification particulière doit indiquer de telles informations sur les dimensions afin de décrire correctement les condensateurs.

4.3 Montage

La spécification particulière doit donner des recommandations sur les méthodes de montage pour une utilisation normale. Les montages pour les essais et les mesures (si exigés) doivent être conformes à 8.4 de la présente spécification intermédiaire.

4.4 Valeurs assignées et caractéristiques

4.4.1 Généralités

Les valeurs assignées et les caractéristiques doivent être conformes aux articles correspondants de la présente spécification intermédiaire, ainsi qu'aux paragraphes 4.4.2, 4.4.3 et 4.4.4.

4.4.2 Plage de capacités nominales

Voir 6.2.4.1.

Si des produits approuvés conformément à la spécification particulière comportent des plages différentes, il convient d'ajouter la phrase suivante: "La plage de valeurs de capacités disponibles dans chaque plage de tensions est indiquée dans le registre des agréments, disponible par exemple sur le site internet du système de certificats en ligne de l'IECQ à l'adresse: www.iecq.org".

4.4.3 Caractéristiques particulières

Des caractéristiques supplémentaires peuvent être indiquées, si elles sont considérées comme nécessaires pour spécifier de façon appropriée le composant pour les besoins de la conception et de l'application.

4.4.4 Brasage

La spécification particulière doit prescrire les méthodes d'essai, les sévérités et les exigences applicables aux essais de brasabilité et de résistance à la chaleur de brasage.

4.5 Marquage

La spécification particulière doit indiquer le contenu du marquage sur le condensateur et sur l'emballage. Les écarts par rapport à l'Article 5 de la présente spécification intermédiaire doivent être indiqués de manière spécifique.

5 Marquage

5.1 Généralités

Voir 2.4 de l'IEC 60384-1:2016, avec les détails de 5.2 à 5.6.

5.2 Informations relatives au marquage

Les informations fournies par le marquage sont normalement sélectionnées dans la liste suivante; l'importance relative de chaque élément est indiquée par sa position dans la liste:

- capacité nominale;
- tension assignée (la tension continue peut être représentée par le symbole: --- [IEC 60417-5031(2002-10)] ou —);
- tolérance sur la capacité nominale;
- sous-classe de diélectrique, selon le cas (conformément à 6.2.5);
- année et mois (ou semaine) de fabrication;
- nom du fabricant ou marque commerciale;
- catégorie climatique;
- désignation du type par le fabricant;
- référence à la spécification particulière.

5.3 Marquage sur le corps

En général, le corps de ces condensateurs n'est pas marqué. Si un marquage peut être appliqué, il doit être bien lisible et comporter le maximum d'éléments cités en 5.2 selon ce qui est jugé utile. Il convient d'éviter les redondances sur le marquage du condensateur.

5.4 Exigences relatives au marquage

Tout marquage doit être lisible et difficilement effaçable par frottement des doigts.

5.5 Marquage de l'emballage

L'emballage contenant le ou les condensateurs doit comporter un marquage clair indiquant toutes les informations présentées en 5.2.

5.6 Marquage supplémentaire

Tout autre marquage doit être appliqué de telle sorte que cela n'entraîne pas de confusion.

6 Valeurs assignées et caractéristiques préférentielles

6.1 Caractéristiques préférentielles

Les catégories climatiques préférentielles doivent être données uniquement dans les caractéristiques préférentielles.

Les condensateurs couverts par le présent document sont classés en catégories climatiques conformément aux règles générales données dans l'Annexe A de l'IEC 60068-1:2013.

Les températures minimale et maximale de catégorie et la durée de l'essai continu de chaleur humide doivent être choisies dans la liste ci-dessous

- température minimale de catégorie: -55 °C , -40 °C , -25 °C , -10 °C et $+10\text{ °C}$;
- température maximale de catégorie: $+70\text{ °C}$, $+85\text{ °C}$, $+100\text{ °C}$, $+125\text{ °C}$ et $+150\text{ °C}$;
- durée de l'essai continu de chaleur humide (température = 40 °C , humidité relative = 93 %): 4, 10, 21 et 56 jours.

Les sévérités pour les essais de froid et de chaleur sèche sont les températures minimale et maximale de catégorie, respectivement.

NOTE La résistance à l'humidité résultant de catégorie climatique ci-dessus concerne les condensateurs dans l'état non monté. La performance climatique des condensateurs après montage dépend considérablement du substrat de montage, de la méthode de montage (voir 8.4) et du revêtement final.

6.2 Valeurs assignées préférentielles

6.2.1 Température assignée (T_R)

La température assignée est égale à la température maximale de catégorie pour les condensateurs dont la température maximale de catégorie ne dépasse pas 125 °C , sauf indication contraire de la spécification particulière.

6.2.2 Tension assignée (U_R)

Les valeurs préférentielles de la tension assignée sont les valeurs de la série R5 de l'ISO 3. Si d'autres valeurs sont nécessaires, elles doivent être choisies dans la série R10.

La somme de la tension continue et de la plus grande parmi la valeur de crête de la tension alternative et la valeur crête à crête de la tension alternative, appliquée au condensateur ne doit pas dépasser la tension assignée.

6.2.3 Tension de catégorie (U_C)

La tension de catégorie est égale à la tension assignée pour les condensateurs dont la température maximale de catégorie ne dépasse pas 125 °C. Toutes les tensions de catégorie différentes de la tension assignée, pour des condensateurs dont la température maximale de catégorie dépasse 125 °C ou pour des condensateurs à haute tension avec des tensions assignées d'environ 500 V, doivent être fournies par la spécification particulière.

Les valeurs préférentielles de la tension de catégorie à la température maximale de catégorie de 125 °C pour des condensateurs de grand volume, avec une tension assignée inférieure ou égale à 16 V et une température assignée de 85 °C sont données dans le Tableau 1.

Tableau 1 – Valeurs préférentielles de tensions de catégorie

U_R	V	2,5	4	6,3	10	16
U_C	V	1,6	2,5	4	6,3	10
NOTE Les valeurs numériques de U_C sont calculées à partir de la formule suivante:						
$U_C = 0,63 \times U_R$.						

6.2.4 Valeurs préférentielles de capacité nominale et valeurs de tolérance associées

6.2.4.1 Valeurs préférentielles de la capacité nominale

Les valeurs de capacité nominale doivent provenir de la série de valeurs de l'IEC 60063; les séries E3, E6 et E12 sont les séries préférentielles.

6.2.4.2 Tolérances préférentielles sur la capacité nominale

Voir Tableau 2.

Tableau 2 – Tolérances préférentielles

Séries préférentielles	Tolérance %	Lettre de codage
E3 et E6	-20/+80	Z
	-20/+50	S
E6	± 20	M
E6 et E12	± 10	K

6.2.5 Caractéristique de température de condensateur

Le Tableau 3 indique par une croix les valeurs préférentielles de la caractéristique de température avec et sans tension continue appliquée. La méthode de codage de la sous-classe est également donnée; par exemple un diélectrique avec un pourcentage de variation de ± 20 % sans tension continue appliquée sur la plage de températures allant de -55 °C à +125 °C, sera défini comme un diélectrique de sous-classe 2C1.

La plage de températures pour laquelle la caractéristique de température du diélectrique est définie est la même que la plage de températures de catégorie.

Tableau 3 – Caractéristique de température de condensateur

Code alphanumérique de sous-classe	Variation maximale de capacité dans la plage de températures de catégorie par rapport à la capacité à 20 °C mesurée avec et sans tension continue appliquée %		Plage de températures de catégorie et code numérique correspondant					
			-55/+150 °C	-55/+125 °C	-55/+85 °C	-40/+85 °C	-25/+85 °C	+10/+85 °C
	sans tension continue appliquée	avec tension continue appliquée (NOTE 1)	0	1	2	3	4	6
2B	±10	Exigences indiquées dans la spécification particulière			x	x	x	
2C	±20			x	x	x		
2D	+20/-30			x			x	
2E	+22/-56				x	x	x	x
2F	+30/-80				x	x	x	x
2R	±15		x	x	x		x	
Lorsque la température maximale de catégorie dépasse 125 °C, il convient que les limites de variation de capacité, avec et sans application de tension continue, figurent dans la spécification particulière.								
NOTE 1 La tension continue appliquée est soit la tension assignée soit la tension indiquée dans la spécification particulière.								
NOTE 2 "x" indique préférentiel.								

NOTE Se reporter à l'Annexe C pour la température de référence de 25 °C, à titre de recommandation informative.

6.2.6 Dimensions

Les règles proposées pour la spécification et le codage des dimensions sont présentées à l'Annexe A.

Les dimensions spécifiques doivent être données dans la spécification particulière.

7 Procédures d'assurance de la qualité

7.1 Etape initiale de fabrication

L'étape initiale de fabrication est le premier lancement en commun de l'assemblage de l'électrode et du diélectrique.

7.2 Modèles associables

Les condensateurs considérés comme étant associables sont des condensateurs produits à partir de matériaux et processus similaires, bien que leurs valeurs et les tailles des boîtiers puissent être différentes.

7.3 Enregistrements certifiés de lots livrés

Les informations exigées en Q.1.5 de l'IEC 60384-1:2016 doivent être mises à disposition, lorsqu'elles sont prescrites dans la spécification particulière et lorsqu'elles sont demandées par un acheteur. Après l'essai d'endurance les paramètres pour lesquels des informations de variables sont exigées, sont la variation de capacité, tan δ et la résistance d'isolement.

7.4 Homologation

7.4.1 Généralités

Les procédures d'essais d'homologation sont présentées à l'Article Q.2 de l'IEC 60384-1:2016.

Le programme à utiliser pour les essais d'homologation s'appuyant sur des essais lot par lot et des essais périodiques est présenté en 7.5. La procédure utilisant un programme avec un nombre d'échantillons fixe est présentée en 7.4.2 et 7.4.3.

7.4.2 Homologation fondée sur les procédures avec un nombre d'échantillons fixe

La procédure avec un nombre d'échantillons fixe est décrite en Q.2.4 de l'IEC 60384-1:2016. L'échantillon doit être représentatif de la gamme de condensateurs pour lesquels une homologation est demandée. Il peut ou non s'agir de la gamme complète couverte par la spécification particulière.

Pour chaque caractéristique de température, l'échantillon doit être constitué de spécimens de condensateurs de tailles maximale et minimale et ayant pour chacune de ces tailles la valeur maximale de capacité pour la tension assignée la plus élevée et pour la tension assignée minimale des plages de tensions pour lesquelles une homologation est demandée. En présence de plus de quatre tensions assignées, une tension intermédiaire doit également faire l'objet d'essais. Ainsi, pour homologuer une plage, il est exigé que les essais portent sur quatre ou six valeurs (combinaisons capacité/tension) pour chaque caractéristique de température. Lorsque la plage totale est composée de moins de quatre valeurs, le nombre de spécimens soumis à essai doit être celui exigé pour quatre valeurs.

Lorsqu'un niveau d'assurance EZ est utilisé, des spécimens de rechange sont admis selon les modalités suivantes:

Deux (pour six valeurs) ou trois (pour quatre valeurs) par valeur peuvent être utilisés pour remplacer les spécimens non conformes en raison d'incidents non attribuables au fabricant.

Les nombres donnés dans le Groupe 0 laissent présumer que tous les groupes sont applicables. Si ce n'est pas le cas, les nombres peuvent être réduits en conséquence.

Lorsque des groupes supplémentaires sont ajoutés au programme d'essais d'homologation, le nombre de spécimens exigé pour le Groupe 0 doit se voir ajouter le nombre exigé pour les groupes supplémentaires.

Le Tableau 4 donne le nombre d'échantillons à soumettre à essai dans chaque groupe ou sous-groupe et le nombre admissible d'éléments non-conformités pour les essais d'homologation.

7.4.3 Essais

Les séries complètes d'essais spécifiés dans le Tableau 4 et le Tableau 5 sont exigées pour l'homologation des condensateurs couverts par une spécification particulière. Les essais de chaque groupe doivent être effectués dans l'ordre indiqué.

La totalité de l'échantillon doit être soumise aux essais du Groupe 0, puis divisée pour les autres groupes.

Les spécimens non conformes trouvés pendant les essais du Groupe 0 ne doivent pas être utilisés pour les autres groupes.

"Un élément non conforme" est comptabilisé lorsqu'un condensateur ne satisfait pas à la totalité ou à une partie des essais d'un groupe.

L'homologation est accordée lorsque le nombre d'éléments non conformes est zéro.

Ensemble, le Tableau 4 et le Tableau 5 forment le programme d'essais avec un nombre d'échantillons fixe. Le Tableau 4 inclut les informations détaillées relatives à l'échantillonnage et aux éléments non conformes admissibles pour les différents essais ou groupes d'essais. Le

Tableau 5, avec les informations détaillées sur l'essai contenues dans l'Article 8, présente un résumé complet des conditions d'essais et des exigences de performances et indique quand un choix doit être fait dans la spécification particulière, par exemple pour la méthode d'essai ou les conditions d'un essai.

Les conditions d'essai et les exigences de performances pour le programme d'essais avec un nombre d'échantillons fixe doivent être identiques à celles prescrites dans la spécification particulière pour le contrôle de conformité de la qualité.

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Tableau 4 – Plan d'essais avec un nombre d'échantillons fixe pour homologation – Niveau d'assurance EZ

Groupe n°	Essai	Paragraphe de cette publication	Nombre de spécimens ^e	Nombre admissible d'éléments non conformes ^c
0	Examen visuel	8.5	132 + 24 ^f	0
	Dimensions	8.5		
	Capacité	8.6.1		
	Tangente de l'angle de perte	8.6.2		
	Résistance d'isolement	8.6.3		
	Tenue en tension	8.6.4		
	Spécimens de rechange		12	
1A	Robustesse des sorties ^g	8.16	12	0
	Résistance à la chaleur de brasage	8.10		
	Résistance au solvant des composants ^b	8.17		
1B	Impédance ^b	8.6.5	12	0
	Résistance série équivalente ^b	8.6.6		
	Brasabilité	8.11		
	Résistance au solvant du marquage ^b	8.18		
2	Essai de courbure du substrat ^d	8.9	12	0
3 ^a	Montage	8.4	84 + 24 ^f	0 ^c
	Examen visuel	8.5		
	Capacité	8.6.1		
	Tangente de l'angle de perte	8.6.2		
	Résistance d'isolement	8.6.3		
	Tenue en tension	8.6.4		
3.1	Essai de cisaillement ^h	8.8	24	0
	Variations rapides de température	8.12		
	Séquence climatique	8.13		
3.2	Chaleur humide, essai continu	8.14	24	0
3.3	Endurance	8.15	36	0
3.4	Chaleur humide, essai continu accéléré ^b	8.19	24 ^f	0
4	Caractéristique de température de condensateur	8.7	12	0

^a Les valeurs de ces mesures servent de mesures initiales pour les essais du Groupe 3.

^b Si exigé dans la spécification particulière.

^c Les condensateurs s'avérant être des éléments non conformes après le montage ne doivent pas être pris en compte pour le calcul des non-conformités admissibles pour les essais suivants. Ils doivent être remplacés par des condensateurs de rechange.

^d Ne s'applique pas aux condensateurs, qui, conformément à leur spécification particulière, doivent seulement être montés sur des substrats en alumine.

^e Combinaisons capacité/tension, voir 7.4.2.

^f Condensateurs supplémentaires si le Groupe 3.4 est soumis à essai.

^g S'applique aux condensateurs avec des sorties à lamelle.

^h Ne s'applique pas aux condensateurs avec des sorties à lamelle.

Numéro de paragraphe et essai (voir NOTE 1)	D ou ND	Conditions d'essai (voir NOTE 1)	Nombre de spécimens (<i>n</i>) et nombre d'éléments non conformes (<i>c</i>)	Exigences de performances (voir NOTE 1)
		Matériau de polissage: coton hydrophile Rétablissement: ...		
GRUPE 2 8.9 Essai de courbure du substrat 8.9.2 Mesure initiale 8.9.3 Contrôle final	D	Flexion: ... Nombre de courbures: ... Capacité Capacité (avec carte imprimée en position courbée) Examen visuel	Voir Tableau 4	Se reporter à la spécification particulière $ \Delta C/C \leq 10\%$ Aucun dommage visible
GRUPE 3 8.4 Montage	D	Matériau du substrat: ... ^b Examen visuel Capacité Tangente de l'angle de perte Résistance d'isolement Tenue en tension	Voir Tableau 4	Selon 8.5.3 Sans dépasser la tolérance spécifiée Selon 8.6.2.3 Selon 8.6.3.4 Pas de claquage ni de contournement
GRUPE 3.1 8.8 Essai de cisaillement 8.12.3 Mesure initiale 8.12 Variations rapides de température 8.12.6 Mesures finales 8.13 Séquence climatique 8.13.3 Mesure initiale 8.13.4 Chaleur sèche 8.13.5 Chaleur humide, cyclique, essai Db, premier cycle 8.13.6 Froid 8.13.7 Chaleur humide, cyclique, essai Db, cycles restants	D	Examen visuel Capacité Préconditionnement spécial selon 8.2 T_A = Température minimale de catégorie T_B = Température maximale de catégorie Cinq cycles Durée $t_1 = 30$ min Rétablissement: $24 \text{ h} \pm 2 \text{ h}$ Examen visuel Capacité Préconditionnement spécial selon 8.2 Capacité Température: température maximale de catégorie Durée: 16 h Température: température minimale de catégorie Durée: 2 h Examen visuel Rétablissement: $24 \text{ h} \pm 2 \text{ h}$	Voir Tableau 4	Aucun dommage visible Aucun dommage visible $\Delta C/C$: selon 8.12.6 Aucun dommage visible

Numéro de paragraphe et essai (voir NOTE 1)	D ou ND	Conditions d'essai (voir NOTE 1)	Nombre de spécimens (n) et nombre d'éléments non conformes (c)	Exigences de performances (voir NOTE 1)
8.13.8 Mesures finales		Examen visuel Capacité Tangente de l'angle de perte Résistance d'isolement		Aucun dommage visible Marquage visible $\Delta C/C$: selon 8.13.8 Selon 8.13.8 Selon 8.13.8
GRUPE 3.2 8.14 Chaleur humide, essai continu 8.14.3 Mesure initiale 8.14.6 Mesures finales	D	Préconditionnement spécial selon 8.2 Capacité Rétablissement: 24 h \pm 2 h Examen visuel Capacité Tangente de l'angle de perte Résistance d'isolement	Voir Tableau 4	Aucun dommage visible Marquage visible $\Delta C/C$: selon 8.14.6 Selon 8.14.6 Selon 8.14.6
GRUPE 3.3 8.15 Endurance 8.15.3 Mesure initiale 8.15.6 Mesures finales	D	Préconditionnement spécial selon 8.2 Durée: ... h Température: ...°C Tension: ... V Capacité Rétablissement: 24 h \pm 2 h Examen visuel Capacité Tangente de l'angle de perte Résistance d'isolement	Voir Tableau 4	Aucun dommage visible Marquage visible $\Delta C/C$: selon 8.15.6 Selon 8.15.6 Selon 8.15.6
GRUPE 3.4 8.19 Chaleur humide, essai continu accéléré (si cela est exigé) 8.19.2 Mesure initiale 8.19.5 Mesure finale	D	Durée: ... h Température: 85 °C \pm 2 °C Humidité relative: 85 % \pm 3 % Résistance d'isolement Rétablissement: 24 h \pm 2 h Résistance d'isolement	Voir Tableau 4	Selon 8.19.2 Selon 8.19.5
GRUPE 4 8.7 Caractéristique de température de condensateur	ND	Préconditionnement spécial selon 8.2	Voir Tableau 4	$\Delta C/C$: selon 8.7.3
NOTE 1 Les numéros des paragraphes des essais et des exigences de performances font référence à l'Article 8.				
NOTE 2 Dans ce tableau: D = destructif, ND = non destructif.				
a Cet essai peut être effectué sur les condensateurs montés sur un substrat.				
b Lorsque différents matériaux de substrat sont utilisés pour un sous-groupe, la spécification particulière doit indiquer le matériau de substrat utilisé dans chaque sous-groupe.				

7.5 Contrôle de conformité de la qualité

7.5.1 Formation des lots de contrôle

7.5.1.1 Contrôle des groupes A et B

Ces essais doivent être effectués lot par lot.

Un fabricant peut répartir la production actuelle en lots de contrôle soumis aux moyens de protection suivants.

- 1) Le lot de contrôle doit être constitué de condensateurs associables (voir 7.2).
- 2a) L'échantillon soumis à essai doit être représentatif des valeurs et des dimensions présentes dans le lot de contrôle:
 - en fonction de leur nombre;
 - avec un minimum de cinq valeurs quelconques.
- 2b) Si l'échantillon contient moins de cinq valeurs quelconques, le prélèvement des échantillons doit faire l'objet d'un accord entre le fabricant et l'organisme de certification.

7.5.1.2 Contrôle du Groupe C

Ces essais doivent être effectués périodiquement.

Les échantillons doivent être représentatifs de la production actuelle pour les durées spécifiées et doivent être divisés en tailles petites, moyennes et grandes. Pour couvrir la gamme d'homologation sur n'importe quelle durée, l'essai à une tension doit être effectué dans chaque groupe de tailles. Pour les durées suivantes, les essais doivent porter sur d'autres tailles et tensions assignées en production pour couvrir toute la gamme.

7.5.2 Programme d'essais

Le programme pour les essais lot par lot et pour les essais périodiques pour le contrôle de conformité de la qualité est présenté à l'Article 2 de la spécification particulière-cadre.

7.5.3 Livraison différée

Si, conformément aux procédures de Q.1.7 de l'IEC 60384-1:2016, un autre contrôle doit être effectué, la brasabilité et la capacité doivent être contrôlées comme cela est spécifié dans le contrôle des groupes A et B.

7.5.4 Niveaux d'assurance

Il convient de choisir les niveaux d'assurance donnés dans la spécification particulière-cadre depuis le Tableau 6 et le Tableau 7.