

INTERNATIONAL STANDARD

IEC 60384-21

First edition
2004-06

Fixed capacitors for use in electronic equipment –

Part 21:

Sectional specification:

**Fixed surface mount multilayer capacitors
of ceramic dielectric, Class 1**

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Fixed surface mount multilayer capacitors of ceramic dielectric, Class 1

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INTERNATIONAL ELECTROTECHNICAL COMMISSION

FIXED CAPACITORS FOR USE IN ELECTRONIC EQUIPMENT –

**Part 21: Sectional specification:
Fixed surface mount multilayer capacitors
of ceramic dielectric, Class 1**

FOREWORD

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International Standard IEC 60384-21 has been prepared by IEC technical committee 40: Capacitors and resistors for electronic equipment.

This standard and its related publications (CEI 60384-21-1, IEC 60384-22 and IEC 60384-22-1) cancel and replace IEC 60384-10 (1989) and its Amendments 1 (1993) and 2 (2000) as well as IEC 60384-10-1 (1989) and its Amendment 1 (1993).

The text of this standard is based on the following documents:

FDIS	Report on voting
40/1420/FDIS	40/1451/RVD

Full information on the voting for the approval of this standard can be found in the report on voting indicated in the above table.

This publication has been drafted in accordance with the ISO/IEC Directives, Part 2.

The committee has decided that the contents of this publication will remain unchanged until the maintenance result date indicated on the IEC web site under "<http://webstore.iec.ch>" in the data related to the specific publication. At this date, the publication will be

- reconfirmed;
- withdrawn;
- replaced by a revised edition, or
- amended.

A bilingual version of this standard may be issued at a later date.

The contents of the corrigendum of September 2004 have been included in this copy.

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Withdrawn

FIXED CAPACITORS FOR USE IN ELECTRONIC EQUIPMENT –

Part 21: Sectional specification: Fixed surface mount multilayer capacitors of ceramic dielectric, Class 1

1 General

1.1 Scope

This sectional specification is applicable to fixed unencapsulated surface mount multilayer capacitors of ceramic dielectric, Class 1, for use in electronic equipment. These capacitors have metallized connecting pads or soldering strips and are intended to be mounted on printed boards, or directly onto substrates for hybrid circuits.

Capacitors for electromagnetic interference suppression are not included, but are covered by IEC 60384-14.

1.2 Object

The object of this standard is to prescribe preferred ratings and characteristics and to select from IEC 60384-1:1999 the appropriate quality assessment procedures, tests and measuring methods and to give general performance requirements for this type of capacitor. Test severities and requirements prescribed in detail specifications referring to this sectional specification shall be of equal or higher performance level, lower performance levels are not permitted.

1.3 Normative references

The following referenced documents are indispensable for the application of this document. For dated references, only the edition cited applies. For undated references, the latest edition of the referenced document (including any amendments) applies.

IEC 60063:1963, *Preferred number series for resistors and capacitors*
Amendment 1 (1967)
Amendment 2 (1977)

IEC 60068-1:1988, *Environmental testing – Part 1: General and guidance*

IEC 60068-2-58:1999, *Environmental testing – Part 2-58: Tests – Test Td – Test methods for solderability, resistance to dissolution of metallization and to soldering heat of surface mounting devices (SMD)*

IEC 60384-1:1999, *Fixed capacitors for use in electronic equipment – Part 1: Generic specification*

IEC 60410:1973, *Sampling plans and procedures for inspection by attributes*

ISO 3 :1973, *Preferred numbers – Series of preferred numbers*

1.4 Information to be given in a detail specification

Detail specification shall be derived from the relevant blank detail specification.

Detail specifications shall not specify requirements inferior to those of the generic, sectional or blank detail specification. When more severe requirements are included, they shall be listed in 1.9 of the detail specification and indicated in the test schedules, for example by an asterisk.

NOTE The information given in 1.4.1 may, for convenience, be presented in tabular form.

The following information shall be given in each detail specification and the values quoted shall preferably be selected from those given in the appropriate Clause of this sectional specification.

1.4.1 Outline drawing and dimensions

There shall be an illustration of the capacitors as an aid to easy recognition and for comparison of the capacitors with others.

Dimensions and their associated tolerances, which affect interchangeability and mounting, shall be given in the detail specification. All dimensions shall preferably be stated in millimetres, however, when the original dimensions are given in inches, the converted metric dimensions in millimetres shall be added.

Normally the numerical values shall be given for the length, width and height of the body. When necessary, for example when a number of items (sizes and capacitance/voltage ranges) are covered by a detail specification, the dimensions and their associated tolerances shall be placed in a table below the drawing.

When the configuration is other than described above, the detail specification shall state such dimensional information as will adequately describe the capacitors.

1.4.2 Mounting

The detail specification shall give guidance on methods of mounting for normal use. Mounting for test and measurement purposes (when required) shall be in accordance with 4.3 of this sectional specification.

1.4.3 Rating and characteristics

The ratings and characteristics shall be in accordance with the relevant Clauses of this specification, together with the following:

1.4.3.1 Rated capacitance range

See 2.2.4.1

NOTE When products approved to the detail specification have different ranges, the following statement should be added: "The range of capacitance values available in each voltage range is given in IECQ 001005".

1.4.3.2 Particular characteristics

Additional characteristics may be listed, when they are considered necessary to specify adequately the component for design and application purposes.

1.4.3.3 Soldering

The detail specification shall prescribe the test methods, severities and requirements applicable for the solderability and the resistance to soldering heat tests.

1.4.4 Marking

The detail specification shall specify the content of the marking on the capacitor and on the package. Deviations from 1.6 of this sectional specification shall be specifically stated.

1.5 Terms and definitions

For the purposes of this sectional specification, the terms and definitions given in IEC 60384-1, as well as the following apply.

1.5.1

surface mount capacitor

a capacitor whose small dimensions and nature or shape of terminations make it suitable for surface mounting in hybrid circuits and on printed boards

1.5.2

fixed capacitors, ceramic dielectric, Class 1

a capacitor specially designed and suited for resonant circuit application where low losses and high stability of capacitance are essential or where a precisely defined temperature coefficient is required, for example for compensating temperature effects in the circuit.

The ceramic dielectric is defined by its rated temperature coefficient (α)

1.5.3

subclass

for a given rated temperature coefficient, the subclass is defined by the rated tolerance on the temperature coefficient (see Table 2)

NOTE The rated temperature coefficient value and its tolerance refer to the temperature interval of +20 °C to +85 °C, but because in practice TC curves are not strictly linear, it is necessary to define limiting capacitance deviation ($\Delta C/C$) for other temperatures (see Table 3)

1.5.4

category temperature range

range of ambient temperatures for which the capacitor has been designed to operate continuously; this is given by the lower and upper category temperature

1.5.5

rated temperature

maximum ambient temperature at which the rated voltage may be continuously applied

1.5.6

rated voltage (d.c.)

U_R

maximum direct voltage or peak value of pulse voltage which may be applied continuously to a capacitor at any temperature between the lower category temperature and the rated temperature

NOTE The sum of the d.c. voltage and the peak a.c. voltage or the peak to peak a.c. voltage, whichever is the greater, applied to the capacitor shall not exceed the rated voltage. The value of the peak a.c. voltage shall not exceed the value determined by the permissible reactive power.

1.5.7

category voltage

U_C

maximum voltage which may be applied continuously to a capacitor at its upper category temperature

1.6 Marking

See 2.4 of IEC 60384-1 with the following details:

1.6.1 The information given in the marking is normally selected from the following list; the relative importance of each item is indicated by its position in the list:

- a) rated capacitance;
- b) rated voltage (d.c. voltage may be indicated by the symbol $\overline{\quad}$ or $\underline{\quad}$);
- c) tolerance on rated capacitance;
- d) temperature coefficient and its tolerance as applicable (according to 2.2.5);
- e) year and month (or week) of manufacture;
- f) manufacturer's name or trade mark;
- g) climatic category;
- h) manufacturer's type designation;
- i) reference to the detail specification.

1.6.2 These capacitors are generally not marked on the body. If some marking can be applied, they shall be clearly marked with as many as possible of the above items as is considered useful. Any duplication of information in the marking on the capacitor should be avoided.

1.6.3 Any marking shall be legible and not easily smeared or removed by rubbing with the finger.

1.6.4 The package containing the capacitor(s) shall be clearly marked with all the information listed in 1.6.1.

1.6.5 Any additional marking shall be so applied that no confusion can arise.

2 Preferred rating and characteristics

2.1 Preferred characteristics

The values given in the detail specification shall preferably be selected from the following:

2.1.1 Preferred climatic categories

The capacitors covered by this sectional specification are classified into climatic categories according to the general rules given in IEC 60068-1.

The lower and upper category temperatures and the duration of the damp heat, steady state test shall be chosen from the following:

Lower category temperature: $-55\text{ }^{\circ}\text{C}$, $-40\text{ }^{\circ}\text{C}$, $-25\text{ }^{\circ}\text{C}$, $-10\text{ }^{\circ}\text{C}$ and $+10\text{ }^{\circ}\text{C}$

Upper category temperature: $+70\text{ }^{\circ}\text{C}$, $+85\text{ }^{\circ}\text{C}$, $+100\text{ }^{\circ}\text{C}$, $+125\text{ }^{\circ}\text{C}$ and $+150\text{ }^{\circ}\text{C}$

Duration of the damp heat, steady state test: 4, 10, 21 and 56 days.

The severities for the cold and dry heat tests are the lower and upper category temperatures respectively.

NOTE The resistance to humidity resulting from the above climatic category is for the capacitors in their unmounted state. The climatic performance of the capacitors after mounting is greatly influenced by the mounting substrate, the mounting method (see 4.3) and the final coating.

2.2 Preferred values of ratings

2.2.1 Rated temperature

For capacitors covered by this sectional specification, the rated temperature is equal to the upper category temperature, unless the upper category temperature exceeds 125 °C.

2.2.2 Rated voltage (U_R)

The preferred values of the rated voltage are the values of the R5 series of ISO 3. If other values are needed they shall be chosen from the R10 series.

2.2.3 Category voltage (U_C)

When the rated temperature is defined as the upper category temperature, the category voltage is equal to the rated voltage as defined in IEC 60384-1, 2.2.17. If the upper category temperature exceeds 125 °C, or the rated voltages exceed 500 V, the category voltage shall be given in the detail specification.

2.2.4 Preferred values of rated capacitance and associated tolerance values

2.2.4.1 Preferred values of rated capacitance

Rated capacitance values shall be taken from the series of IEC 60063; the E6, E12 and E24 series are preferred.

2.2.4.2 Preferred tolerance on rated capacitance

See Table 1.

Table 1 – Preferred tolerance on rated capacitance

Preferred series	Tolerance			
	$C_R \geq 10 \text{ pF}$	Letter code	$C_R < 10 \text{ pF}$	Letter code
E 6	$\pm 20 \%$	M	$\pm 2 \text{ pF}$	G
E 12	$\pm 10 \%$	K	$\pm 1 \text{ pF}$	F
E 24	$\pm 5 \%$	J	$\pm 0.5 \text{ pF}$	D
	$\pm 2 \%$	G	$\pm 0.25 \text{ pF}$	C
	$\pm 1 \%$	F	$\pm 0.1 \text{ pF}$	B

2.2.5 Temperature coefficient (α)

2.2.5.1 Table 2 shows the preferred rated temperature coefficients and the associated tolerances, expressed in parts per million per degree Kelvin ($10^{-6}/K$), and the corresponding subclasses and codes.

The detail specification shall specify for each temperature coefficient the minimum value of capacitance for which the given tolerance of temperature coefficient may be verified, considering the accuracy of the method of capacitance measurement specified.

For values of capacitance lower than this minimum value:

- a) The detail specification shall specify a multiplying factor for the tolerance on α , as well as the permissible changes of capacitance at the lower and upper category temperature.

- b) Special methods of measurement may be necessary and if required shall be tested in the detail specification.

Table 2 – Rated temperature coefficient and tolerance

Rated temperature coefficient ($10^{-6}/K$)	Tolerance on temperature coefficient ($10^{-6}/K$)	Subclass	Letter code for	
			α	Tolerance
+100	± 30	1B	A	G
<u>0</u>	± 30	1B	C	G
-33	± 30	1B	H	G
-75	± 30	1B	L	G
<u>-150</u>	± 30	1B	P	G
-220	± 30	1B	R	G
-330	± 60	1B	S	H
-470	± 60	1B	T	H
<u>-750</u>	± 120	1B	U	J
-1 000	± 250	1F	Q	K
-1 500	± 250	1F	V	K
$+140 \geq \alpha \geq -1\ 000$	a)	1C	SL	-

a) This temperature coefficient value is not subject to inspection, since no limits for relative capacitance variation are specified in Table 3.

NOTE 1 Preferred temperature coefficients values (α) are underlined.

NOTE 2 The rated temperature coefficients and their tolerances are defined using the capacitance change between the temperatures 20 °C and 85 °C.

NOTE 3 A capacitor with a temperature coefficient $0 \times 10^{-6}/K$ and a tolerance on temperature coefficient of $\pm 30 \times 10^{-6}/K$ is designated as a CG capacitor (subclass 1B).

2.2.5.2 Table 3 shows for each combination of temperature coefficient and tolerance, the permissible relative variation of capacitance expressed in parts per thousand at both the upper and lower category temperatures. Temperature coefficients and tolerances are expressed in parts per million per degree Kelvin ($10^{-6}/K$).

Table 3 – Combination of temperature coefficient and tolerance

		Permissible relative variation in capacitance in parts per 1 000 between 20 °C and given temperature							
		Lower category temperature				Upper category temperature			
α 10 ⁻⁶ /K	Tolerance 10 ⁻⁶ /K	-55 °C	-40 °C	-25 °C	-10 °C	+70 °C	+85 °C	+100 °C	+125 °C
+100	±30(G)	-9,75 / -3,71	-7,80 / -2,96	-5,85 / -2,22	-3,90 / -1,48	3,50 / 6,50	4,55 / 8,45	5,60 / 10,4	7,35 / 13,7
<u>0</u>	±30(G)	-2,25 / 5,45	-1,80 / 4,36	-1,35 / 3,27	-0,90 / 2,18	-1,50 / 1,50	-1,95 / 1,95	-2,40 / 2,40	-3,15 / 3,15
-33	±30(G)	0,225 / 8,47	0,180 / 6,77	0,135 / 5,08	0,090 / 3,39	-3,15 / -0,15	-4,10 / -0,195	-5,04 / -0,240	-6,62 / -0,32
-75	±30(G)	3,38 / 12,3	2,70 / 9,85	2,03 / 7,39	1,35 / 4,92	-5,25 / -2,25	-6,83 / -2,93	-8,40 / -3,60	-11,0 / -4,73
<u>-150</u>	±30(G)	9,00 / 19,2	7,20 / 15,3	5,40 / 11,5	3,60 / 7,67	-9,00 / -6,0	-11,7 / -7,80	-14,4 / -9,60	-18,9 / -12,6
-220	±30(G)	14,3 / 25,6	11,4 / 20,46	8,55 / 15,3	5,70 / 10,2	-12,5 / -9,50	-16,2 / -12,4	-20,0 / -15,2	-26,3 / -20,0
-330	±60(H)	20,3 / 38,4	16,2 / 30,7	12,2 / 23,0	8,10 / 15,4	-19,5 / 13,5	-25,4 / -17,6	-31,2 / -21,6	-41,0 / -28,4
-470	±60(H)	30,8 / 51,2	24,6 / 41,0	18,5 / 30,7	12,3 / 20,5	-26,5 / -20,5	-34,5 / -26,7	-42,4 / -32,8	-55,7 / -43,1
<u>-750</u>	±120(J)	47,3 / 82,3	37,8 / 65,8	28,4 / 49,4	18,9 / 32,9	-43,5 / -31,5	-56,6 / -41,0	-69,6 / -50,4	-91,4 / -66,2
-1000	±250(K)	56,3 / 117	45,0 / 93,7	33,8 / 70,2	22,5 / 46,8	-62,5 / -37,5	-81,3 / -48,8	-100 / -60,0	-131 / -78,8
-1500	±250(K)	93,8 / 163	75,0 / 130	56,3 / 97,7	37,5 / 65,1	-87,5 / -62,5	-114 / -81,3	-140 / -100	-184 / -131

NOTE Preferred temperature coefficient values (α) are underlined.

When the upper category temperature is above 125 °C, the limits shall be given in the detail specification.

2.2.6 Dimensions

Suggested rules for the specification and coding of dimensions are given in Annex A.

Specific dimensions shall be given in the detail specification.

3 Quality assessment procedures

3.1 Primary stage of manufacture

The primary stage of manufacture is the first common firing of the dielectric-electrode assembly.

3.2 Structurally similar components

Capacitors considered as being structurally similar are capacitors produced with similar processes and materials, through they may be of different case sizes and values.

3.3 Certified records of released lots

The information required in IEC 60384-1, 3.9 shall be made available when prescribed in the detail specification and when requested by a purchaser. After the endurance test, the parameters for which variables information is required are the capacitance change, $\tan \delta$ and the insulation resistance.

3.4 Qualification approval

The procedures for qualification approval testing are given in IEC 60384-1, 3.5.

The schedule to be used for qualification approval testing on the basis of lot-by-lot and periodic tests is given in 3.5 of this specification. The procedure using a fixed sample size schedule is given in 3.4.1 and 3.4.2.

3.4.1 Qualification approval on the basis of the fixed sample size procedures

The fixed sample size procedure is described in item b) of 3.5.3 of IEC 60384-1. The sample shall be representative of the range of capacitors for which approval is sought. This may or may not be the complete range covered by the detail specification.

For each temperature coefficient the sample shall consist of specimens of capacitors of maximum and minimum size and for each of these sizes, the maximum capacitance value for the highest rated voltage and minimum rated voltage of the voltage ranges for which approval is sought. When there are more than four rated voltages, an intermediate voltage shall also be tested. Thus for the approval of a range, testing is required of either four or six values (capacitance/voltage combinations) for each temperature coefficient. Where the total range consists of less than four values, the number of specimens to be tested shall be that required for four values. When approval is sought for more than one temperature coefficient, see 3.4.2.

In case assessment level EZ is used, spare specimens are permitted as follows:

Two (for six values) or three (for four values) per value which may be used as replacements for specimens, which are non-conforming because of incidents not attributable to the manufacturer.

The numbers given in Group 0 assume that all groups are applicable. If this is not so, the numbers may be reduced accordingly.

When additional groups are introduced into the qualification approval test schedule, the number of specimens required for Group 0 shall be increased by the same number as that required for the additional groups.

Table 4 gives the number of samples to be tested in each group or subgroup together with the number of permissible non-conformances for the qualification approval test.

3.4.2 Tests

The complete series of tests specified in Table 4 and 5 are required for the approval of capacitors covered by one detail specification. The tests of each group shall be carried out in the order given.

The whole sample shall be subjected to the tests of Group 0 and then divided for the other groups.

Non-conforming specimens found during the tests of Group 0 shall not be used for the other groups.

“One non-conforming item” is counted when a capacitor has not satisfied the whole or a part of the tests of a group.

When approval is sought for more than one temperature coefficient at the same time, the test schedule and sample size required for the smallest temperature coefficient are those of Group 1, 2 and 3. For each additional temperature coefficient, the testing is limited to the tests and sample sizes as specified for Subgroup 3.3 and Group 4.

The approval is decided on an individual temperature coefficient basis in accordance with the permissible number of non-conforming items indicated in Table 4. In order to calculate the total actual non-conforming items for temperature coefficients other than the smallest, the non-conforming items in Group 1, 2 and 3 for the smallest temperature coefficient are added to the non-conforming items in Subgroup 3.3 and Group 4 for that particular temperature coefficient.

The approval is granted when the number of non-conforming items do not exceed the specified number of permissible non-conforming items for each group or subgroup and the total number of permissible non-conformances.

NOTE Tables 4 and 5 together form the fixed sample size test schedule. Table 4 includes the details for the sampling and permissible non-conforming items for the different tests or groups of tests. Table 5 together with the details of the test contained in Clause 4 gives a complete summary of test conditions and performance requirements and indicates where, for example for the test method or conditions of test, a choice has to be made in detail specification.

The conditions of test and performance requirements for the fixed sample size test schedule shall be identical to those prescribed in the detail specification for quality conformance inspection.

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Table 4 – Fixed sample size test plan for qualification approval – Assessment level EZ

Group No.	Test	Subclause of this publication	Number of specimens <i>n</i> ^e	Permissible number of non conforming items <i>c</i>
0	Visual examination	4.4	132 + 24 ^f	0
	Dimensions	4.4		
	Capacitance	4.5.1		
	Tangent of loss angle	4.5.2		
	Insulation resistance	4.5.3		
	Voltage proof	4.5.4		
	Spare specimens		12	
1A	Robustness of termination ^g	4.15	12	0
	Resistance to soldering heat	4.9		
	Component solvent resistance ^b	4.16		
1B	Solderability	4.10	12	0
	Solvent resistance of marking ^b	4.17		
2	Substrate bending test ^d	4.8	12	0
3 ^a	Mounting	4.3	84 + 24 ^f	0 ^c
	Visual examination	4.4		
	Capacitance	4.5.1		
	Tangent of loss angle	4.5.2		
	Insulation resistance	4.5.3		
	Voltage proof	4.5.4		
3.1	Shear test ^h	4.7	24	0
	Rapid change of temperature	4.11		
	Climatic sequence	4.12		
3.2	Damp heat, steady state	4.13	24	0
3.3	Endurance	4.14	36	0
3.4	Accelerated damp heat, steady state ^b	4.18	24 ^f	0
4	Temperature coefficient and temperature cycle drift	4.6	12	0

^a The values of these measurements serve as initial measurements for the tests of Group 3.

^b If required in the detail specification.

^c The capacitors found non-conforming items after mounting shall not be taken into account when calculating the permissible non-conforming for the following tests. They shall be replaced by spare capacitors.

^d Not applicable to capacitors, which according to their detail specification shall only be mounted on alumina substrates.

^e Capacitance/voltage combinations, see 3.4.1.

^f Additional capacitors, if Group 3.4 is tested.

^g Applicable to capacitors with strip terminations.

^h Not applicable to capacitors with strip terminations.

Table 5 – Tests schedule for qualification approval

Subclause number and test (see NOTE 1)	D or ND	Conditions of test (see NOTE 1)	Number of specimens (<i>n</i>) and number of non-conforming items (<i>c</i>)	Performance requirements (see NOTE 1)
GROUP 0 4.4 Visual examination 4.4 Dimension (detail) 4.5.1 Capacitance 4.5.2 Tangent of loss angle (tanδ) 4.5.3 Insulation resistance 4.5.4 Voltage proof	ND	Frequency: ... Hz Measuring voltage: ... V r.m.s. Frequency and measuring voltage same as in 4.5.1 See detail specification for the method See detail specification for the method	See Table 4	As in 4.4.2 Legible marking and as specified in the detail specification See the detail specification Within specified tolerance As in 4.5.2 As in 4.5.3.3 No breakdown or flashover
GROUP 1A 4.15 Robustness of terminations (if applicable) 4.9.1 Initial measurement 4.9 Resistance to soldering heat 4.9.4 Final measurement 4.16 Component solvent resistance (if applicable)	D	Test Ua1, Force: 2,5 N Test Ub, Method 1, Force: 2,5 N Number of bends: 1 Visual examination Capacitance See detail specification for the method Recovery: 6 h to 24 h Visual examination Capacitance Solvent: ... Solvent temperature: ... Method 2 Recovery: ...	See Table 4	No visible damage As in 4.9.4 As in 4.9.4 See detail specification
GROUP 1B 4.10 Solderability 4.10.3 Final measurements 4.17 Solvent resistance of the marking ^a (if applicable)	D	See detail specification for the method Visual examination Solvent: ... Solvent temperature: ... Method 1 Rubbing material: cotton wool Recovery: ...	See Table 4	As in 4.10.3 Legible marking

Subclause number and test (see NOTE 1)	D or ND	Conditions of test (see NOTE 1)	Number of specimens (<i>n</i>) and number of non- conforming items (<i>c</i>)	Performance requirements (see NOTE 1)
GROUP 2 4.8 Substrate bending test 4.8.1 Initial measurement 4.8.2 Final inspection	D	Deflection: ... Number of bends: ... Capacitance Capacitance (with printed board in bent position) Visual examination	See Table 4	See detail specification $ \Delta C/C < 5\%$ No visible damage
GROUP 3 4.3 Mounting	D	Substrate material: ... ^b Visual examination Capacitance Tangent of loss angle Insulation resistance Voltage proof	See Table 4	As in 4.4.2 Within specified tolerance As in 4.5.2 As in 4.5.3.3 No breakdown or flashover
GROUP 3.1 4.7 Shear test 4.11.1 Initial measurement 4.11 Rapid change of temperature 4.11.4 Final measurements 4.12 Climatic sequence 4.12.1 Initial Measurement 4.12.2 Dry heat 4.12.3 Damp heat, cyclic, test Db, first cycle 4.12.4 Cold	D	Visual examination Capacitance T_A = Lower category temperature T_B = Upper category temperature Five cycles Duration t_1 = 30 min Recovery: 6 h to 24 h Visual examination Capacitance Capacitance Temperature: upper category temperature Duration: 16 h Temperature: lower category temperature Duration: 2 h Visual examination	See Table 4	No visible damage No visible damage $\Delta C/C$ as in 4.11.4 No visible damage

Subclause number and test (see NOTE 1)	D or ND	Conditions of test (see NOTE 1)	Number of specimens (<i>n</i>) and number of non- conforming items (<i>c</i>)	Performance requirements (see NOTE 1)
4.12.5 Damp heat, cyclic, test Db, remaining cycles 4.12.6 Final measurements		Recovery: 6 h to 24 h Visual examination Capacitance Tangent of loss angle Insulation resistance		No visible damage Legible marking $\Delta C/C$: As in 4.12.6 As in 4.12.6 As in 4.12.6
GROUP 3.2 4.13 Damp heat, steady state 4.13.1 Initial measurement 4.13.4 Final measurements	D	Capacitance Recovery: 6 h to 24 h Visual examination Capacitance Tangent of loss angle Insulation resistance	See Table 4	No visible damage Legible marking $\Delta C/C$: As in 4.13.4 As in 4.13.4 As in 4.13.4
GROUP 3.3 4.14 Endurance 4.14.1 Initial measurement 4.14.4 Final measurements	D	Duration: ... h Temperature: ... °C Voltage: ... V Capacitance Recovery: 6 h to 24 h Visual examination Capacitance Tangent of loss angle Insulation resistance	See Table 4	No visible damage Legible marking $\Delta C/C$: As in 4.14.4 As in 4.14.4 As in 4.14.4
GROUP 3.4 4.18 Accelerated damp heat, steady state (if required) 4.18.1 Initial measurement 4.18.4 Final measurements	D	Duration: ... h Temperature: (85 ± 2) °C Humidity: (85 ± 3) % Insulation resistance Recovery: 6 h to 24 h Insulation resistance	See Table 4	As in 4.18.1 As in 4.18.4
Group 4 4.6 Temperature coefficient and cyclic drift	ND	Preliminary drying: 16 h to 24 h	See Table 4	$\Delta C/C$: As in 4.6.3

NOTE 1 Subclause numbers of test and performance requirements refer to Clause 4.

NOTE 2 In this table: D = destructive, ND = non- destructive.

^a This test may be carried out on capacitors mounted on a substrate.

^b When different substrate materials are used for the individual subgroup, the detail specification shall indicate which substrate material is used in each subgroup.

3.5 Quality conformance inspection

3.5.1 Formation of inspection lots

3.5.1.1 Group A and B inspection

These tests shall be carried out on a lot-by-lot basis.

A manufacturer may aggregate the current production into inspection lots subject to the following safeguards:

- 1) The inspection lot shall consist of structurally similar capacitors (see 3.2).
- 2a) The sample tested shall be representative of the values and the dimensions contained in the inspection lot:
 - in relation to their number;
 - with a minimum of five of any one value.
- 2b) If there are less than five of any one value in the sample the basis for the drawing of samples shall be agreed between the manufacturer and the National Supervising Inspectorate.

3.5.1.2 Group C inspection

These tests shall be carried out on a periodic basis.

Samples shall be representative of the current production of the specified periods and shall be divided into small, medium and large sizes. In order to cover the range of approvals in any period, one voltage shall be tested from each group of sizes. In subsequent periods, other sizes and/or voltage ratings in production shall be tested with the aim of covering the whole range.

3.5.2 The schedule

The schedule for the lot-by-lot and periodic tests for Quality Conformance Inspection is given in Clause 2, Table 4 of the Blank Detail Specification, for example in IEC 60384-21-1.

3.5.3 Delayed delivery

When, according to the procedures of 3.10 of IEC 60384-1, re-inspection has to be made, solderability and capacitance shall be checked as specified in Group A and B inspection.

3.5.4 Assessment levels

The assessment level(s) given in the Blank Detail Specification shall preferably be selected from Tables 6a and 6b.

Table 6a – Lot-by-lot inspection

Inspection Subgroup ^d	EZ		
	IL ^a	<i>n</i> ^a	<i>c</i> ^a
A0	100% ^b		
A1	S-4	<i>c</i>	0
A2	S-3	<i>c</i>	0
B1	S-3	<i>c</i>	0
B2	S-2	<i>c</i>	0

^a IL = inspection level
 n = sample size
c = permissible number of non-conforming items
^b 100 % testing shall be followed by re-inspection by sampling in order to monitor outgoing quality level by non-conforming items per million (ppm). The sampling level shall be established by the manufacturer. For the calculation of ppm values, any parametric failure shall be counted as a non-conforming item. In case one or more non-conforming items occur in a sample, this lot shall be rejected.
^c Number to be tested: Sample size as directly allotted to the code letter for IL in Table IIA of IEC 60410.
^d The content of the Inspection subgroup is described in Clause 2 of the relevant blank detail specification.

Table 6b – Periodic tests

Inspection Subgroup ^b	EZ		
	<i>p</i> ^a	<i>n</i> ^a	<i>c</i> ^a
C1	3	12	0
C2	3	12	0
C3.1	6	27	0
C3.2	6	15	0
C3.3	3	15	0
C3.4 ^c	6	15	0
C4	6	9	0

^a *p* = periodicity in months
 n = sample size
c = permissible number of non-conforming items
^b The content of the inspection subgroup is described in Clause 2 of the relevant blank detail specification.
^c If required.

4 Test and measurement procedures

This Clause supplements the information given in Clause 4 of IEC 60384-1.

4.1 Preliminary drying

See 4.3 of IEC 60384-1.

4.2 Measuring conditions

See 4.2.1 of IEC 60384-1.

4.3 Mounting

See 4.33 of IEC 60384-1.

4.4 Visual examination and check of dimensions

See 4.4 of IEC 60384-1, with the following details:

4.4.1 Visual examination

A visual examination shall be carried out with suitable equipment with approximately 10× magnification and lighting appropriate to the specimen under test and the quality level required.

NOTE The operator should have available facilities for incident or transmitted illumination as well as an appropriate measuring facility.

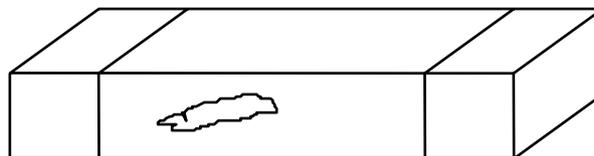
4.4.2 Requirements

Quantitative values for the requirements below may be given in the detail or in the manufacturer's specification.

The capacitor shall:

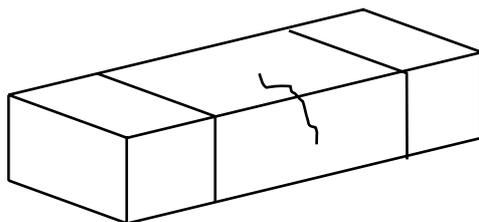
4.4.2.1 For the ceramic:

- 1) Be free of cracks or fissures, except small damages on the surface, which do not deteriorate the performance of the capacitor. (Examples; see Figures 1 and 2)



IEC 759/04

Figure 1 – Fault: crack or fissure

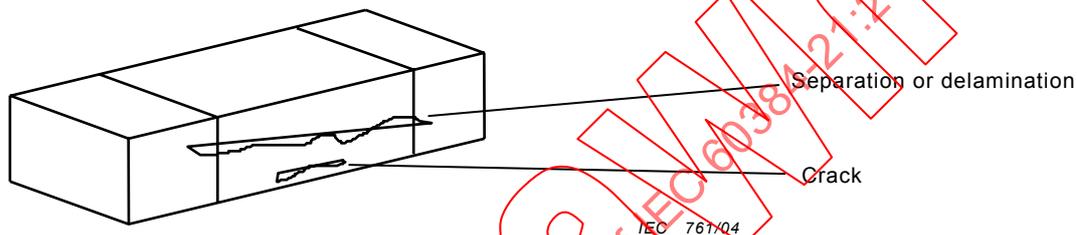


IEC 760/04

NOTE crack or fissure on one side or extending from one face to another over a corner.

Figure 2 – Fault: crack or fissure

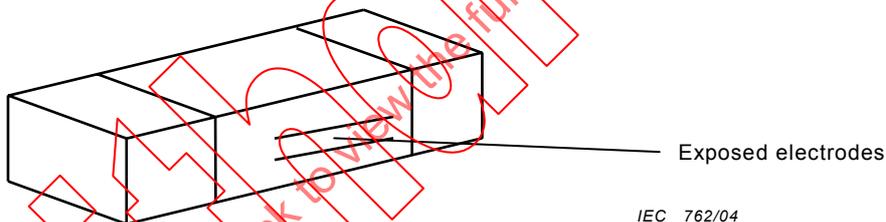
- 2) Not exhibit visible separation or delamination between the layers of the capacitor (see Figure 3)



IEC 761/04

Figure 3 – Separation or delamination

- 3) Not exhibit exposed electrodes between the two terminations (see Figure 4).



IEC 762/04

Figure 4 – Exposed electrodes

- 4) The ceramic body shall be free of any conducting smears (metallization, tinning) on a central zone between two adjacent terminations which is equal to the minimum distance between those (Annex A, dimension L_4).

4.4.2.2 For the metallization

- 1) Not exhibit any visible detachment of the metallized terminations and not exhibit any exposed electrodes (see Figure 4).
- 2) The principal faces (see Figure 5) are those noted A, B and C.

In the case of capacitors of square section, the faces D and E are also considered principal.

The maximum area of gaps in metallization on each principal face shall not be greater than 15 % of the area of that face; these gaps shall not be concentrated in the same area. The gaps in metallization shall not affect the two principal edges of each extremity of the block (or four edges for square section capacitors). Dissolution of the end face plating (leaching) shall not exceed 25 % of the length of the edge concerned.

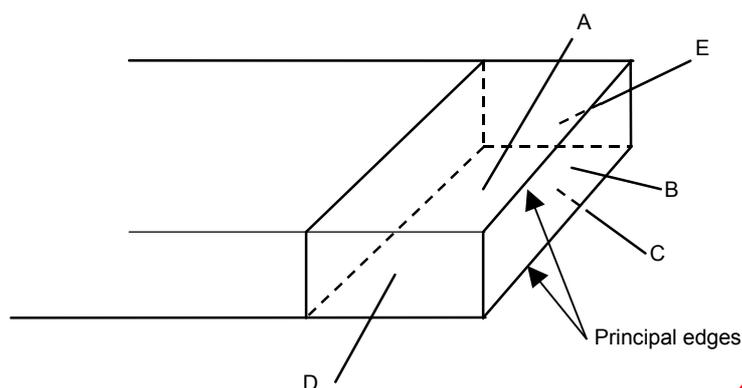


Figure 5 – Principal faces

IEC 757/04

4.5 Electrical tests

4.5.1 Capacitance

See 4.7 of IEC 60384-1, with the following details:

4.5.1.1 Measuring conditions

Unless otherwise specified in the detail specification:

Measuring voltage: ≤ 5 V r.m.s.,

Frequency: $C_R \leq 1\,000$ pF 1 MHz or 100 kHz (reference frequency 1 MHz),

$C_R > 1\,000$ pF 1 kHz or 100 kHz (reference frequency 1 kHz).

4.5.1.2 Requirements

The capacitance value, as measured in the unmounted state, shall correspond with the rated value taking into account the specified tolerance.

The capacitance as measured in the mounted state according to Group 3 is for reference purposes only in further tests.

4.5.2 Tangent of loss angle ($\tan \delta$)

See 4.8 of IEC 60384-1, with the following details:

4.5.2.1 Measuring conditions

The measuring conditions are the same as 4.5.1. The inaccuracy of the measuring equipment shall not exceed 3×10^{-4} .

4.5.2.2 Requirements

The tangent of loss angle as measured in the unmounted state shall not exceed the limit given in Table 7.

Table 7 – Tangent of loss angle limits

Rated capacitance pF	Tangent of loss angle (tan δ) ×10 ⁻⁴		
	+100 ≥ α > -750 and SL (1C)	-750 ≥ α > -1 500	α = -1 500
$C_R \geq 50$	15	20	30
$5 \leq C_R < 50$	$1,5 \left(\frac{150}{C_R} + 7 \right)$	$2 \left(\frac{150}{C_R} + 7 \right)$	$3 \left(\frac{150}{C_R} + 7 \right)$
$C_R < 5$	When the measurement is required, the detail specification shall specify the limit.		

The tangent of loss angle as measured in the mounted state according to Group 3 is for reference purpose only in further tests.

4.5.3 Insulation resistance (Ri)

See 4.5 of IEC 60384-1, with the following details:

4.5.3.1

Prior to the test, capacitors shall be carefully cleaned to remove any contamination.

Care shall be taken to maintain cleanliness in the test chambers and during post test measurements. Before the measurement the capacitors shall be fully discharged. The insulation resistance shall be measured between the terminations.

4.5.3.2 Measuring conditions

See 4.5.2 of IEC 60384-1, with the following details:

The measuring voltage may be of any value not greater than U_R , the referee voltage being U_R , for capacitors with a rated voltage below or equal to 1 kV. For $U_R > 1$ kV the referee voltage shall be 1 kV.

The insulation resistance (R_i) shall be measured after the voltage has been applied for (60 ± 5) s.

For lot-by-lot testing (Group A) the test may be terminated in a shorter time, if the required value of insulation resistance is reached.

The product of the internal resistance of the voltage source and the rated capacitance of the capacitor shall not exceed 1 s unless otherwise prescribed in the detail specification.

The charge current shall not exceed 0,05 A. For capacitors with rated voltages of 1 kV and above, a lower limit may be given in the detail specification.

4.5.3.3 Requirements

$C_R \leq 10$ nF	$R_i \geq 10\ 000$ MΩ
$C_R > 10$ nF	$R_i \times C_R \geq 100$ s

4.5.4 Voltage proof

See 4.6 of IEC 60384-1, with the following details:

4.5.4.1 Test conditions

The product of R_1 and the rated capacitance C_X shall be smaller than or equal to 1 s.

The charge current shall not exceed 0.05 A.

For capacitors with rated voltages of 1kV and above, a lower limit may be given in the detail specification. To protect the capacitors against flashover, the test may be performed in a suitable insulating medium.

4.5.4.2

The test voltages according to Table 8 shall be applied between the measuring points of Table 3 in 4.5.3 of IEC 60384-1, for a period of 1 min for qualification approval testing and for a period of 1 s for the lot-by-lot quality conformance testing.

Table 8 – Test voltages

Rated voltage (V)	Test voltage (V)
$U_R \leq 100$	$2,5 U_R$
$100 < U_R \leq 200$	$1,5 U_R + 100$
$200 < U_R \leq 500$	$1,3 U_R + 100$
$U_R > 500$	$1,3 U_R$

4.5.4.3 Requirement

There shall be no breakdown or flashover during the test.

4.6 Temperature coefficient (α) and temperature cycle drift

See 4.24.3.2 of IEC 60384-1, with the following details:

4.6.1 Preliminary drying

The capacitors shall be dried according to 4.1 for 16 h to 24 h.

4.6.2 Measuring conditions

See 4.24.1.2 and 4.24.1.3 of IEC 60384-1, with the following details:

The capacitors shall be measured in the unmounted state.

4.6.3 Requirements

The capacitance deviation at upper and lower category temperature (and at such other temperatures as may be specified in the detail specification) shall not exceed the limits given in Table 3.

The temperature cyclic drift shall not exceed the limits given in Table 9.

Table 9 – Temperature cyclic drift limits

$+100 \geq \alpha > -150$	$-150 \geq \alpha > -1\ 500$ and SL(1C)	$\alpha = -1\ 500$
0,3 % (*) or 0,05 pF	1 % (*) or 0,05 pF	2 % (*) or 0,05 pF
(*) whichever is the greater.		

4.7 Shear test

See 4.34 of IEC 60384-1.

4.8 Substrate bending test

See 4.35 of IEC 60384-1. The deflection *D* and the number of bends shall be specified in the detail specification.

4.8.1 Initial measurement

Capacitance: for reference see Table 5, Group 2.

4.8.2 Final inspection

Capacitance (with board in bent position): for reference see Table 5, group 2.

Visual examination: for references see Table 5, Group 2.

4.9 Resistance to soldering heat

See IEC 60068-2-58 with the following details:

4.9.1 Initial measurement

The capacitance shall be measured according to 4.5.1.

4.9.2 Test conditions

4.9.2.1 Solder bath method (applicable to 1608 M, 2012 M and 3216 M)

NOTE See Table A.1 for explanation of the size code.

See Clauses 5 and 6 of IEC 60068-2-58, with the following details, if not otherwise specified in the detail specification:

The specimen shall be preheated to a temperature of 110 °C to 140 °C and maintained for 30 s to 60 s.

Temperature: 260 °C ± 5 °C

Duration of immersion: 10 s ± 1 s

Depth of immersion: 10 mm

Number of immersions: 1

4.9.2.2 Infrared and forced gas convection soldering system

See Clauses 7 and 8 of IEC 60068-2-58, with the following details:

- a) the solder paste shall be applied to the test substrate;
- b) the thickness of solder deposit shall be specified in the detail specification;
- c) the terminations of the specimen shall be placed on the solder paste;
- d) unless otherwise specified in the detail specification, the specimen and test substrate shall be preheated to a temperature of $(150 \pm 10) ^\circ\text{C}$ and maintained for 60 s to 120 s in infrared and forced gas convection soldering systems;
- e) the temperature of the reflow system shall be quickly raised until the specimen has reached $(235 \pm 5) ^\circ\text{C}$ and maintained at this temperature for $(10 \pm 1) \text{ s}$. Number of tests: 2;
- f) the temperature profile shall be specified in the detail specification.

4.9.3 Recovery

The capacitors shall recover for 6 h to 24 h.

The flux residues shall be removed with a suitable solvent.

4.9.4 Final inspection, measurements and requirements

After recovery, the capacitors shall be visually examined and measured and shall meet the following requirements:

Under normal lighting and approximately $10\times$ magnification, there shall be no signs of damage such as cracks.

Dissolution of the end face plating (leaching) shall not exceed 25 % of the length of the edge concerned. The detail specification may prescribe further details.

The capacitance shall be measured according to 4.5.1 and the change shall not exceed the values in Table 10.

Table 10 – Maximum capacitance change

α rated in $10^{-6}/\text{K}$	Requirements(*)
$+100 \geq \alpha \geq -750$	0,5 % or 0,5 pF
$-750 > \alpha \geq -1\ 500$ and SL(1C)	1 % or 1 pF
(*) whichever is the greater	

4.10 Solderability

See IEC 60068-2-58 with the following details:

4.10.1 Test conditions

4.10.1.1 Solder bath method (applicable to 1608 M, 2012 M and 3216 M)

NOTE See Table A.1 for explanation of the size code.

See Clauses 5 and 6 of IEC 60068-2-58, with the following details, if not otherwise specified in the detail specification:

The specimen shall be preheated to a temperature of (80 to 140) °C and maintained for 30 s to 60 s.

Temperature:	(235 ± 5) °C
Duration of immersion:	(2 ± 0,2) s
Depth of immersion:	10 mm
Number of immersions:	1

4.10.1.2 Infrared and forced gas convection soldering system

See Clauses 7 and 8 of IEC 60068-2-58, with the following details:

- the solder paste shall be applied to the test substrate;
- the thickness of solder deposit shall be specified in the detail specification;
- the terminations of the specimen shall be placed on solder paste;
- unless otherwise specified in the detail specification, the specimen and test substrate shall be preheated to a temperature of (150 ± 10) °C and maintained for 60 s to 120 s in infrared and forced gas convection soldering system;
- the temperature of reflow system shall be quickly raised until the specimen has reached (215 ± 3) °C and maintained at this temperature for (10 ± 1) s;
- temperature profile shall be specified in the detail specification.

4.10.2 Recovery

The flux residues shall be removed with a suitable solvent.

4.10.3 Final inspection, measurements and requirements

The capacitors shall be visually examined under normal lighting and approximately 10× magnification. There shall be no signs of damage.

Both end faces and the contact areas shall be covered with a smooth and bright solder coating with no more than a small amount of scattered imperfections such as pinholes or unwetted or de-wetted areas. These imperfections shall not be concentrated in one area.

The detail specification may prescribe further requirements.

4.11 Rapid change of temperature

(Applicable only to capacitors for which the category temperature is greater 110 °C).

See 4.16 of IEC 60384-1, with the following details:

The capacitors shall be mounted according to 4.3.

4.11.1 Initial measurement

The capacitance shall be measured according to 4.5.1.

4.11.2 Number of cycles: 5

Duration of exposure at the temperature limits: 30 min.

4.11.3 Recovery

The capacitors shall recover for 6 h to 24 h.

4.11.4 Final inspection, measurements and requirements

The capacitors shall be visually examined. There shall be no visible damage.

The capacitance shall be measured according to 4.5.1 and the change shall not exceed the values in Table 11.

Table 11 – Maximum capacitance change

α rated in $10^{-6}/K$	Requirements(*)
$+100 \geq \alpha \geq -750$	1 % or 1 pF
$-750 > \alpha \geq -1\ 500$ and SL(1C)	2 % or 1 pF
(*) whichever is the greater.	

4.12 Climatic sequence

See 4.21 of IEC 60384-1, with the following details:

4.12.1 Initial measurement

The capacitance shall be measured according to 4.5.1.

4.12.2 Dry heat

See 4.21.2 of IEC 60384-1.

4.12.3 Damp heat, cyclic, Test Db, first cycle

See 4.21.3 of IEC 60384-1.

4.12.4 Cold

See 4.21.4 of IEC 60384-1, with the following details:

4.12.4.1 Final inspection and requirements

The capacitors shall be visually examined. There shall be no visible damage.

4.12.5 Damp heat, cyclic, Test Db, remaining cycles

See 4.21.6 of IEC 60384-1, with the following details:

4.12.5.1 Conditions of test

No voltage applied.

Table 12 – Number of damp heat cycles

Category	No. of cycles of 24 h
- / - / 56	5
- / - / 21	1
- / - / 10	1
- / - / 04	0

4.12.5.2 Recovery

The capacitors shall recover for 6 h to 24 h.

4.12.6 Final inspection, measurements and requirements

The capacitors shall be visually examined. There shall be no visible damage.

The capacitors shall be measured and shall meet the requirements in Table 13.

Table 13 – Final inspection measurements and requirements

Measurement	Measurement and conditions	α rated and (Subclass)	Requirements
Capacitance	Subclause 4.5.1	$+100 \geq \alpha \geq -750$ (1B)	Capacitance change $\leq 2\%$ or 1 pF(*)
		$-750 > \alpha \geq -1\ 500$ (1F) SL (1C)	Capacitance change $\leq 3\%$ or 1 pF(*)
Tangent of loss angle	Subclause 4.5.2	All α 's and Subclasses	$\leq 2 \times$ value in the table of Subclause 4.5.2
Insulation resistance	Subclause 4.5.3	All α 's and Subclasses	$R_i \geq 2\ 500\ M\Omega$ or $R_i \times C_R \geq 25\ s$ (**)
NOTE See 2.2.5 for an explanation of the subclass codes.			
(*) whichever is the greater.			
(**) whichever is less.			

4.13 Damp heat, steady state

See 4.22 of IEC 60384-1, with the following details:

The capacitors shall be mounted according to 4.3.

4.13.1 Initial measurement

The capacitance shall be measured according to 4.5.1.

4.13.2 Conditions of test

No voltage shall be applied, unless otherwise specified in the detail specification.

When the application of voltage is prescribed, U_R shall be applied to one half of the lot and no voltage shall be applied to other half of the lot.